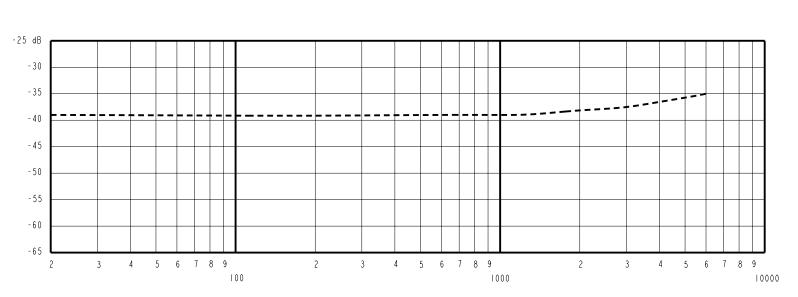
BU-23173-000 SHT I.I NOTE:  $7.87 \pm 0.05$  $[.310 \pm .002]$ ➤ LOCATED FROM TWO SURFACES FOR CUSTOMER CONVENIENCE. ONLY APPLICABLE FROM ONE SURFACE, NOT TO BE USED TOGETHER. HORIZONTAL LOCATION FOR TERMINAL CENTERED  $TO \pm 0, 17 [.007].$ → 5,54±0,05  $[.218\pm .002]$  $1,61\pm0,10$  $[.064 \pm .004]$  $|,6|\pm 0,10$  $[.064\pm.004]$  $1,35 \pm 0,07$  $4.06 \pm 0.07$  $[.053\pm.003]$  $[.160\pm.003]$ XXXX  $2.71 \pm 0.07$  $[.107 \pm .003]$ 0,64 [.025] MAXIMUM → SOLDER BUILDUP -POSITIVE NEGATIVE-TERMINAL TERMINAL -OUTPUT TERMINAL Revision C.O. # Implementation Date RELEASE LEVEL REVISION NOMINAL WEIGHT В .28 GRAM Palancad MIDIDISIS 7-16-07 DIMENSIONS IN MILLIMETERS [INCHES]

**KNOWLES ELECTRONICS** ITASCA, ILLINOIS U.S.A.

<u> </u>		
SCALE: 2:1	DR. BY	DATE
DO NOT SCALE DRAWING	AB CK. BY	9 - 20 - 06 DATE
VIBRATION TRANSDUCER BU-23173-000	GJP	9-25-06
OUTLINE DRAWING SHT I.I	GJP	9-25-06

TO 1.0 VOLT/g



FREQUENCY IN HERTZ

# <u>SENSITIVITY</u>

#### FREQUENCY MIN. NOM. MAX. -39.0 1000 -43.5 -39.0 -34.5 -37.5 3000

## <u>DEVICE CONFORMITY</u>

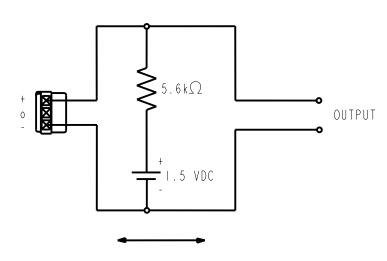
RANGE OF DEVIATION	FROM	k H z
-1.5	+1.5	
- I . O	+2.0	

### NOTES:

- I. ALL DATA MEASURED WITH CIRCUIT CONFIGURATION SHOWN BELOW.
- 2. CASE CONNECTED TO NEGATIVE TERMIANL.

DC	BATTERY	"A" WEIGHTED NOISE (RE I.OV)	OUTPUT IMPEDANCE OHMS (AT   kHz)		
SUPPLY	CURRENT DRAIN		MIN.	NOM.	MAX.
1.5 V	50 μA MAX.	-103 dB MAX.	4900	5200	5500

# 3. TEST CONDITIONS:



DIRECTION OF ACCELERATION

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
			_	
В	M10101515	7 - 16 - 07	Released	l K
А	MI0101175	9-20-06		ט

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WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION PR. BY CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION

TION OF EQUIPMENT AND TEST METHOD VARIATION	AB	9-20-06
	CK. BY	DATE
VIBRATION TRANSDUCER BU-23173-000	GJP	9-25-06
115 1	APP. BY	DATE
PERFORMANCE SPECIFICATION SHT 2.1	GJP	9-25-06

DATE

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