

BUL58D

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- STMicroelectronics PREFERRED SALESTYPE
- NPN TRANSISTOR
- HIGH VOLTAGE CAPABILITY
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- LOW BASE-DRIVE REQUIREMENTS
- VERY HIGH SWITCHING SPEED
- FULLY CHARACTERISED AT 125°C
- HIGH RUGGEDNESS
- INTEGRATED ANTIPARALLEL COLLECTOR-EMITTER DIODE

APPLICATIONS

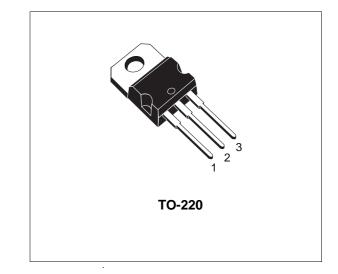
- ELECTRONIC TRANSFORMERS FOR HALOGEN LAMPS
- ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING
- SWITCH MODE POWER SUPPLIES

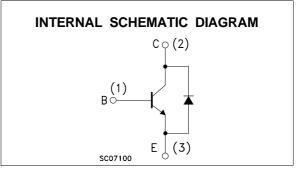
DESCRIPTION

The BUL58D is manufactured using high voltage Multi Epitaxial Planar technology to enhance switching speeds while maintaining a wide RBSOA.

The BUL series is designed for use in lighting applications and low cost switch-mode power supplies.

ABSOLUTE MAXIMUM RATINGS





Symbol	Parameter	Value	Unit	
V _{CES}	Collector-Emitter Voltage (V _{BE} = 0)	800	V	
V_{CEO}	Collector-Emitter Voltage (I _B = 0)	450	V	
Vebo	Emitter-Base Voltage (Ic = 0)	9	V	
lc	Collector Current	8	A	
Ісм	Collector Peak Current (t _p < 5 ms)	16	A	
Ι _Β	Base Current	4	A	
I _{BM}	Base Peak Current (t _p < 5 ms)	8	A	
Ptot	Total Dissipation at $T_c = 25 \ ^{\circ}C$	85	W	
T _{stg}	Storage Temperature	-65 to 150	°C	
Tj	Max. Operating Junction Temperature	150	°C	

THERMAL DATA

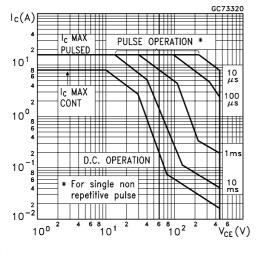
R _{thj-case}	Thermal Resistance Junction-Case	Max	1.47	°C/W
R _{thj-amb}	Thermal Resistance Junction-Ambient	Max	62.5	°C/W

ELECTRICAL CHARACTERISTICS ($T_{case} = 25 \,^{\circ}C$ unless otherwise specified)

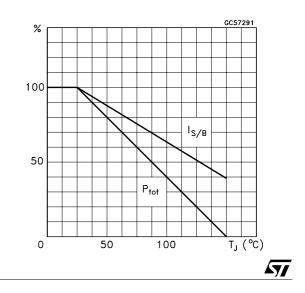
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
ICES	Collector Cut-off Current (V _{BE} = 0)	$V_{CE} = 800 V$ $V_{CEO} = 800 V$ $T_j = 125 \ ^{o}C$			200 500	μΑ μΑ
I _{CEO}	Collector Cut-off Current ($I_B = 0$)	V _{CE} = 450 V			200	μA
$V_{CEO(sus)}$	Collector-Emitter Sustaining Voltage	I _C = 100 mA L = 25 mH	450			V
V _{EBO}	Emitter-Base Voltage (I _C = 0)	I _E = 10 mA	9			V
V _{CE(sat)} *	Collector-Emitter Saturation Voltage				1.5 2	V V
V _{BE(sat)} *	Base-Emitter Saturation Voltage				1.3 1.5	V V
h _{FE} *	DC Current Gain	$ I_{C} = 5 A V_{CE} = 5 V \\ I_{C} = 500 \text{ mA} V_{CE} = 5 V $	5	38		
t _s t _f	INDUCTIVE LOAD Storage Time Fall Time			1 90	1.8 180	μs ns
t _s t _f	INDUCTIVE LOAD Storage Time Fall Time			1.5 180		μs ns
Vf	Diode Forward Voltage	I _C = 3 A			3	V

* Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %

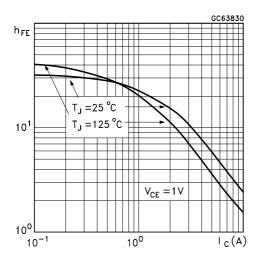
Safe Operating Areas



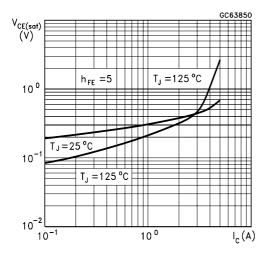
Derating Curve

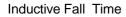


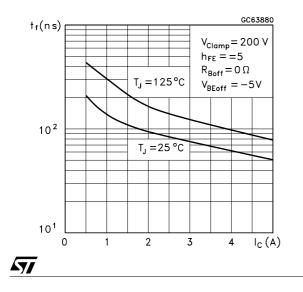
DC Current Gain



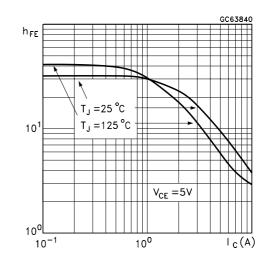
Collector Emitter Saturation Voltage



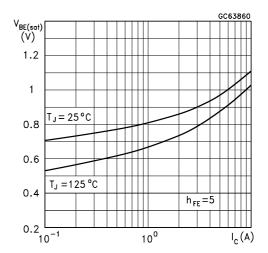




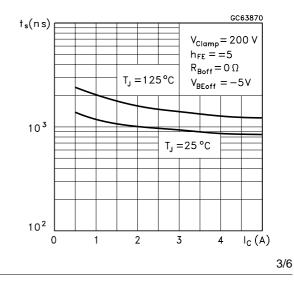
DC Current Gain



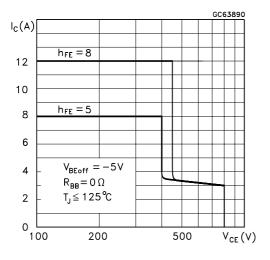




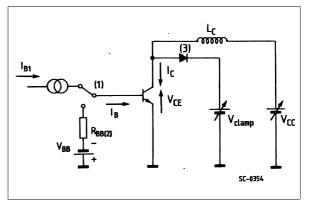




Reverse Biased SOA



RBSOA and Inductive Load Switching Test Circuit

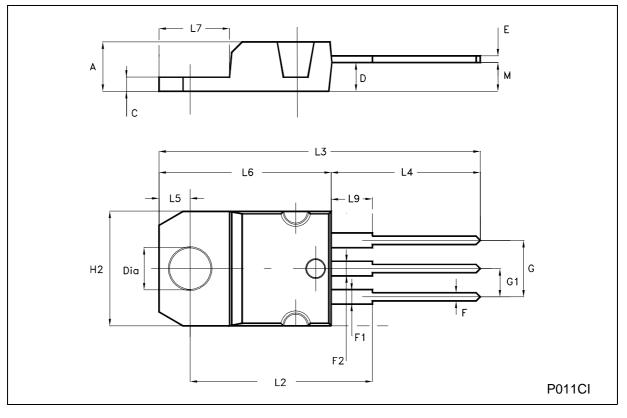


57

(1) Fast electronic switch
(2) Non-inductive Resistor
(3) Fast recovery rectifier

DIM.		mm			inch	
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.40		4.60	0.173		0.181
С	1.23		1.32	0.048		0.052
D	2.40		2.72	0.094		0.107
Е	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.202
G1	2.40		2.70	0.094		0.106
H2	10.00		10.40	0.394		0.409
L2		16.40			0.645	
L4	13.00		14.00	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.20		6.60	0.244		0.260
L9	3.50		3.93	0.137		0.154
М		2.60			0.102	





\$77

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57

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