

PEMH9

50 V, 100 mA NPN/NPN resistor-equipped double transistor; R1 = 10 k Ω , R2 = 47 k Ω

29 December 2022

Product data sheet

1. General description

NPN/NPN double Resistor-Equipped Transistor (RET) in an ultra small and flat lead SOT666 Surface-Mounted Device (SMD) plastic package.

PNP/PNP complement: PEMB9 NPN/PNP complement: PEMD9

2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- · Reduces component count
- · Reduces pick and place costs

3. Applications

- Low current peripheral driver
- Controlling IC inputs
- · Replacement for general purpose transistors in digital applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
V _{CEO}	collector-emitter voltage	open base		-	-	50	V
Io	output current			-	-	100	mA
R1	bias resistor 1 (input)		[1]	7	10	13	kΩ
R2/R1	bias resistor ratio		[1]	3.7	4.7	5.7	

[1] See "Test information" for resistor calculation and test conditions.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	GND1	GND (emitter) TR1		O1 I2 GND2
2	I1	input (base) TR1	6 5 4	
3	O2	output (collector) TR2		R1 R2
4	GND2	GND (emitter) TR2		TR2
5	12	input (base) TR2		TR1 R2 R1
6	01	output (collector) TR1	1 2 3	
			SOT666	
				GND1 I1 O2 sym063

6. Ordering information

Table 3. Ordering information

Type number	pe number Package						
	Name	Description	Version				
РЕМН9		plastic, surface-mounted package; 6 leads; 0.5 mm pitch; 1.6 mm x 1.2 mm x 0.55 mm body	<u>SOT666</u>				

7. Marking

Table 4. Marking codes

Type number	Marking code
РЕМН9	н9

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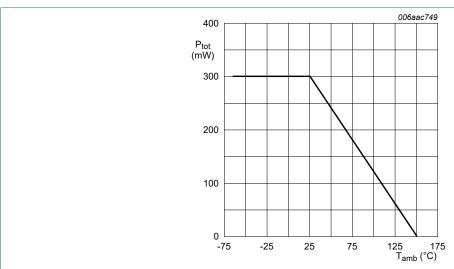
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transisto	or			'		
V _{CBO}	collector-base voltage	open emitter		-	50	V
V _{CEO}	collector-emitter voltage	open base		-	50	V
V _{EBO}	emitter-base voltage	open collector		-	6	V
V _I	input voltage	positive		-	40	V
		negative		-	-6	V
Io	output current			-	100	mA
I _{CM}	peak collector current	t _p ≤ 1 ms; single pulse		-	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Per device				,		
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.



FR4 PCB, single-sided, 35 μm copper, tin-plated and standard footprint

Fig. 1. Per device: Power derating curve

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor	Per transistor						
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W
Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	417	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

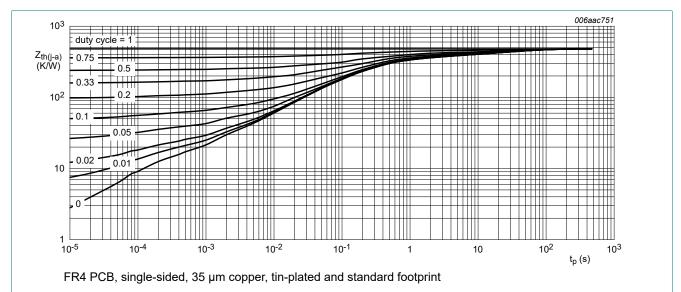


Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transist	or						
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = 100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I_C = 2 mA; I_B = 0 A; T_{amb} = 25 °C		50	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = 50 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
I _{CEO} collector-emitter cut-off	V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C		-	-	100	nA	
	current	V _{CE} = 30 V; I _B = 0 A; T _j = 150 °C		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	150	μΑ
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 5 mA; T _{amb} = 25 °C		100	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 5 \text{ mA}; I_B = 0.25 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	100	mV
V _{I(off)}	off-state input voltage	V _{CE} = 5 V; I _C = 100 μA; T _{amb} = 25 °C		-	0.7	0.5	V
V _{I(on)}	on-state input voltage	$V_{CE} = 0.3 \text{ V}; I_{C} = 1 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		1.4	0.8	-	V
R1	bias resistor 1 (input)		[1]	7	10	13	kΩ
R2/R1	bias resistor ratio		[1]	3.7	4.7	5.7	
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$		-	-	2.5	pF
f _T	transition frequency	V_{CE} = 5 V; I_{C} = 10 mA; f = 100 MHz; T_{amb} = 25 °C	[2]	-	230	-	MHz

- [1] See "Test information" for resistor calculation and test conditions.
- [2] Characteristics of built-in transistor

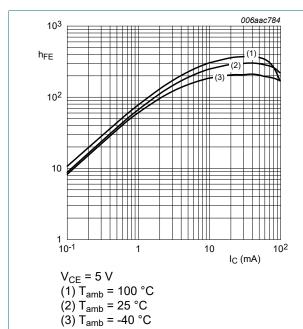
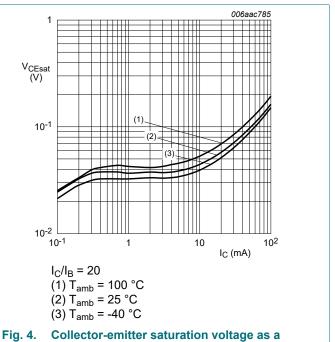
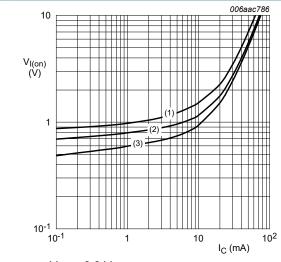


Fig. 3. DC current gain as a function of collector current; typical values



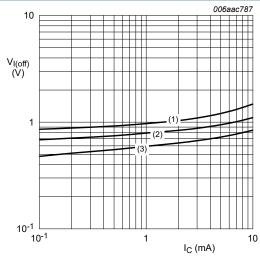
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 $V_{CE} = 0.3 V$

(1) T_{amb} = -40 °C (2) T_{amb} = 25 °C (3) T_{amb} = 100 °C

Fig. 5. On-state input voltage as a function of collector | Fig. 6. current; typical values



V_{CE} = 5 V (1) T_{amb} = -40 °C (2) T_{amb} = 25 °C (3) T_{amb} = 100 °C

Off-state input voltage as a function of collector current; typical values

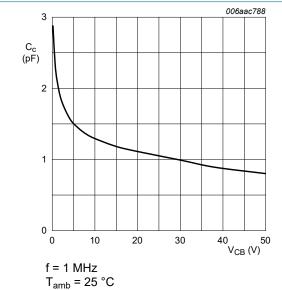
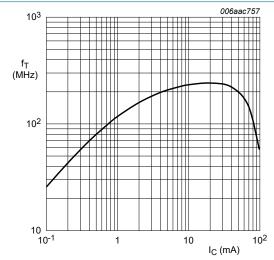


Fig. 7. Collector capacitance as a function of collectorbase voltage; typical values



 V_{CE} = 5 V; T_{amb} = 25 °C

Fig. 8. Transition frequency as a function of collector current; typical values of built-in transistor

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11. Test information

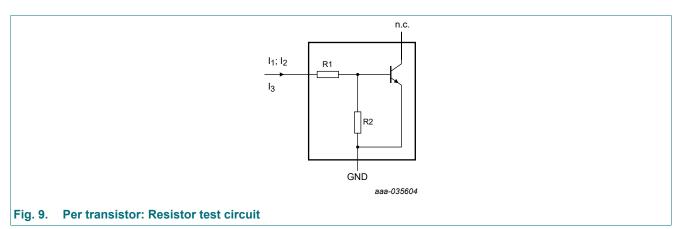
Resistor calculation

• Calculation of bias resistor 1 (R1)

$$R_1 = \frac{V(I_2) - V(I_1)}{I_2 - I_1}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I3)}{R1 \cdot I3} - 1$$



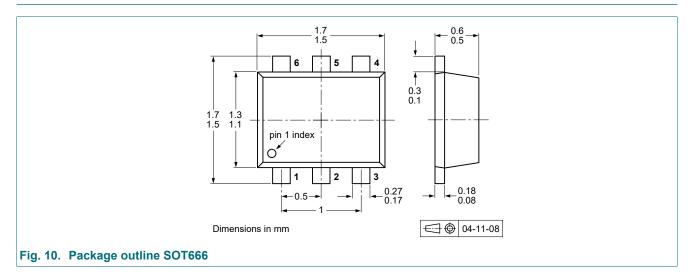
Resistor test conditions

Table 8. Resistor test conditions

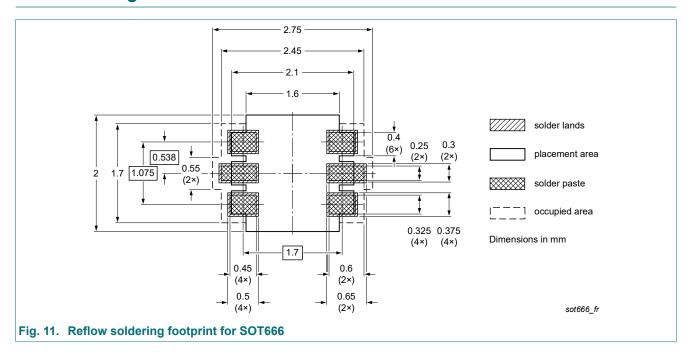
Type number	R1 (kΩ)	R2 (kΩ)	Test conditions		
			I ₁	l ₂	l ₃
РЕМН9	10	47	350 μΑ	450 μΑ	-100 µA

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12. Package outline



13. Soldering



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14. Revision history

Table 9. Revision history

Table 5. Nevision history						
Document ID	Release date	Data sheet status	Change notice	Supersedes		
PEMH9 v.6	20221229	Product data sheet	-	PEMH9_PIMH9_PUMH9 v.5		
Modification:	 The format of this data sheet has been redesigned to comply with the identity guideline of Nexperia. Legal texts have been adapted to the new company name where appropriate. Family data sheet reduced to single type data sheet. Product(s) changed to non-automotive qualification. Packing information removed. 					
PEMH9_PIMH9_PUMH9 v.5	20131112	Product data sheet	-	PIMH9_PUMH9_PEMH9 v.4		
PIMH9_PUMH9_PEMH9 v.4	20040414	Product data sheet	-	PIMH9_PUMH9_PEMH9 v.3		
PIMH9_PUMH9_PEMH9 v.3	20030915	Product specification		-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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