

Vishay Semiconductors

Silicon NPN Phototransistor



DESCRIPTION

VEMT2003X01 series are silicon NPN epitaxial planar phototransistors with daylight blocking filter in a miniature, black dome lens package for surface mounting. Filter bandwidth is matched with 830 nm to 950 nm IR emitters.

FEATURES

- · Package type: surface mount
- Package form: GW, RGW
- Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.55
- AEC-Q101 qualified
- High radiant sensitivity
- Daylight blocking filter matched with 830 nm to 950 nm IR emitters
- Fast response times
- Angle of half sensitivity: $\phi = \pm 35^{\circ}$
- Package matched with IR emitter series VSMB2943RGX01 and VSMB2943GX01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- · Lead (Pb)-free reflow soldering
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Detector in automotive applications
- Photo interrupters
- Miniature switches
- Counters
- Encoders
- Position sensors

PRODUCT SUMMARY			
COMPONENT	I _{ca} (mA)	φ (deg)	λ _{0.5} (nm)
VEMT2003X01	2.7	± 35	790 to 970
VEMT2023X01	2.7	± 35	790 to 970

Note

Test condition see table "Basic Characteristics"

ORDERING INFORMATION			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
VEMT2003X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing
VEMT2023X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing

Note

• MOQ: minimum order quantity

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COMPLIANT HALOGEN FREE <u>GREEN</u> (5-2008)



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Collector emitter voltage		V _{CEO}	20	V	
Emitter collector voltage		V _{ECO}	7	V	
Collector current		Ι _C	50	mA	
Power power dissipation	T _{amb} ≤ 75 °C	Pv	100	mW	
Junction temperature		Тj	100	°C	
Operating temperature range		T _{amb}	- 40 to + 100	°C	
Storage temperature range		T _{stg}	- 40 to + 100	°C	
Soldering temperature	Acc. reflow profile fig. 8	T _{sd}	260	°C	
Thermal resistance junction/ambient	Acc. J-STD-051	R _{thJA}	250	K/W	

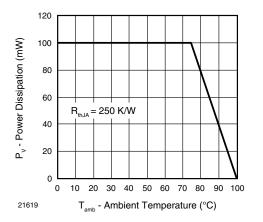


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	I _C = 0.1 mA	V _{CEO}	20			V
Collector dark current	$V_{CE} = 5 V, E = 0$	I _{CEO}		1	100	nA
Collector emitter capacitance	$V_{CE} = 0 V, f = 1 MHz, E = 0$	C _{CEO}		25		pF
Collector light current	$\begin{array}{l} E_{e} = 1 \ mW/cm^2, \lambda = 950 \ nm, \\ V_{CE} = 5 \ V \end{array}$	I _{ca}	1.3	2.7	4.1	mA
Angle of half sensitivity		φ		± 35		deg
Wavelength of peak sensitivity		λ _p		860		nm
Range of spectral bandwidth		λ _{0.5}		790 to 970		nm
Collector emitter saturation voltage	I _C = 0.05 mA	V _{CEsat}			0.4	V
Temperature coefficient of Ica	$\begin{array}{l} E_{e} = 1 \ mW/cm^2, \lambda = 950 \ nm, \\ V_{CE} = 5 \ V \end{array}$	Tk _{lca}		1.1		%/K

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

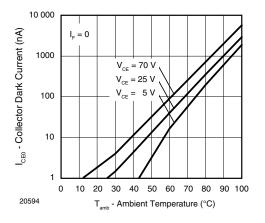


Fig. 2 - Collector Dark Current vs. Ambient Temperature

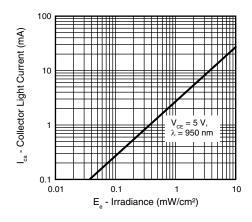


Fig. 3 - Collector Light Current vs. Irradiance

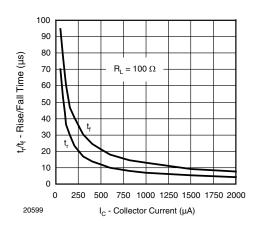


Fig. 4 - Rise/Fall Time vs. Collector Current

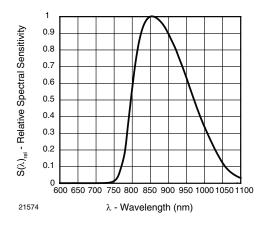


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

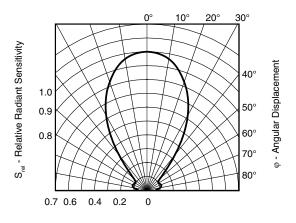


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

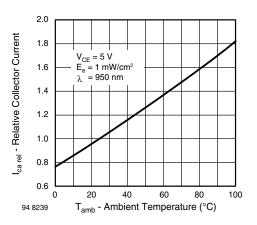


Fig. 7 - Relative Collector Current vs. Ambient Temperature

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REFLOW SOLDER PROFILE

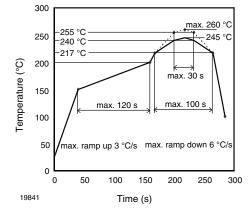


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

50

0.75

Collector

17

Drawing refers to following types:

Drawing-No.: 6.544-5409.02-4 Issue: prel. 03.08.12

2.3

VEMT2003X01, VEMT2023X01

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DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

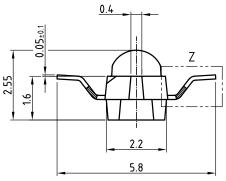
FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label: Floor life: 4 weeks Conditions: $T_{amb} < 30\ ^\circ C,\ RH < 60\ \%$ Moisture sensitivity level 2a, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

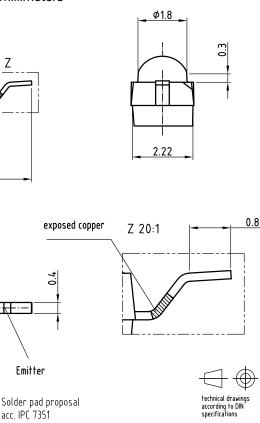




2.3

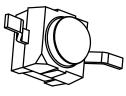
Pin ID

6.7



Dimensions in mm

Not indicated tolerances ±0.2



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Emitter

acc. IPC 7351

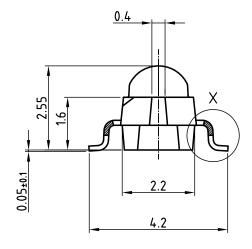
Ø2.3±0.1

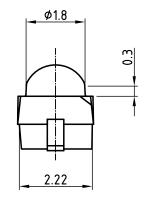
VEMT2x03X01

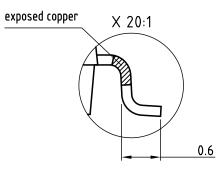


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PACKAGE DIMENSIONS VEMT2023X01 in millimeters

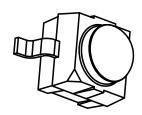


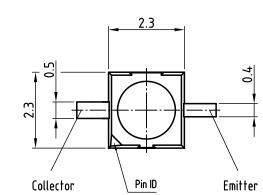


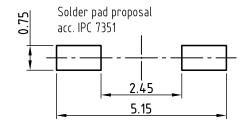




Dimensions in mm Not indicated tolerances ±0.2



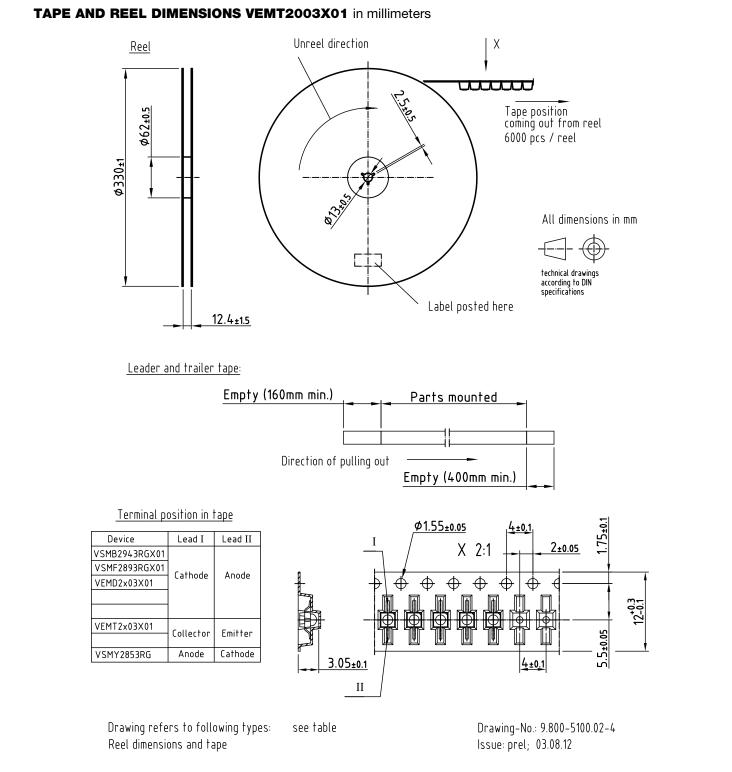




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VEMT2x23X01

Drawing-No.: 6.544-5408.02-4 Issue: prel; 03.08.12



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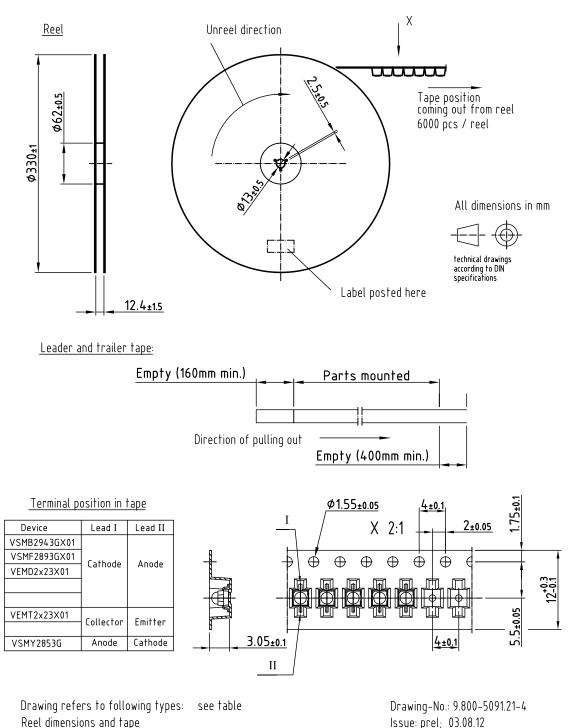
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TAPE AND REEL DIMENSIONS VEMT2023X01 in millimeters



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