

PMEG3002TV

0.2 A very low V_F MEGA Schottky barrier dual rectifier in SOT666 package

Rev. 02 — 15 January 2010

Product data sheet

1. Product profile

1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier dual rectifier with an integrated guard ring for stress protection, encapsulated in a SOT666 ultra small and flat lead Surface Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		Configuration	
	Nexperia	JEITA		
PMEG3002TV	SOT666	-	dual isolated	

1.2 Features

- Forward current: ≤ 0.2 A
- Reverse voltage: ≤ 30 V
- Very low forward voltage
- Ultra small and flat lead SMD plastic package

1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications

1.4 Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per diode						
l _F	forward current	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> -	-	0.2	А
V _R	reverse voltage		-	-	30	V
V _F	forward voltage	I _F = 200 mA	[2] _	420	480	mV

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$.

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2. Pinning information

Table 3.	Pinning		
Pin	Description	Simplified outline	Symbol
1	anode (diode 1)		
2	not connected		
3	cathode (diode 2)		
4	anode (diode 2)	0	
5	not connected		1 2 3 <i>006aaa440</i>
6	cathode (diode 1)	1 2 3	

3. Ordering information

Table 4. C	Ordering	information		
Type numbe	er	Package		
		Name	Description	Version
PMEG3002T	V	-	plastic surface mounted package; 6 leads	SOT666

4. Marking

Table 5.	Marking codes	
Type num	iber	Marking code
PMEG300	02TV	2M

5. Limiting values

Table 6

Limiting values

Symbol	Parameter	Conditions		Min	Max	Unit
Per diod	e					
V _R	reverse voltage			-	30	V
l _F	forward current	$T_{amb} \leq 25 ~^{\circ}C$	[1]	-	0.2	А
I _{FRM}	repetitive peak forward current	$t_p \leq$ 1 ms; $\delta \leq 0.25$		-	1	А
I _{FSM}	non-repetitive peak forward current	square wave; t _p = 8 ms	[1]	-	2.5	A
P _{tot}	total power dissipation	$T_{amb} \leq 25 ~^{\circ}C$	[1]	-	200	mW
			[2]	-	300	mW
Per devi	ce					
P _{tot}	total power dissipation	$T_{amb} \leq 25 \ ^{\circ}C$	[1]	-	300	mW
			[2]	-	400	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	+150	°C
T _{stg}	storage temperature			-65	+150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter Conditions Min Typ Max Un					Unit
Per devic	ce					
R _{th(j-a)}	thermal resistance from	in free air	[1][2] _	-	416	K/W
	junction to ambient		<u>[1][3]</u>	-	318	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		<u>[4]</u> -	-	195	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses. Nomograms for determining the reverse power losses P_R and $I_{F(AV)}$ rating are available on request.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

7. Characteristics

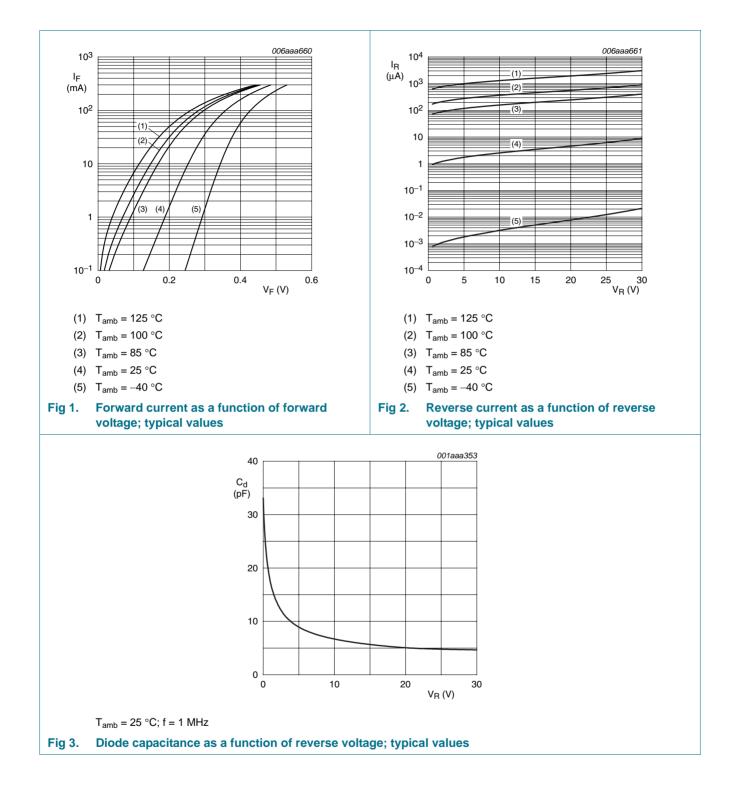
Table 8. **Characteristics** $T_{amb} = 25 \$ °C unless otherwise specified. Conditions Symbol Parameter Min Тур Max Unit Per diode V_{F} [1] forward voltage $I_{\rm F} = 0.1 \, {\rm mA}$ -130 190 m٧ $I_F = 1 \text{ mA}$ 190 250 m٧ - $I_{\rm F} = 10 \, {\rm mA}$ 255 300 m٧ -I_F = 100 mA 355 400 m٧ - $I_{\rm F} = 200 \, {\rm mA}$ 420 480 m٧ -V_R = 10 V I_R 3 10 reverse current μA $V_{R} = 30 V$ 10 30 μA - $V_R = 10 \text{ V}; \text{ T}_{amb} = 100 \text{ }^{\circ}\text{C}$ 400 -μΑ $V_{R} = 1 V; f = 1 MHz$ C_{d} diode capacitance 20 pF -25

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

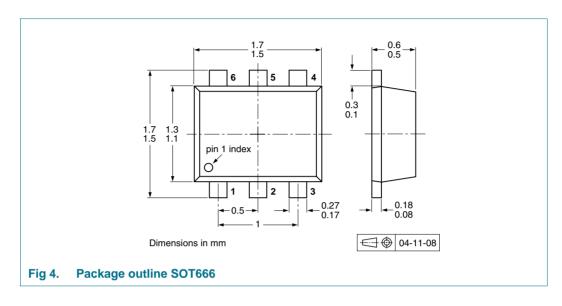
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PMEG3002TV

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8. Package outline



9. Packing information

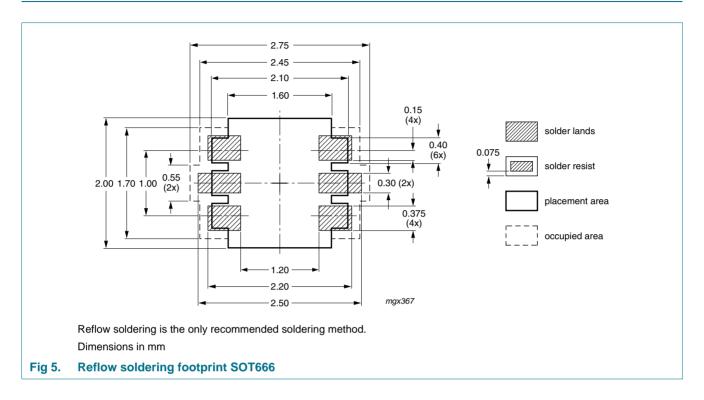
Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity		
			4000	8000	
PMEG3002TV SOT666		2 mm pitch, 8 mm tape and reel	-	-315	
		4 mm pitch, 8 mm tape and reel	-115	-	

[1] For further information and the availability of packing methods, see Section 13.

10. Soldering



11. Revision history

Table 10. Revision h	nistory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG3002TV_2	20100115	Product data sheet	-	PMEG3002TV_1
Modifications:		eet was changed to reflect w legal definitions and disc		
PMEG3002TV_1	20051021	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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