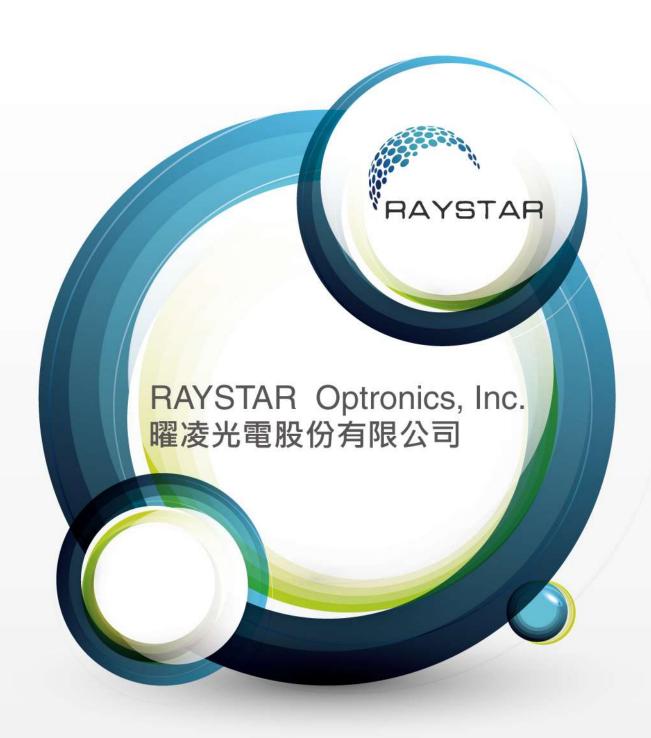
# **OLED DISPLAY SPECIFICATION**





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## **SPECIFICATION**

Model No:

# REG010032AYPP5N00100

### **CUSTOMER:**

APPROVED BY	
PCB VERSION	
DATE	
FOR CLISTOMER LISE ONLY	

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

Release DATE:

APPROVAL FOR SPECIFICATIONS ONLY	<b>1</b>
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☐ APPROVAL FOR SPECIFICATIONS AND SAMPLE



# **Revision History**

VERSION	DATE	REVISED PAGE NO.	Note
0	2021/03/22		First release





## **Contents**

- 1.General Specification
- 2. Module Classification Information
- 3.Interface Pin Function
- 4. Contour Drawing & Block Diagram
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- 6. Electrical Characteristics
- 7. Optical Characteristics
- 8.OLED Lifetime
- 9.Reliability
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## 1.General Specification

The Features is described as follow:

■ Module dimension: 98.0 x 60.0 x 10.0 (max) mm

■ View area: 77.0 x 25.2 mm

■ Active area: 58.95 x 19.15 mm

■ Dot Matrix: 100 x 32

Dot size: 0.54 x 0.55 mmDot pitch: 0.59 x 0.60 mm

■ Duty: 1/16

■ Emitting Color: OLED , Yellow

IC:RS0010-TXInterface: 6800Size: 2.44 inch



## 2. Module Classification information

1	2	3	4	5	6	7	8	9	10	11	12	13	14
R	Е	G	010032	Α	Y	Р	Р	5	N	0	0	1	00

	<del>,</del>					
1	Brand : Raystar	Optronics Inc.				
2	E:OLED					
	(	C : COB Character G : COB Graphic				
3	Dioploy Type	X : COG H : COG + FR				
3	Display Type	P: COG + FR + PCB T: COF				
		A: COG + PCB N: COF + FR + PCB				
4	Dot Matrix: 10	0*32				
5	Series					
		A : Amber R : Red C : Full Color				
6	Emitting Color	B : Blue W : White				
6	Emitting Color	G : Green Y : Yellow				
		S : Sky Blue X : Dual Color				
7	P: With Polarizer; N: Without Polarizer					
/	Polarizer	A : Anti-glare Polarizer				
8	Display Mode	P: Passive Matrix; N: Active Matrix				
9	Driver Voltage	3:3.0~3.3V; 5:5.0V				
10	Touch Panel	N : Without touch panel; T: With touch panel				
		0 : Standard				
	Draduat type	1 : Daylight Readable				
11	Product type	2 : Transparent OLED (TOLED)				
		3 : Flexible OLED (FOLED)				
	N Y	4 : OLED Lighting				
		0 : Standard				
12	Inspection Grade	2 : B grade				
IZ	mspection Grade	C : Automotive grade				
		Y : Consumer grade				
12	Ontion	0 : Default ; 1 : COB_6800 ; 2 : COB_8080 ; 3 : COB_SPI				
13	Option	D : Demo Kit ; Z : Semi-customized				
14	Serial No.	Serial number(00~ZZ)				
	1					

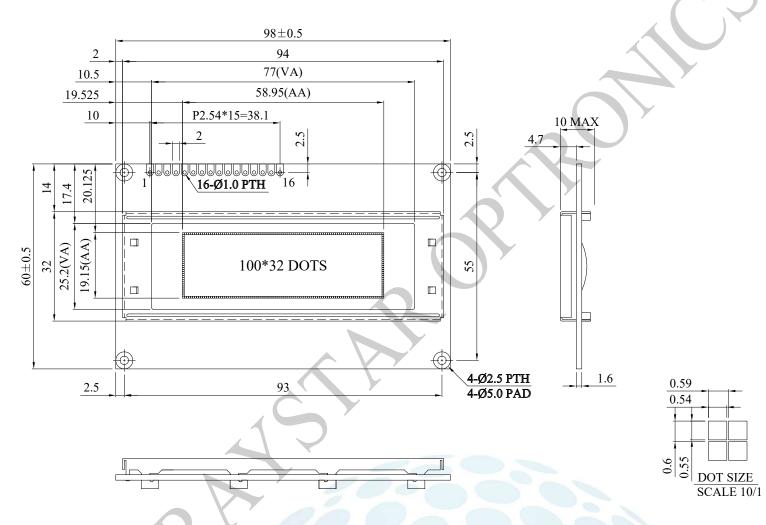


# 3.Interface Pin Function

Pin No.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply Voltage for logic
3	NC	_	
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(Module→MPU) L: Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	CS1		Chip1 select input pin
16	CS2		Chip2 select input pin



## **4.Contour Drawing & Block Diagram**

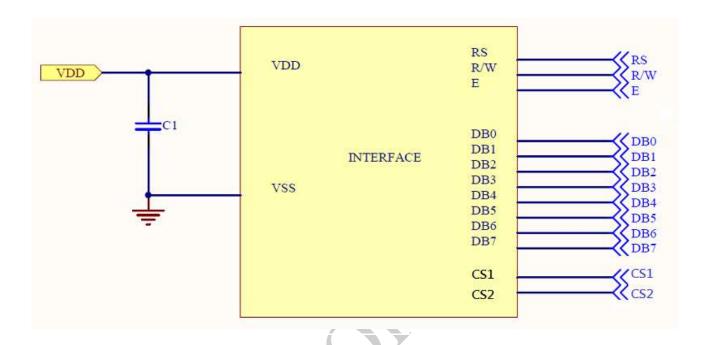


PIN NO.	SYMBOL
1	VSS
2	VDD
3	NC
4	RS
5	$R/\overline{W}$
6	Е
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	CS1
16	CS2

The non-specified tolerance of dimension is  $\pm 0.3 \text{ mm}$ .



## 3.1 Application recommendations



Pin connected to MCU interface: RS, R/W, E, DB0~7,CS1,CS2

C1: 1.0uF (1)

#### Note

(1) The capacitor value is recommended value. Select appropriate value against module application.





# **5.Absolute Maximum Ratings**

Item	Symbol	Min	Max	Unit	Notes
Operating Temperature	TOP	-40	+80	°C	-
Storage Temperature	TST	-40	+85	ο̈́	
Supply Voltage For Logic	VDD-VSS	-0.3	5.3	V	-



### **6.Electrical Characteristics**

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	VDD-VSS	_	4.8	5.0	5.3	V
Input High Volt.	VIH	_	0.8 VDD	_	VDD	V
Input Low Volt.	VIL	_	GND		0.2 VDD	٧
Output High Volt.	VOH	IOH=-0.5mA	0.8 VDD		VDD	V
Output Low Volt.	VOL	IOL=0.5mA	GND	F	0.2 VDD	V
50% Check Board Operating Current	IDD	VDD=5V		60	90	mA

Note: When you use 5V for Vdd please don't use 3V or 3.3V for logic I/O this will cause module does not work.

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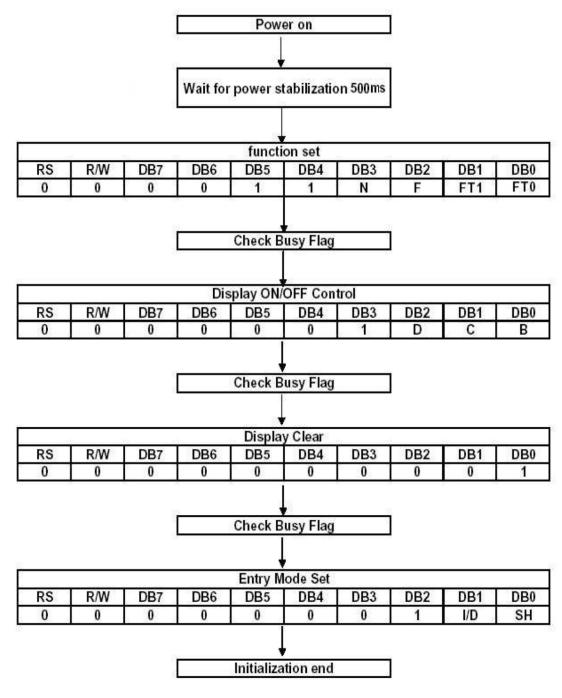




### 6.2 Initial code

#### INITIALIZATION BY INSTRUCTION

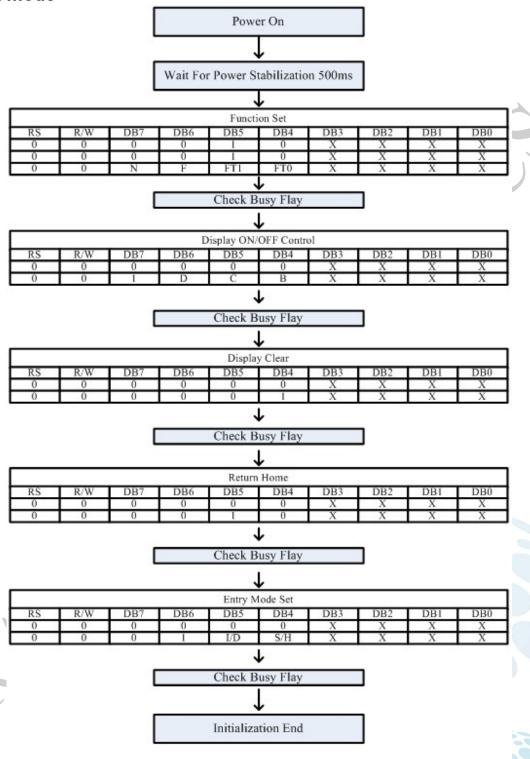
### (1)8-bit mode







### (2)4-bit mode



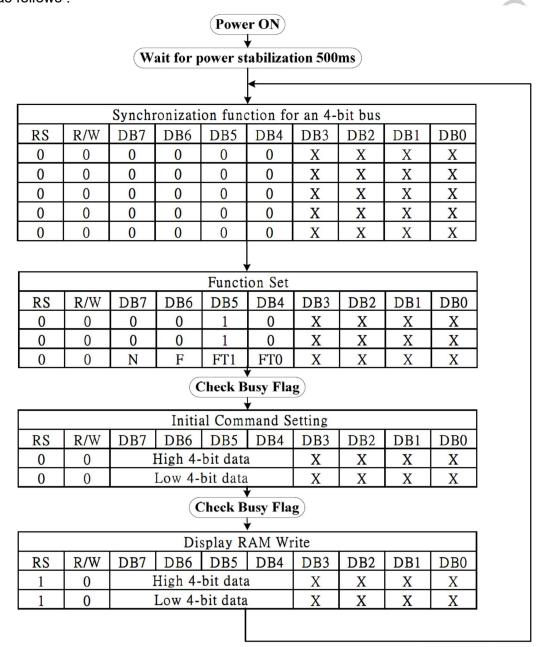


#### Notes

### Repeated procedures for an 4-bit bus interface

Noise causing transfer mismatch between the four upper and lower bits can be corrected by a reset triggered by consecutively writing a "0000" instruction five times. The next transfer starts from the lower four bits and then first instruction "Function set" can be executed normally.

Please insert the synchronization function in the head of procedures. The repeated procedures are show as follows:

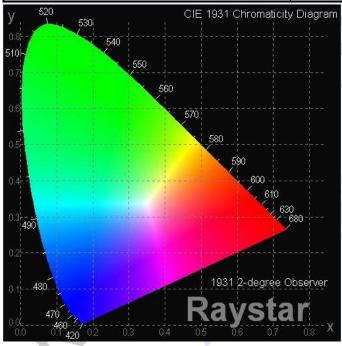


Note: Initial code is for reference only. Please make the best adjustment with the OLED module.



# 7. Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V)θ	_	160	_	_	deg
View / trigio	(Η)φ	_	160		7	deg
Contrast Ratio	CR	Dark	10,000:1	4		
Response Time	T rise	_	_	10		μs
Trespende Time	T fall	_	-	10	_	μs
Display with 50% check Board Brightness			70	80	_	cd/m2
CIEx(Yello	(CIE1931)	0.45	0.47	0.49	_	
CIEy(Yello	w)	(CIE1931)	0.48	0.50	0.52	_





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### 8.OLED Lifetime

ITEM	Conditions	Min	Тур	Remark
Operating Life Time	Ta=25°C / Initial 50% check Board Typical Brightness Value	80,000 Hrs	100,000 Hrs	Note

#### Note:

- 1. Life time is defined the amount of time when the luminance has decayed to <50% of the initial value.
- 2. This analysis method uses life data obtained under accelerated conditions to extrapolate an estimated probability density function (*pdf*) for the product under normal use conditions.
- 3. Screen saving mode will extend OLED lifetime.





# 9.Reliability

**Content of Reliability Test** 

Environmenta	I Test	T	Annliachta
Test Item	Content of Test	Test Condition	Applicable Standard
High Temperature storage	Endurance test applying the high storage temperature for a long time.	85°C 240hrs	-, (
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-40°C 240hrs	-
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80°C 240hrs	
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-40°C 240hrs	_
High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60°C,90%RH 240hrs	
High Temperature/ Humidity Operation	Endurance test applying the high temperature and high humidity Operation for a long time.	60°C,90%RH 120hrs	
Temperature Cycle	Endurance test applying the low and high temperature cycle40°C 25°C 80°C  30min 5min 30min	-40°C /80°C 30 cycles	256
Mechanical Tes	st C		
Vibration test	Endurance test applying the vibration during transportation and using.	Frequency:10~55Hz amplitude:1.5mm Time:0.5hrs/axis Test axis:X,Y,Z	
Others		·	
Static electricity test	Endurance test applying the electric stress to the finished product housing.	Air Discharge model ±4kv,10 times	

<sup>\*\*\*</sup> Supply voltage for OLED system =Operating voltage at 25°C



#### Test and measurement conditions

- 1. All measurements shall not be started until the specimens attain to temperature stability. After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5°C; 55±15% RH.
- 2. All-pixels on/off exchange is used as operation test pattern.
- 3. The degradation of Polarizer are ignored for High Temperature storage, High Temperature/ Humidity Storage, Temperature Cycle

#### **Evaluation criteria**

- 1. The function test is OK.
- 2. No observable defects.
- 3. Luminance: > 50% of initial value.
- 4. Current consumption: within ± 50% of initial value.

#### **APPENDIX:**

#### **RESIDUE IMAGE**

Because the pixels are lighted in different time, the luminance of active pixels may reduce or differ from inactive pixels. Therefore, the residue image will occur. To avoid the residue image, every pixel needs to be lighted up uniformly.



### 10.Inspection specification

### **Inspection Standard:**

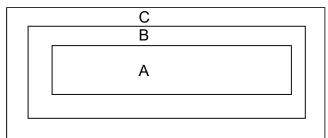
MIL-STD-105E table normal inspection single sample level II.

#### **Definition**

1 Major defect : The defect that greatly affect the usability of product.

2 Minor defect : The other defects, such as cosmetic defects, etc.

Definition of inspection zone:



Zone A: Active Area

Zone B: Viewing Area except Zone A

Zone C: Outside Viewing Area

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble of quality and assembly to customer's product.

### **Inspection Methods**

1 The general inspection: Under fluorescent light illumination: 750~1500 Lux, about 30cm viewing distance, within 45° viewing angle, under 25±5°C.

2 The luminance and color coordinate inspection: By SR-3 or BM-7 or the equal equipments, in the dark room, under 25±5°C.

NO	Item	Criterion	AQL
01	Electrical Testing	<ol> <li>1.1 Missing vertical, horizontal segment, segment contrast defect.</li> <li>1.2 Missing character, dot or icon.</li> <li>1.3 Display malfunction.</li> <li>1.4 No function or no display.</li> <li>1.5 Current consumption exceeds product specifications.</li> <li>1.6 OLED viewing angle defect.</li> <li>1.7 Mixed product types.</li> <li>1.8 Contrast defect.</li> </ol>	0.65
02	Black or white spots on OLED (display only)	<ul> <li>2.1 White and black spots on display □0.25mm, no more than three white or black spots present.</li> <li>2.2 Densely spaced: No more than two spots or lines within 3mm.</li> </ul>	2.5



NO	Item	Criterion			AQL	
	OLED black spots, white spots, contamin ation (non- display)	3.1 Round type : As following drawing Φ=(x+y)/2  → X	SIZE	Acceptable QTY ignore 2 1 0	Zone A+B A+B A+B A+B	2.5
03		3.2 Line type : (As  w Length  L≤3.0  L≤2.5	$\begin{array}{c c} n & \text{Width} \\ \hline & \text{W} \leq 0.02 \\ 0 & 0.02 < \text{W} \leq 0.0 \\ 5 & 0.03 < \text{W} \leq 0.0 \\ \end{array}$	Acceptable Q TY ignore	Zone A+B A+B A+B	2.5
			0.05 <w< td=""><td>As round type</td><td></td><td></td></w<>	As round type		
04	Polarizer bubbles /Dent	4.1 If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.  4.2 The polarizer d	Size $\Phi$ $\Phi \le 0.20$ $0.20 < \Phi \le 0.50$ $0.50 < \Phi \le 1.00$ $1.00 < \Phi$ Total Q TY	Acceptable Q TY ignore 3 2 0 3 ecification.	Zone A+B A+B A+B A+B	2.5
05	Scratches	Follow NO.3 OLED black spots, white spots, contamination.				



Symbols Define:	NO	Item	Criterion		
6.1.2 Corner crack:    Z: Chip thickness   y: Chip width   x: Chip length   Z ≤ 1/2t   Not over viewing area   x ≤ 1/8a   1/2t < z ≤ 2t   Not exceed 1/3k   x ≤ 1/8a   ⊙ If there are 2 or more chips, x is the total length of each chip.    Symbols :			x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: OLED side length L: Electrode pad length: 6.1 General glass chip: 6.1.1 Chip on panel surface and crack between panels:		
x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: OLED side length L: Electrode pad length 6.2 Protrusion over terminal : 6.2.1 Chip on electrode pad :  y: Chip width x: Chip length z: Chip thickness	06	g	$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$		
		V /	x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: OLED side length L: Electrode pad length 6.2 Protrusion over terminal: 6.2.1 Chip on electrode pad:  y: Chip width x: Chip length z: Chip thickness		



NO	Item	Criterion	
06	Glass crack	<ul> <li>6.2.2 Non-conductive portion:          <ul> <li>y: Chip width x: Chip length x≤ Chip thickness y≤ L x≤1/8a 0 &lt; z≤ t</li> </ul> </li> <li>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</li> <li>⊙ If the product will be heat sealed by the customer, the alignment mark not be damaged.</li> <li>6.2.3 Substrate protuberance and internal crack.</li> <li>y: width x: length y≤1/3L x≤ a</li> </ul>	2.5
07	Cracked glass	The OLED with extensive crack is not acceptable.	
08	Backlight elements		
09	Bezel	<ul><li>9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.</li><li>9.2 Bezel must comply with job specifications.</li></ul>	



NO	Item	Criterion	AQL
		10.1 COB seal may not have pinholes larger than 0.2mm or contamination.	2.5
		<ul><li>10.2 COB seal surface may not have pinholes through to the IC.</li><li>10.3 The height of the COB should not exceed the height</li></ul>	2.5 0.65
10	PCB, COB	indicated in the assembly diagram.  10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.	2.5
10	1 05, 005	10.5 No oxidation or contamination PCB terminals.	2.5
		10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.	0.65
		10.7 The jumper on the PCB should conform to the product characteristic chart.	0.65
		10.8 If solder gets on bezel tab pads, OLED pad, zebra pad or screw hold pad, make sure it is smoothed down.	2.5
		11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections,	2.5 2.5
11	Soldering	oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 0.65
		12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
	, (	12.2 No cracks on interface pin (OLB) of TCP. 12.3 No contamination, solder residue or solder balls on	0.65 2.5
12	General	product.  12.4 The IC on the TCP may not be damaged, circuits.  12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5 2.5
	appearance	12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		<ul><li>12.7 Sealant on top of the ITO circuit has not hardened.</li><li>12.8 Pin type must match type in specification sheet.</li></ul>	2.5 0.65
		12.9 OLED pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65



Check Item	Classification	Criteria
No Display	Major	
Missing Line	Major	
Pixel Short	Major	
Darker Short	Major	
Wrong Display	Major	
Un-uniform B/A x 100% < 70% A/C x 100% < 70%	Major	A Normal B Dark Pixel C Light Pixel



### 11.Precautions in use of OLED Modules

### **Modules**

- (1) Avoid applying excessive shocks to module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, change the components or modify its shape of OLED display module.
- (3) Don't disassemble the OLED display module.
- (4) Do not apply input signals while the logic power is off.
- (5) Don't operate it above the absolute maximum rating.
- (6) Don't drop, bend or twist OLED display module.
- (7) Soldering: only to the I/O terminals.
- (8) Hot-Bar FPC soldering condition: 280~350C, less than 5 seconds.
- (9) Raystar has the right to change the passive components (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.) and change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Raystar have the right to modify the version.)
- (10) Raystar has the right to upgrade or modify the product function.
- (11) For COG & COF structure OLED products, customers should reserve VCC (VPP) adjustment function or software update function when designing OLED supporting circuit. (The progress of OLED light-emitting materials will increase the conversion efficiency and the brightness. The brightness can be adjusted if necessary).

#### 11.1. Handling Precautions

- (1) Since the display panel is being made of glass, do not apply mechanical impacts such as dropping from a high position.
- (2) If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance.
- (3) If pressure is applied to the display surface or its neighborhood of the OLED display module, the cell structure may be damaged. So, be careful not to apply pressure to these sections.
- (4) The polarizer covering the surface of the OLED display module is soft and easily scratched.
- (5) When the surface of the polarizer of the OLED display module has soil, clean the surface. It takes advantage by using following adhesion tape.
  - \* Scotch Mending Tape No. 810 or an equivalent
  - Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.

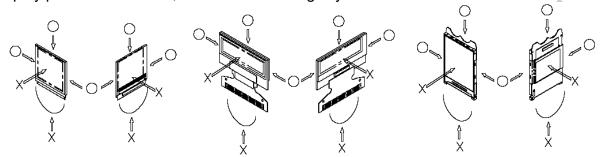
Also, pay attention that the following liquid and solvent may spoil the polarizer:

- \* Water
- \* Ketone
- \* Aromatic Solvents
- (6) Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OLED display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5.
- (7) Do not touch the following sections whenever possible while handling the OLED display



modules.

- \* Pins and electrodes
- \* Pattern layouts such as the TCP & FPC
- (8) Hold OLED display module very carefully when placing OLED display module into the System housing. Do not apply excessive stress or pressure to OLED display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases.



- (9) Do not apply stress to the LSI chips and the surrounding molded sections.
- (10) Pay sufficient attention to the working environments when handing OLED display modules to prevent occurrence of element breakage accidents by static electricity.
  - \* Be sure to make human body grounding when handling OLED display modules.
  - \* Be sure to ground tools to use or assembly such as soldering irons.
  - \* To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
  - \* Protective film is being applied to the surface of the display panel of the OLED display module. Be careful since static electricity may be generated when exfoliating the protective film.

#### 11.2. Storage Precautions

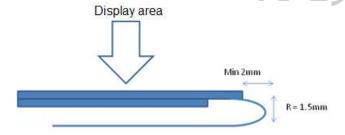
- (1) When storing OLED display modules, put them in static electricity preventive bags to avoid be directly exposed to sun or lights of fluorescent lamps. And, also, place in the temperature 25±5°C and Humidity below 65% RH.(We recommend you to store these modules in the packaged state when they were shipped from Raystar. At that time, be careful not to let water drops adhere to the packages or bags.)
- (2) When the OLED display module is being dewed or when it is placed under high temperature or high humidity environments, the electrodes may be corroded if electric current is applied. Please store it in clean environment.

#### 11.3. Designing Precautions

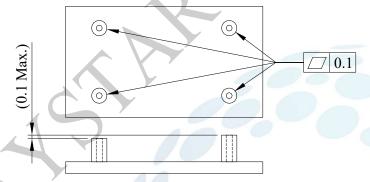
- (1) The absolute maximum ratings are the ratings which cannot be exceeded for OLED display module, and if these values are exceeded, OLED display module may be damaged.
- (2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the VIL and VIH specification and to make the signal line cable as short as possible.
- (3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (VDD / VCC). (Recommend value: 0.5A)
- (4) Pay sufficient attention to avoid occurrence of mutual noise interference with the nearby devices.



- (5) As for EMI, take necessary measures on the equipment side basically.
- (6) If the power supplied to the OLED display module is forcibly shut down by such errors as taking out the main battery while the OLED display panel is in operation, we cannot guarantee the quality of this OLED display module.
  - \* Connection (contact) to any other potential than the above may lead to rupture of the IC.
- (7) If this OLED driver is exposed to light, malfunctioning may occur and semiconductor elements may change their characteristics.
- (8) The internal status may be changed, if excessive external noise enters into the module. Therefore, it is necessary to take appropriate measures to suppress noise generation or to protect module from influences of noise on the system design.
- (9) We recommend you to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data) to cope with catastrophic noise.
- (10) It's pretty common to use "Screen Saver" to extend the lifetime and Don't use the same image for long time in real application. When an OLED display module is operated for a long of time with fixed pattern, an afterimage or slight contrast deviation may occur.
- (11) The limitation of FPC and Film bending.



(12) The module should be fixed balanced into the housing, or the module may be twisted.



(13) Please heat up a little the tape sticking on the components when removing it; otherwise the components might be damaged.

### 11.4. Precautions when disposing of the OLED display modules

(1) Request the qualified companies to handle industrial wastes when disposing of the OLED display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.



Page: 1

Module Sample Estimate Feedback Sheet						
Module Number :						
1 · Panel Specification :						
1. Panel Type :	□ Pass	□NG ,				
2. Numbers of Pixel:	□ Pass	□NG ,				
3. View Area:	□ Pass	□NG ,				
4. Active Area:	□ Pass	□NG ,				
5.Emitting Color:	□ Pass	□NG ,				
6.Uniformity:	□Pass	□NG ,				
7.Operating	□ Pass	□NG ,				
Temperature :						
8.Storage	□ Pass	□NG ,				
Temperature :						
9.Others :						
2 · Mechanical Specificat	ion:					
1. PCB Size:	□Pass	□NG ,				
2.Frame Size :	□Pass	□NG ,				
3.Materal of Frame :	□Pass	□NG ,				
4.Connector Position:	□Pass	□NG ,				
5.Fix Hole Position:	□Pass	nng ,				
6. Thickness of PCB:	□Pass	□NG ,				
7. Height of Frame to	□Pass	□NG ,				
PCB:		10000				
8.Height of Module:	□Pass	□NG ,				
9.Others:	□Pass	□NG ,				
3 Relative Hole Size:						
1.Pitch of Connector:	□Pass	□NG ,				
2.Hole size of	□Pass	□NG ,				
Connector:						
3.Mounting Hole size : □Pass		□NG ,				
4.Mounting Hole Type : □Pass		□NG ,				
5.Others:	□Pass	□NG .				

>> Go to page 2 <<



		Page: 2		
Module Number :				
4 · Electronic Characteristics of Module :				
1.Input Voltage :	□Pass	□NG ,		
2.Supply Current:	□Pass	□NG ,		
3.Driving Voltage for OLED :	□Pass	ING ,		
4.Contrast for OLED :	□Pass	□NG ,		
5.Negative Voltage Output :	□Pass	□NG ,		
6.Interface Function :	□Pass	□NG ,		
7.ESD test:	□Pass	□NG ,		
8.Others:	□Pass	□NG ,		
5 \ Summary :				
Sales signature :				
Customer Signature: Date: / /				