

# T1620T-8I, T1635T-8I

#### Snubberless™ 16 A Triac

Datasheet - production data

#### **Features**

- High static and dynamic commutation
- Three quadrants
- Snubberless device
- Package is RoHS (2002/95/EC) compliant
- Tab insulated, voltage = 2500 V rms
- UL certified (ref. file E81734)

### **Applications**

- General purpose AC line load switching
- Home appliances:
  - Fan
  - Pump
  - Solenoid
- Lighting
- Heaters
- Inrush current limiting circuits
- Overvoltage crowbar protection circuits

## **Description**

Available in TO220AB-Ins. (ceramic insulated), the T1620T-8I, and T1635T-8I Triacs can be used as on/off or phase angle function controllers in general purpose AC switching.

These devices can be used without snubber (R + C networks) if the datasheet limits are respected.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

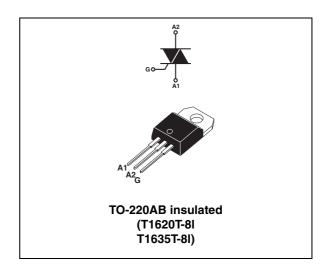


Table 1. Device summary

Order code	Quadrants	Value I <sub>GT</sub> (mA)
T1620T-8I	1 - 11 - 111	20
T1635T-8I	1 - 11 - 111	35

TM: Snubberless is a trademark of STMicroelectronics

Characteristics T1620T-8I, T1635T-8I

## 1 Characteristics

Table 2. Absolute maximum rating ( $T_j = 25$  °C, unless otherwise specified)

Symbol	Parameter				Unit	
	On state was surrent (full size ways)		T <sub>c</sub> = 108 °C	16	^	
I <sub>T(RMS)</sub> On-state rms current (full sine wave)		T <sub>c</sub> = 119 °C		12	Α	
	Non repetitive surge peak on-state current (full	F = 50 Hz	t <sub>p</sub> = 20 ms	120	۸	
I <sub>TSM</sub>	cycle, T <sub>j</sub> initial = 25 °C)	F = 60 Hz	$t_p = 16.7 \text{ ms}$	126	A	
l <sup>2</sup> t	I <sup>2</sup> t Value for fusing		t <sub>p</sub> = 10 ms	95	A <sup>2</sup> s	
V <sub>DRM</sub> /	$V_{DBM}$ / $T_j = 150^{\circ}$			600		
V <sub>RRM</sub>			T <sub>j</sub> = 125 °C	800	V	
V <sub>DSM</sub> , V <sub>RSM</sub>	Non repetitive surge peak off-state voltage	etitive surge peak off-state voltage $t_p = 10 \text{ ms}$		900	V	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ F = 100 Hz			100	A/µs	
I <sub>GM</sub>	Peak gate current $t_p = 20 \mu s$		T <sub>j</sub> = 150 °C	4	Α	
P <sub>G(AV)</sub>	Average gate power dissipation $T_j = 150 ^{\circ}\text{C}$			1	W	
T <sub>stg</sub>	Storage junction temperature range			-40 to +150	°C	
Tj	Operating junction temperature range			-40 to +150		
TL	Lead temperature for soldering during 10 s (at 4 mm from case for TO220AB-ins.)			260	°C	
V <sub>ins</sub> (rms)	Insulation rms voltage, 1 minute, TO220AB ceramic insulated			2500	V	

T1620T-8I, T1635T-8I

Table 3. Electrical characteristics ( $T_j = 25$  °C, unless otherwise specified)

Cymbal	Symbol Test conditions		Quadrant		Value		l lmi4
Symbol			Quadrant		T1620T	T1635T	Unit
I <sub>GT</sub> <sup>(1)</sup>	(1) 1/2 10 1/2 00 0		1 - 11 - 111	MIN.	1	1.75	mA
'GT`	$V_D = 12 \text{ V}, R_L = 30 \Omega$		1 - 11 - 111	MAX.	20	35	mA
V <sub>GT</sub>	$V_D = 12 \text{ V}, \text{ RL} = 30 \Omega$		All	MAX.	1.3		V
$V_{GD}$	$V_D = 800 \text{ V}, R_L = 3.3 \text{ k}\Omega, T_j = 125 \text{ °C}$		All	MIN.	0.2		V
I <sub>H</sub> <sup>(1)</sup>	I <sub>T</sub> = 500 mA			MAX.	25	45	mA
	1 101		1 - 111	MAX.	35	55	m A
IL	$I_{G} = 1.2 I_{GT}$		II	WAA.	40	65	mA
dV/dt (1)	$V_D = 67\% \times 800 \text{ V gate open}$ $T_j = 125 \text{ °C}$			MIN.	1000	2000	V/µs
u v/ut · /	$V_D = 67\% \text{ x } 600 \text{ V } \text{gate open}$ $T_j = 150 \text{ °C}$			IVIIIN.	500	1000	ν/μ5
(dl/dt)c (1)	(dV/dt)c = snubberless (> 20 V/μs)	T <sub>j</sub> = 125 °C		MIN.	6	16	A/ms
	$T_j = 150 ^{\circ}\text{C}$			IVIII V.	4.5	12	7/113
t <sub>GT</sub>	gate controlled turn on time $I_{TM}$ = 13 A, $V_D$ = 400 V, $I_G$ = 100 mA, $dI_G/dt$ = 100 mA/ $\mu$ s, $R_L$ = 30 $\Omega$		1 - 11 - 111	TYP.	2	2	μs

<sup>1.</sup> For both polarities of A2 referenced to A1

Table 4. Static characteristics

Symbol	Test conditions			Value	Unit
V <sub>TM</sub> <sup>(1)</sup>	$I_{TM} = 22.6 \text{ A}, t_p = 380  \mu\text{s}$	T <sub>j</sub> = 25 °C	MAX.	1.55	V
V <sub>to</sub> <sup>(1)</sup>	Threshold voltage	T <sub>j</sub> = 150 °C	MAX.	0.85	V
R <sub>d</sub> <sup>(1)</sup>	Dynamic resistance	T <sub>j</sub> = 150 °C	MAX.	30	mΩ
I <sub>DRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub> = 800 V	T <sub>j</sub> = 25 °C	MAX.	5	μΑ
		T <sub>j</sub> = 125 °C		1	mA
	V <sub>DRM</sub> = V <sub>RRM</sub> = 600 V	T <sub>j</sub> = 150 °C		3.6	

<sup>1.</sup> for both polarities of A2 referenced to A1

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (AC)	2.1	°C/W
R <sub>th(j-a)</sub>	Junction to ambient	60	°C/W

Characteristics T1620T-8I, T1635T-8I

Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case on-state rms current (full cycle) temperature (full cycle)

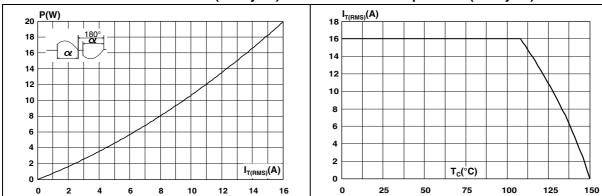


Figure 3. On-state rms current versus ambient temperature (free air convection)

Figure 4. Relative variation of thermal impedance versus pulse duration

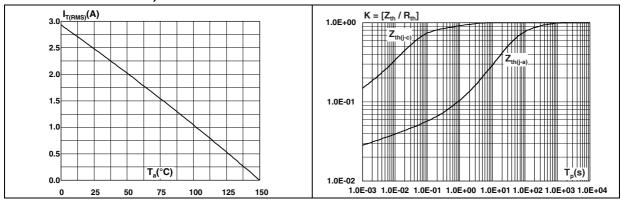
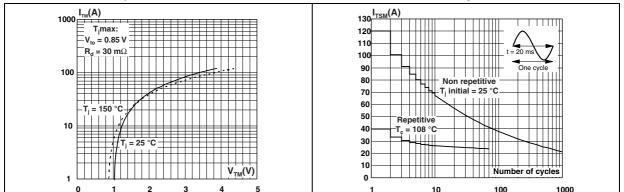


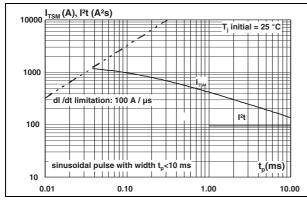
Figure 5. On-state characteristics (maximum Figure 6. Surge peak on-state current versus values)



T1620T-8I, T1635T-8I **Characteristics** 

Non repetitive surge peak on-state Figure 7. current and corresponding values of I<sup>2</sup>t

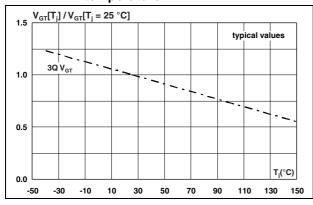
Figure 8. Relative variation of gate trigger current versus junction temperature



 $I_{GT}[T_i]/I_{GT}[T_i] = 25 °C]$ typical values 3Q I<sub>GT</sub> Q3 1.5 3Q I<sub>GT</sub> Q1-Q2 ₹: 0.5 T<sub>i</sub>(°C) -50 -30 -10 10 130

Figure 9. Relative variation of gate trigger voltage versus junction temperature

Figure 10. Relative variation of holding current and latching current versus junction temperature



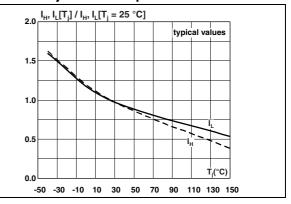
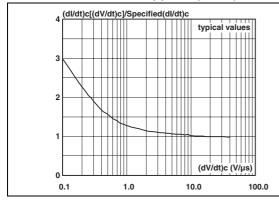
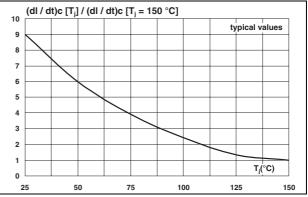


Figure 11. decrease of main current (di/dt)c versus reapplied (dV/dt)c

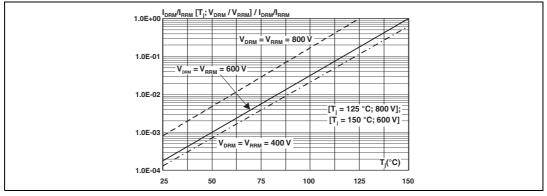
Relative variation of critical rate of Figure 12. Relative variation of critical rate of decrease of main current (di/dt)c versus junction temperature





Characteristics T1620T-8I, T1635T-8I

Figure 13. Relative variation of leakage current versus junction temperature for different values of blocking voltage

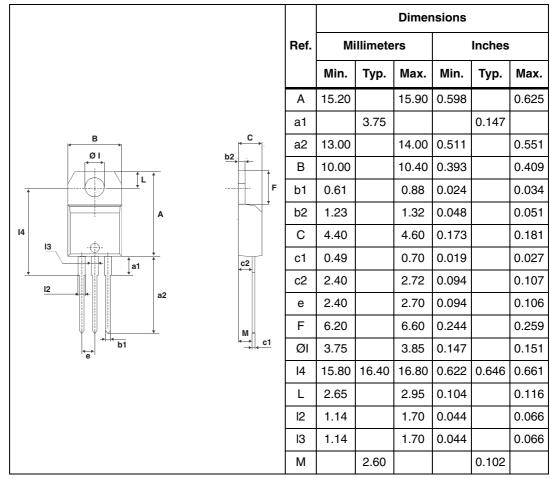


## 2 Package information

- Epoxy meets UL94, V0
- Recommended torque value: 0.4 to 0.6 N⋅m

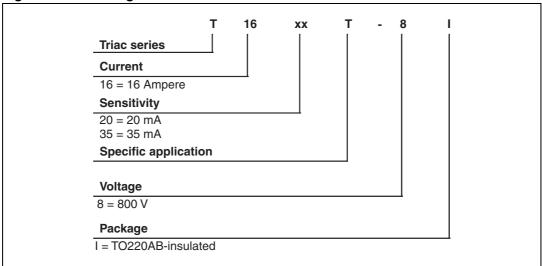
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Table 6. TO-220AB insulated dimensions



# 3 Ordering information scheme

Figure 14. Ordering information scheme



# 4 Ordering information

 Table 7.
 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1620T-8I	T1620T-8I	TO-220AB insulated	2.3	50	Tube
T1635T-8I	T1635T-8I	TO-220AB insulated	2.3	50	Tube

# 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
20-Jan-2012	1	First issue.
25-Apr-2012	2	Updated UL certification.

#### Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY TWO AUTHORIZED ST REPRESENTATIVES, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2012 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

10/10 Doc ID 022134 Rev 2

## **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

STMicroelectronics:

T1620T-8I T1635T-8I