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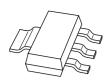
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Kind regards,

Team Nexperia



# PBHV9215Z 150 V, 2 A PNP high-voltage low V<sub>CEsat</sub> (BISS) transistor Rev. 01 — 11 December 2009 Product data

Product data sheet

#### **Product profile**

#### 1.1 General description

PNP high-voltage low V<sub>CEsat</sub> Breakthrough In Small Signal (BISS) transistor in a medium power SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

NPN complement: PBHV8215Z.

#### 1.2 Features

- High voltage
- Low collector-emitter saturation voltage V<sub>CEsat</sub>
- High collector current capability I<sub>C</sub> and I<sub>CM</sub>
- High collector current gain (h<sub>FE</sub>) at high I<sub>C</sub>
- AEC-Q101 qualified
- Medium power SMD plastic package

#### 1.3 Applications

- LED driver for LED chain module
- LCD backlighting
- Automotive motor management
- Switch Mode Power Supply (SMPS)

#### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{CEO}$	collector-emitter voltage	open base	-	-	-150	V
I <sub>C</sub>	collector current		-	-	-2	Α
h <sub>FE</sub>	DC current gain	$V_{CE} = -10 \text{ V};$ $I_{C} = -100 \text{ mA}$	<u>[1]</u> 100	180	-	

[1] Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 





# 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	base		
2	collector	4	2, 4
3	emitter		1—
4	collector	1 -2 -3	3
			sym028

# 3. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PBHV9215Z	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223		

# 4. Marking

Table 4. Marking codes

Type number	Marking code
PBHV9215Z	V9215Z

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#### 150 V, 2 A PNP high-voltage low V<sub>CEsat</sub> (BISS) transistor

## 5. Limiting values

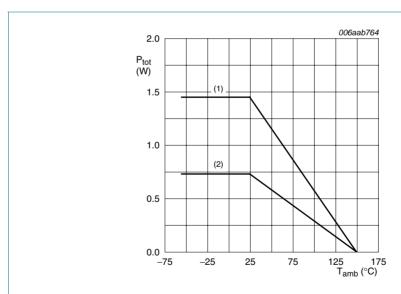
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{\text{CBO}}$	collector-base voltage	open emitter	-	-200	V
$V_{CEO}$	collector-emitter voltage	open base	-	-150	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	-6	V
I <sub>C</sub>	collector current		-	-2	Α
I <sub>CM</sub>	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-4	Α
I <sub>BM</sub>	peak base current	single pulse; $t_p \le 1 \text{ ms}$	-	-500	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25  ^{\circ}C$	<u>[1]</u> _	0.73	W
			[2] _	1.45	W
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.



- (1) FR4 PCB, mounting pad for collector 6 cm<sup>2</sup>
- (2) FR4 PCB, standard footprint

Fig 1. Power derating curves

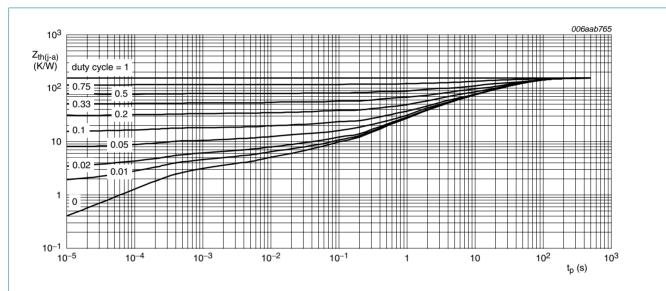
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#### 6. Thermal characteristics

Table 6. Thermal characteristics

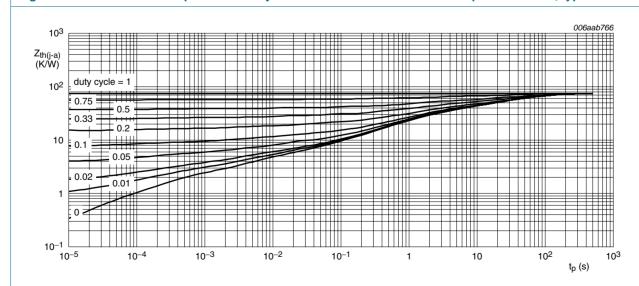
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	170	K/W
			[2] _	-	85	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		-	-	15	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for collector 6 cm<sup>2</sup>.



FR4 PCB, standard footprint

Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for collector 6 cm<sup>2</sup>

PBHV9215Z\_1

Fig 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



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## 150 V, 2 A PNP high-voltage low V<sub>CEsat</sub> (BISS) transistor

#### **Characteristics 7**.

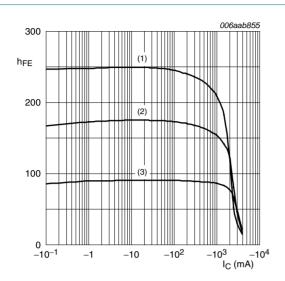
Table 7. Characteristics

 $T_{amb} = 25 \, ^{\circ}\text{C}$  unless otherwise specified.

Symbol	Parameter	Conditions	ı	Min	Тур	Max	Unit
$I_{CBO}$	collector-base cut-off	$V_{CB} = -120 \text{ V}; I_E = 0 \text{ A}$		-	-	-100	nA
	current	$V_{CB} = -120 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 ^{\circ}\text{C}$	•	-	-	-10	μΑ
I <sub>CES</sub>	collector-emitter cut-off current	$V_{CE} = -120 \text{ V}; V_{BE} = 0 \text{ V}$	•	-	-	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -4 \text{ V}; I_C = 0 \text{ A}$	•	-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = -10 \text{ V}$					
		$I_C = -100 \text{ mA}$	[1]	100	180	-	
		$I_{C} = -1 \text{ A}$	[1] {	80	155	-	
		$I_C = -1.5 \text{ A}$	[1]	70	140	-	
		$I_C = -2 A$	[1]	60	120	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -100 \text{ mA};$ $I_B = -20 \text{ mA}$	[1]	-	-25	-50	mV
		$I_C = -1 A$ ; $I_B = -200 \text{ mA}$	<u>[1]</u> .	-	-110	-190	mV
		$I_C = -1.5 \text{ A}; I_B = -300 \text{ mA}$	<u>[1]</u> .	-	-155	-270	mV
		$I_C = -2 \text{ A}; I_B = -400 \text{ mA}$	<u>[1]</u> .	-	-200	-350	mV
R <sub>CEsat</sub>	collector-emitter saturation resistance	$I_C = -2 \text{ A}; I_B = -400 \text{ mA}$	<u>[1]</u> .	-	100	175	mΩ
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = -2 \text{ A}; I_B = -400 \text{ mA}$	[1]	-	-1.0	-1.15	V
t <sub>d</sub>	delay time	$V_{CC} = -6 \text{ V}; I_C = -0.5 \text{ A};$		-	20	-	ns
t <sub>r</sub>	rise time	$I_{Bon} = -0.1 \text{ A}; I_{Boff} = 0.1 \text{ A}$		-	105	-	ns
t <sub>on</sub>	turn-on time			-	125	-	ns
t <sub>s</sub>	storage time			-	875	-	ns
t <sub>f</sub>	fall time			-	150	-	ns
t <sub>off</sub>	turn-off time		•	-	1025	-	ns
f <sub>T</sub>	transition frequency	$V_{CE} = -10 \text{ V};$ $I_E = -10 \text{ mA}; f = 100 \text{ MHz}$	-	-	35	-	MHz
C <sub>c</sub>	collector capacitance	$V_{CB} = -20 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz	•	•	30	-	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = -0.5 \text{ V};$ $I_C = i_c = 0 \text{ A}; f = 1 \text{ MHz}$	•	-	530	-	pF

<sup>[1]</sup> Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

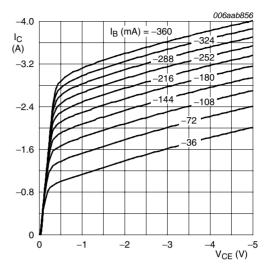
**Product data sheet** 



$$V_{CE} = -10 \text{ V}$$

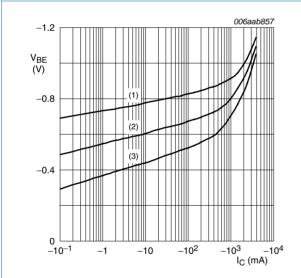
- (1)  $T_{amb} = 100 \, ^{\circ}C$
- (2)  $T_{amb} = 25 \, ^{\circ}C$
- (3)  $T_{amb} = -55 \, ^{\circ}C$

Fig 4. DC current gain as a function of collector current; typical values



T<sub>amb</sub> = 25 °C

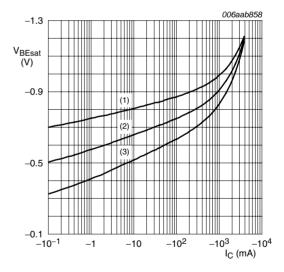
Fig 5. Collector current as a function of collector-emitter voltage; typical values





- (1)  $T_{amb} = -55 \,^{\circ}C$
- (2)  $T_{amb} = 25 \, ^{\circ}C$
- (3)  $T_{amb} = 100 \, ^{\circ}C$

Fig 6. Base-emitter voltage as a function of collector current; typical values

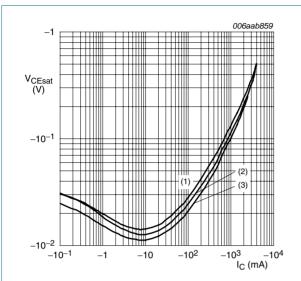


 $I_{\rm C}/I_{\rm B} = 5$ 

- (1)  $T_{amb} = -55 \, ^{\circ}C$
- (2)  $T_{amb} = 25 \, ^{\circ}C$
- (3)  $T_{amb} = 100 \, ^{\circ}C$

Fig 7. Base-emitter saturation voltage as a function of collector current; typical values

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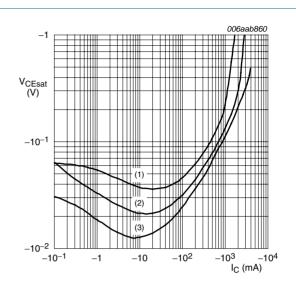
$$I_C/I_B = 5$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig 8. Collector-emitter saturation voltage as a function of collector current; typical values

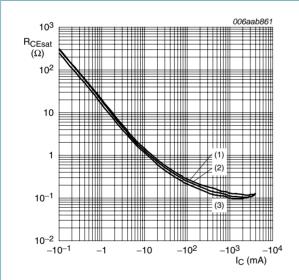


(1) 
$$I_C/I_B = 20$$

(2) 
$$I_C/I_B = 10$$

(3) 
$$I_C/I_B = 5$$

Fig 9. Collector-emitter saturation voltage as a function of collector current; typical values



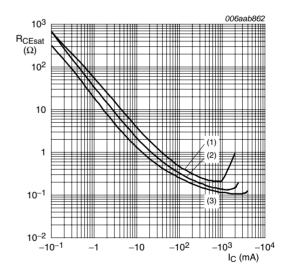
$$I_{\rm C}/I_{\rm B} = 5$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55 \,^{\circ}C$$

Fig 10. Collector-emitter saturation resistance as a function of collector current; typical values



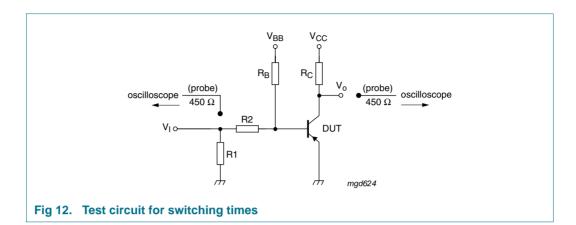
(1) 
$$I_C/I_B = 20$$

(2) 
$$I_C/I_B = 10$$

(3) 
$$I_C/I_B = 5$$

Fig 11. Collector-emitter saturation resistance as a function of collector current; typical values

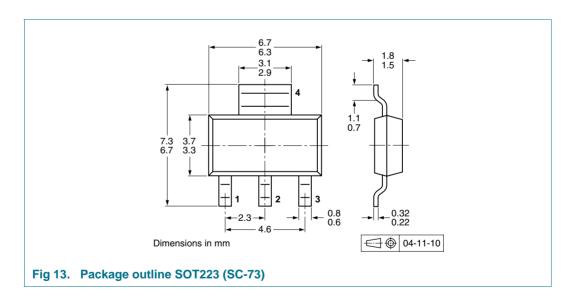
#### 8. Test information



## 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 9. Package outline



# 10. Packing information

Table 8. Packing methods

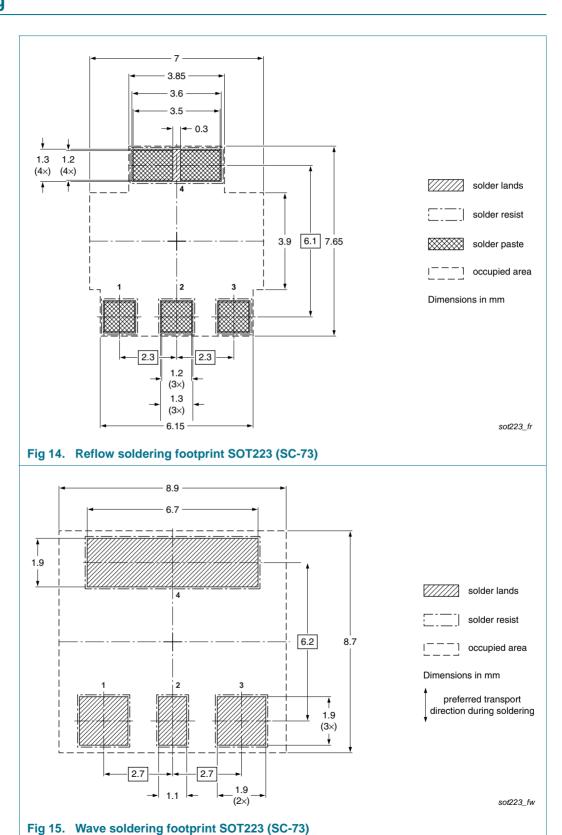
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description Packing qua		<b>Juantity</b>
			1000	4000
PBHV9215Z	SOT223	8 mm pitch, 12 mm tape and reel	-115	-135

[1] For further information and the availability of packing methods, see Section 14.

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## 11. Soldering





# 12. Revision history

#### Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PBHV9215Z_1	20091211	Product data sheet	-	-

**PBHV9215Z** 

#### 150 V, 2 A PNP high-voltage low V<sub>CEsat</sub> (BISS) transistor

## 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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## 150 V, 2 A PNP high-voltage low V<sub>CEsat</sub> (BISS) transistor

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