

Product Summary

V _{RRM} (V)	I _O (A)	V _F Max (V) @ +25°C	I _R Max (mA) +25°C
60	2	0.51	0.15

Description and Applications

The SBR2U60S1F is a single rectifier packaged in SOD123F(Standard). Offering low V_F, low power loss and high efficiency, this device is ideal for use in general rectification applications as a:

- Boost Diode
- Blocking Diode

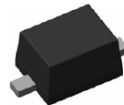
Features and Benefits

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Patented Super Barrier Rectifier SBR[®] Technology
- **Qualified to AEC-Q101 Standards for High Reliability**
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **An Automotive-Compliant Part is Available Under Separate Datasheet ([SBR2U60S1FQ](#))**

Mechanical Data

- Case: SOD123F (Standard)
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Polarity: Cathode Band
- Weight: 0.015 grams (Approximate)

SOD123F (Standard)



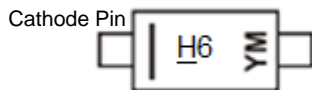
Top View

Ordering Information (Note 4)

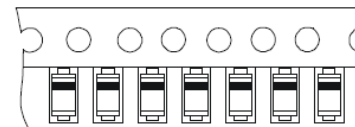
Part Number	Case	Packaging
SBR2U60S1F-7	SOD123F (Standard)	3,000/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated’s definitions of Halogen- and Antimony-free, “Green” and Lead-free.
 3. Halogen- and Antimony-free “Green” products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



H6 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: F = 2018)
 M = Month (ex: 9 = September)
 Bar Denotes Cathode Pin



Date Code Key

Year	2013	2014	2015	2016	2017	2018	2019	2020
Code	A	B	C	D	E	F	G	H

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	60	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _{RM}		
Average Rectified Output Current	I _O	2	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	35	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Case (Note 5)	R _{θJC}	30	°C/W
Typical Thermal Resistance Junction to Ambient (Note 5)	R _{θJA}	88	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V _{(BR)R}	60	—	—	V	I _R = 1.0mA
Forward Voltage Drop	V _F	—	0.37	0.46	V	I _F = 1A, T _J = +25°C
		—	0.44	0.51		I _F = 2A, T _J = +25°C
		—	0.42	—		I _F = 2A, T _J = +125°C
Leakage Current (Note 6)	I _R	—	20	—	μA	V _R = 10V, T _J = +25°C
		—	50	150	μA	V _R = 60V, T _J = +25°C
		—	6.5	—	mA	V _R = 60V, T _J = +125°C
Total Capacitance	C _T	—	75	—	pF	V _R = 10V, f = 1MHz
Reverse Recovery Time	t _{RR}	—	11	—	ns	I _F = 0.5A, I _R = 1A, I _{RR} = 0.25A

Notes: 5. Device mounted on FR-4 substrate, 1.0"×1.0", 2oz, single-sided, PC boards with 0.2"×0.25" copper pad.
6. Short duration pulse test used to minimize self-heating effect.

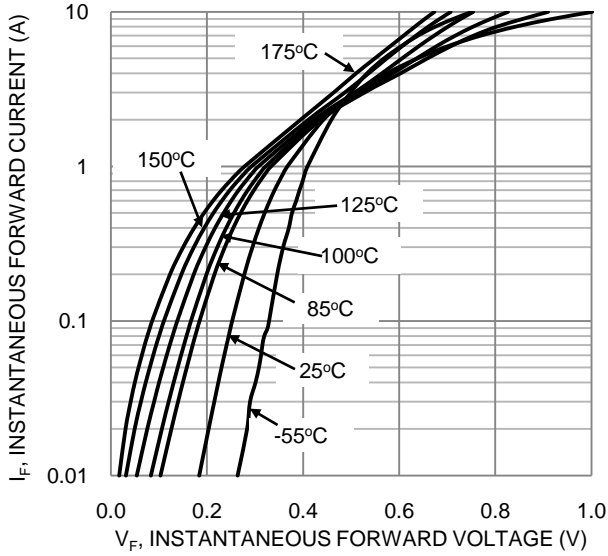


Figure 1. Typical Forward Characteristics

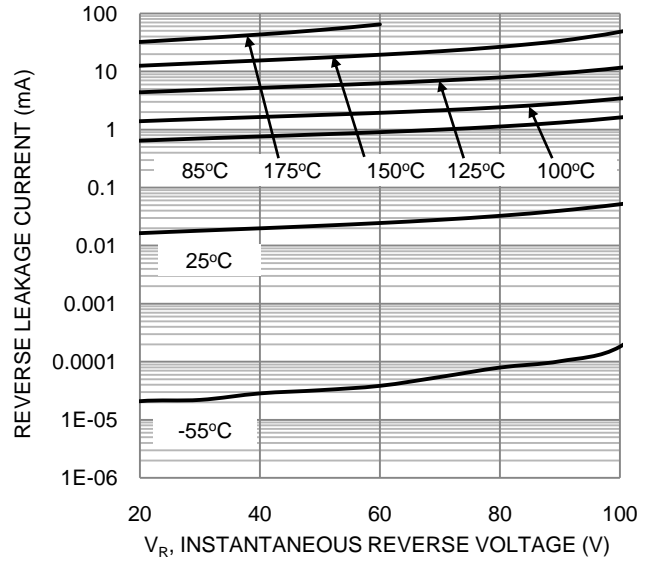


Figure 2. Typical Reverse Characteristics

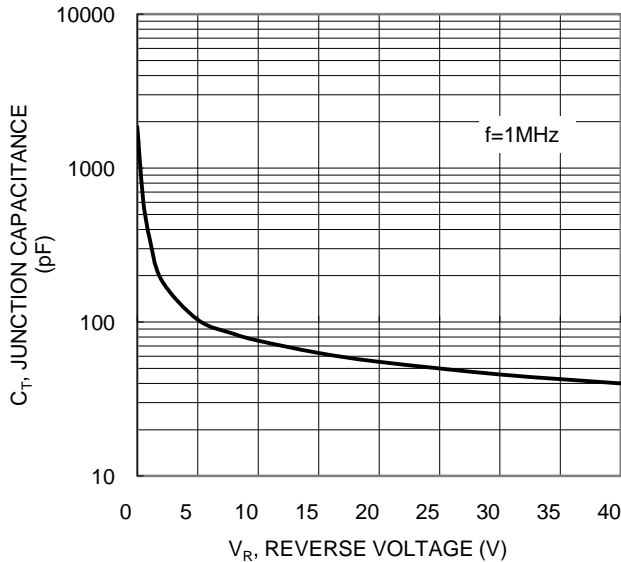


Figure 3. Typical Junction Capacitance

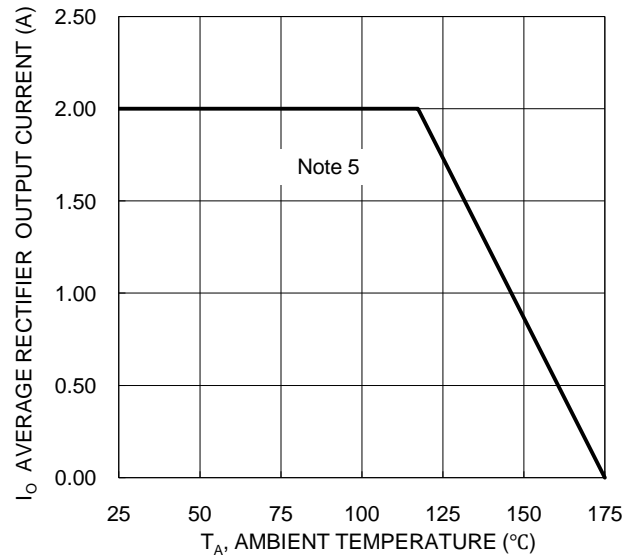


Figure 4. DC Forward Current Derating

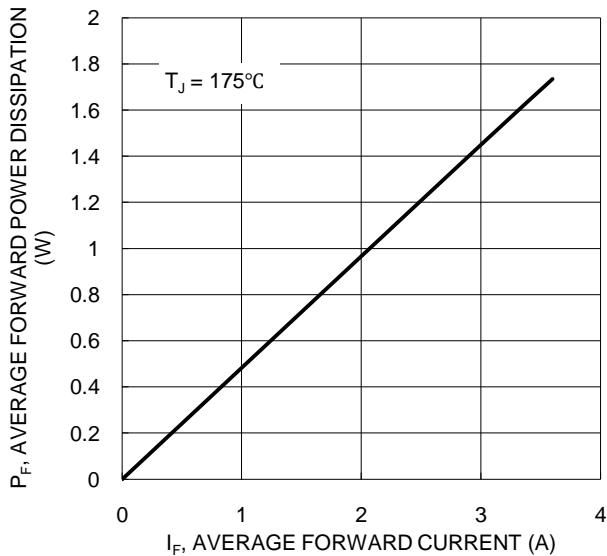
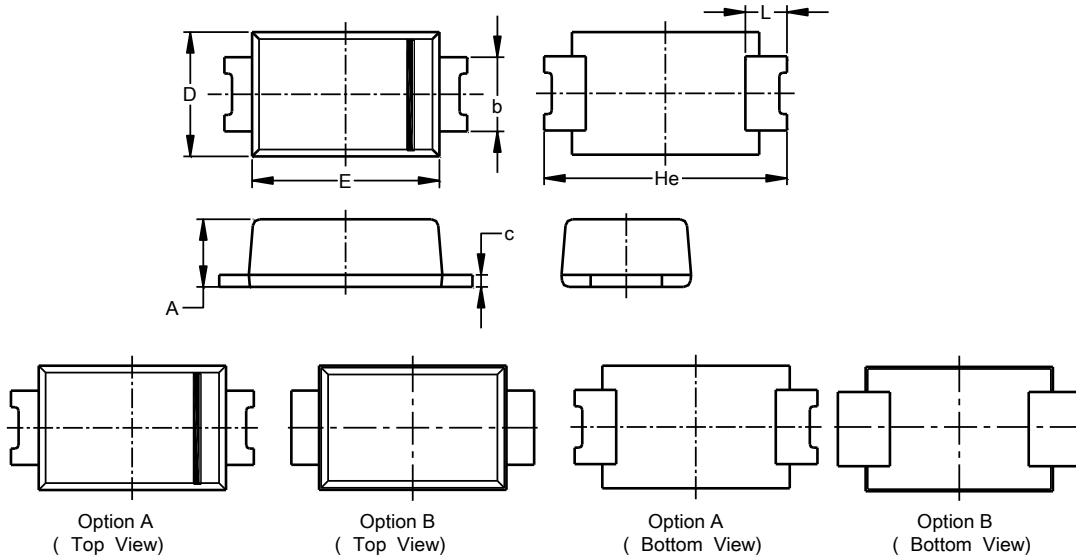


Figure 5. Forward Power Dissipation

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOD123F (Standard)

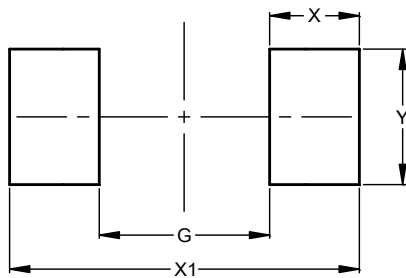


SOD123F (Standard)			
Dim	Min	Max	Typ
A	0.81	1.15	-
b	0.80	1.35	-
c	0.05	0.30	-
D	1.70	1.90	1.80
E	2.60	2.80	2.70
He	3.30	3.70	3.50
L	0.35	0.85	-
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOD123F (Standard)



Dimensions	Value (in mm)
G	1.90
X	1.00
X1	3.90
Y	1.50

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