

OPTICALLY COUPLED BILATERAL SWITCH LIGHT ACTIVATED ZERO VOLTAGE CROSSING TRIAC



APPROVALS

• UL recognised, File No. E91231 Package System " TT "

'X'SPECIFICATIONAPPROVALS

- VDE 0884 in 3 available lead form : -
 - STD
 - G form
 - SMD approved to CECC 00802

DESCRIPTION

The MOC303_ Series are optically coupled isolators consisting of a Gallium Arsenide infrared emitting diode coupled with a monolithic silicon detector performing the functions of a zero crossing bilateral triac mounted in a standard 6 pin dual-in-line package.

FEATURES

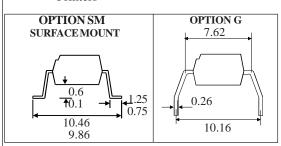
- Options:10mm lead spread add G after part no.
 Surface mount add SM after part no.
- High Isolation Voltage $(5.3kV_{RMS}, 7.5kV_{PK})$

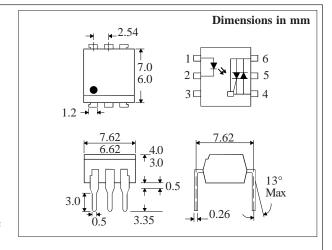
Tape&reel - add SMT&R after part no.

- Zero Voltage Crossing
- 250V Peak Blocking Voltage
- All electrical parameters 100% tested
- Custom electrical selections available

APPLICATIONS

- CRTs
- Power Triac Driver
- Motors
- Consumer appliances
- Printers





ABSOLUTE MAXIMUM RATINGS (25 °C unless otherwise noted)

Storage Temperature	-55°C-+150°C
Operating Temperature	$-40^{\circ}\text{C} - +100^{\circ}\text{C}$
Lead Soldering Temperature	260°C
(1.6mm from case for 10 seconds)	

INPUTDIODE

Forward Current	50mA			
Reverse Voltage	. 6V			
Power Dissipation	120mW			
(derate linearly 1.41mW/°C above 25°C)				

OUTPUT PHOTO TRIAC

Off-State Output Terminal Voltage 250V
Forward Current (Peak)1A
Power Dissipation 150mW
(derate linearly 1.76mW/°C above 25°C)

POWER DISSIPATION

(1 . 1: 1 0.04 \$\text{\$MY}(00 1 0.500)	Total Power Dissipation	250mW
(derate linearly 2.94mW/°C above 25°C)	(derate linearly 2.94mW	'/°C above 25°C)

ISOCOMCOMPONENTS 2004 LTD

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1777/08 DB92006

ELECTRICAL CHARACTERISTICS ($\rm T_{_{A}}$ = 25°C Unless otherwise noted)

	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITION
Input	Forward Voltage (V_F) Reverse Current (I_R)		1.2	1.5 10	V μA	$I_{\rm F} = 20 \text{mA}$ $V_{\rm R} = 6 \text{V}$
Output	$\begin{array}{c} \text{Peak Off-state Current (I}_{\text{DRM}}) \\ \text{Peak Blocking Voltage (V}_{\text{DRM}}) \\ \text{On-state Voltage (V}_{\text{TM}}) \end{array}$	250		500	nA V V	$V_{DRM} = 250 V \text{ (note 1)}$ $I_{DRM} = 500 nA$ $I_{TM} = 100 mA \text{ (peak)}$
	Critical rate of rise of off-state Voltage (dv/dt)	600	1500		V/µs	
Coupled	Input Current to Trigger (I _{FT})(note 2) MOC3030 MOC3031 MOC3032 MOC3033			30 15 10 5	mA mA mA	$V_{TM} = 3V \text{ (note 2)}$
	$\label{eq:current} \begin{array}{l} \mbox{Holding Current , either direction (I}_{\mbox{\tiny H}}) \\ \mbox{Input to Output Isolation Voltage V}_{\mbox{\tiny ISO}} \end{array}$	5300 7500	400		$\begin{array}{c} \mu A \\ V_{_{RMS}} \\ V_{_{PK}} \end{array}$	See note 3 See note 3
Zero Crossing Charact-	Inhibit Voltage (V_{IH})			20	V	I _F = Rated I _{FT} MT1-MT2 Voltage above which device
-eristic	Leakage in Inhibited State (I_S)			500	μΑ	will not trigger $I_F = Rated I_{FT}$ $V_{DRM} = 250V$ off-state

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Note 1. Test voltage must be applied within dv/dt rating. Note 2. Guaranteed to trigger at an I_F value less than or equal to max. I_{FT} , recommended I_F lies between Rated I_{FT} and absolute max. I_F . Note 3. Measured with input leads shorted together and output leads shorted together.