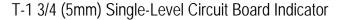


# WP59BL/EGW



## **DESCRIPTIONS**

- The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode
- The Green source color devices are made with Gallium Phosphide Green Light Emitting Diode

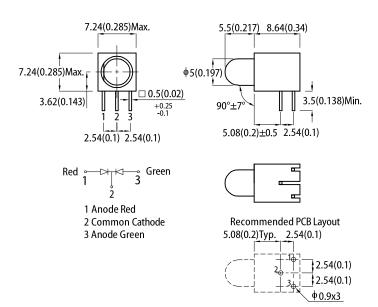
## **FEATURES**

- · Pre-trimmed leads for pc board mounting
- · Black case enhances contrast ratio
- · High reliability-life measured in years
- Housing UL rating: 94V-0
- Housing material: Type 66 nylon
- RoHS compliant

## **APPLICATIONS**

- · Status indicator
- Illuminator
- Signage applications
- · Decorative and entertainment lighting
- · Commercial and residential architectural lighting

# **PACKAGE DIMENSIONS**



#### Notes

- All dimensions are in millimeters (inches).
   Tolerance is ±0.25(0.01") unless otherwise noted.
- Lead spacing is measured where the leads emerge from the package.
   The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice

## **SELECTION GUIDE**

Part Number	Emitting Color (Material)	Lens Type	lv (mcd) @ 20mA [2]		Viewing Angle [1]	
			Min.	Тур.	201/2	
WP59BL/EGW	■ High Efficiency Red (GaAsP/GaP)	White Diffused	30	60		
			*20	*40	30°	
	Green (GaP)		20	60		
			*20	*60		

Notes:

1. 61/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.

2. Luminous intensity / luminous flux: +/-15%.

\* Luminous intensity value is traceable to CIE127-2007 standards.





# ELECTRICAL / OPTICAL CHARACTERISTICS at T<sub>A</sub>=25°C

Doubleston	Symbol	Funithing Colon	Value		l lmi4
Parameter		Emitting Color	Тур.	Max.	Unit
Wavelength at Peak Emission I <sub>F</sub> = 20mA	$\lambda_{peak}$	High Efficiency Red Green	627 565	-	nm
Dominant Wavelength I <sub>F</sub> = 20mA	$\lambda_{dom}$ [1]	High Efficiency Red Green	617 568	-	nm
Spectral Bandwidth at 50% $\Phi$ REL MAX I <sub>F</sub> = 20mA	Δλ	High Efficiency Red Green	45 30	-	nm
Capacitance	С	High Efficiency Red Green	15 15	-	pF
Forward Voltage I <sub>F</sub> = 20mA	V <sub>F</sub> <sup>[2]</sup>	High Efficiency Red Green	2.0 2.2	2.5 2.5	V
Reverse Current (V <sub>R</sub> = 5V)	I <sub>R</sub>	High Efficiency Red Green	-	10 10	μΑ
Temperature Coefficient of $\lambda_{\text{peak}}$ $I_F$ = 20mA, -10°C $\leq T \leq 85^{\circ}C$	$TC_{\lambdapeak}$	High Efficiency Red Green	0.13 0.1	-	nm/°C
Temperature Coefficient of $\lambda_{dom}$ $I_F$ = 20mA, -10°C $\leq T \leq 85^{\circ}C$	$TC_{\lambdadom}$	High Efficiency Red Green	0.06 0.06	-	nm/°C
Temperature Coefficient of $V_F$ $I_F$ = 20mA, -10°C $\leq$ T $\leq$ 85°C	TC <sub>V</sub>	High Efficiency Red Green	-1.9 -2	-	mV/°C

#### Notes:

# ABSOLUTE MAXIMUM RATINGS at T<sub>A</sub>=25°C

	Symbol	Valu			
Parameter		High Efficiency Red	Green	Unit	
Power Dissipation	$P_D$	75	62.5	mW	
Reverse Voltage	$V_R$	5	5	V	
Junction Temperature	T <sub>j</sub>	125	110	°C	
Operating Temperature	T <sub>op</sub>	-40 to +85		°C	
Storage Temperature	T <sub>stg</sub>	-40 to +85		°C	
DC Forward Current	I <sub>F</sub>	30	25	mA	
Peak Forward Current	I <sub>FM</sub> <sup>[1]</sup>	160	140	mA	
Electrostatic Discharge Threshold (HBM)	-	8000	8000	V	
Thermal Resistance (Junction / Ambient)	R <sub>th JA</sub> [2]	550	600	°C/W	
Thermal Resistance (Junction / Solder point)	R <sub>th JS</sub> <sup>[2]</sup>	300	420	°C/W	
Lead Solder Temperature [3]		260°C For 3 Seconds			
Lead Solder Temperature [4]		260°C For 5 Seconds			

Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. R<sub>ib. M.</sub> R<sub>ib. M.</sub> Results from mounting on PC board FR4 (pad size ≥ 16 mm² per pad).
3. 2mm below package base.
4. 5mm below package base.
5. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.



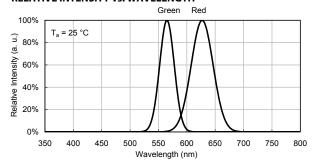
<sup>1.</sup> The dominant wavelength ( $\lambda d$ ) above is the setup value of the sorting machine. (Tolerance  $\lambda d:\pm 1$ nm.)

<sup>2.</sup> Forward voltage: ±0.1V.
3. Wavelength value is traceable to CIE127-2007 standards.
4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

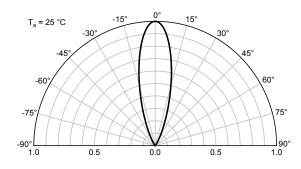


## **TECHNICAL DATA**

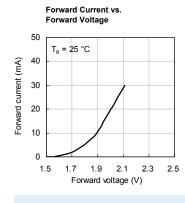
### **RELATIVE INTENSITY vs. WAVELENGTH**

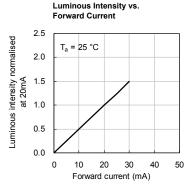


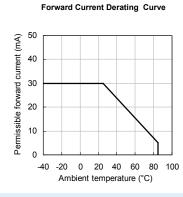
## **SPATIAL DISTRIBUTION**

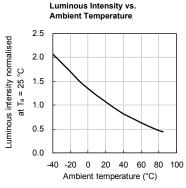


# **HIGH EFFICIENCY RED**

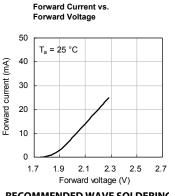


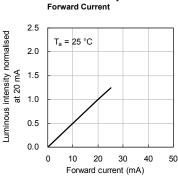




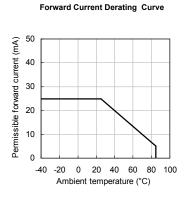


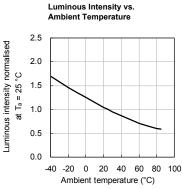
### **GREEN**



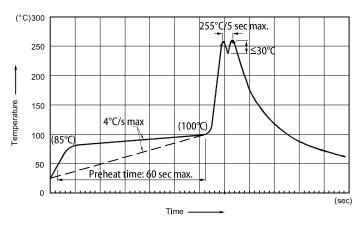


Luminous Intensity vs.





## **RECOMMENDED WAVE SOLDERING PROFILE**

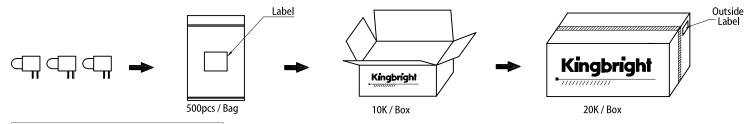


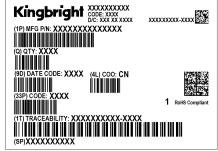
- 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
- 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
  3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
  4. Fixtures should not incur stress on the component when mounting and during soldering process.

- 5. SAC 305 solder alloy is recommended.6. No more than one wave soldering pass



## **PACKING & LABEL SPECIFICATIONS**





#### **PRECAUTIONS**

## **Storage Conditions**

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature ≤ 30°C and relative humidity < 60%.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

## **LED Mounting Method**

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.

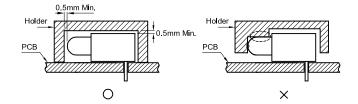
Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

#### <u>Hous</u>ing **Hous**ing Housing Ō Note.2 0 0 0 LED Housing <u>LE</u>D Housing LED Housing Housing LED

Correct mounting method " x " Incorrect mounting method

## **Lead Forming Procedures**

- 1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during solderina.
- 2. The tip of the soldering iron should never touch the lens epoxy.
- 3. Through-hole LEDs are incompatible with reflow soldering.
- 4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



#### PRECAUTIONARY NOTES

- The information included in this document reflects representative usage scenarios and is intended for technical reference only
- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to
- the latest datasheet for the updated specifications.

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