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Vishay Semiconductors

COMPLIANT

HALOGEN

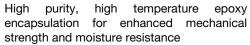
## **High Performance Schottky Rectifier, 8 A**

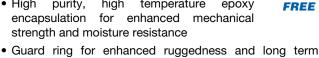


PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	8 A							
V <sub>R</sub>	80 V, 100 V							
V <sub>F</sub> at I <sub>F</sub>	0.58 V							
I <sub>RM</sub>	7 mA at 125 °C							
T <sub>J</sub> max.	175 °C							
E <sub>AS</sub>	7.5 mJ							
Package	D <sup>2</sup> PAK (TO-263AB)							
Circuit configuration	Single							

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Low forward voltage drop
- High frequency operation





- reliability Meets MSL level 1, per J-STD-020, LF maximum peak
- of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION**

The VS-8TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I <sub>F(AV)</sub>	Rectangular waveform	8	A					
V <sub>RRM</sub>	Range	80/100	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	A					
V <sub>F</sub>	8 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.58	V					
TJ	Range	-55 to +175	°C					

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-8TQ080S-M3	VS-8TQ100S-M3	UNITS				
Maximum DC reverse voltage	$V_{R}$	80	100	V				
Maximum working peak reverse voltage	$V_{RWM}$	80	100	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS					
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 157 °C	8	А				
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load	850				
non-repetitive surge current See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	230	Α			
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.50 A, L = 60 mH		7.50	mJ			
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5$ x $V_R$ typical		0.50	А			



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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		8 A	T <sub>.1</sub> = 25 °C	0.72	V			
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	16 A	1j=25 C	0.88				
See fig. 1	VFM (1)	8 A	T <sub>.1</sub> = 125 °C	0.58				
		16 A	1] = 123 0	0.69				
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	0.55	mA			
See fig. 2	IRM (")	T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	7	IIIA			
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz), 25 °C		500	pF			
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 m	8	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C			
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4	2.0	°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth, and greased	0.50	C/VV			
Approximate weight				2	g			
Approximate weight				0.07	OZ.			
Manustinantanan	minimum			6 (5)	kgf · cm			
Mounting torque	maximum			12 (10)	(lbf · in)			
Marking device			Case style D <sup>2</sup> PAK (TO-263AB)	8TQ(	080S			
			Case style D-PAR (10-203AB)	8TQ	100S			

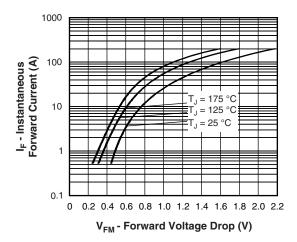


Fig. 1 - Maximum Forward Voltage Drop Characteristics

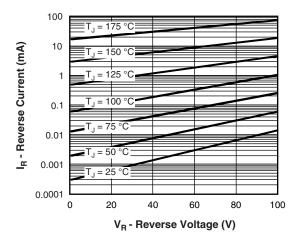


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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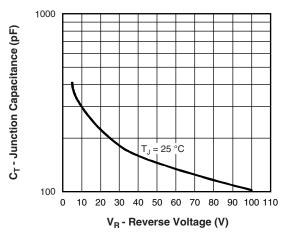


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

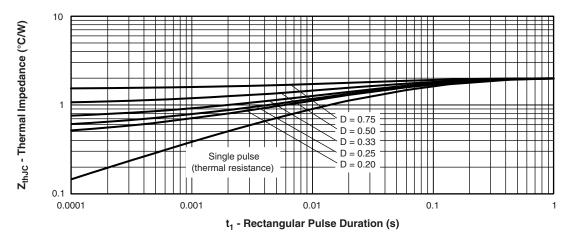


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

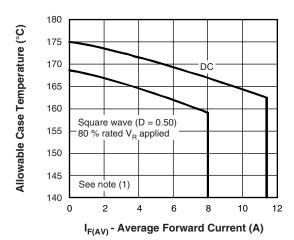


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

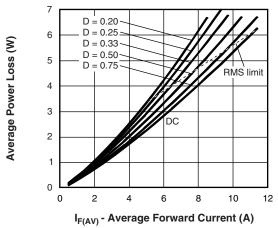
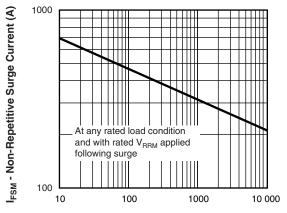


Fig. 6 - Forward Power Loss Characteristics

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t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

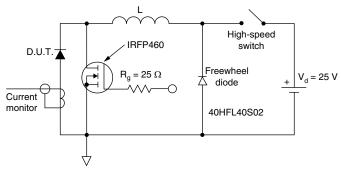
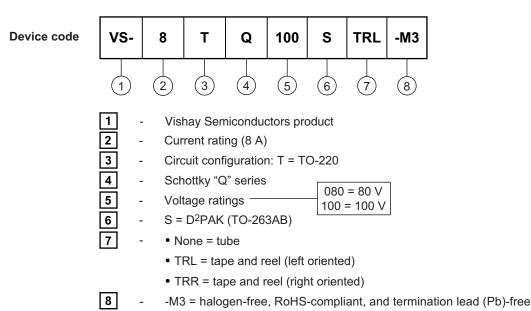


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

#### **ORDERING INFORMATION TABLE**





# VS-8TQ080S-M3, VS-8TQ100S-M3

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ORDERING INFORMATION										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-8TQ080S-M3	50	1000	Antistatic plastic tubes							
VS-8TQ080STRR-M3	800	800	13" diameter reel							
VS-8TQ080STRL-M3	800	800	13" diameter reel							
VS-8TQ100S-M3	50	1000	Antistatic plastic tubes							
VS-8TQ100STRR-M3	800	800	13" diameter reel							
VS-8TQ100STRL-M3	800	800	13" diameter reel							

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96164					
Part marking information	www.vishay.com/doc?95444					
Packaging information	www.vishay.com/doc?96424					
SPICE model	www.vishay.com/doc?96227					



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## D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	BOL MILLIMETERS INCHES NOTES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES				
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



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