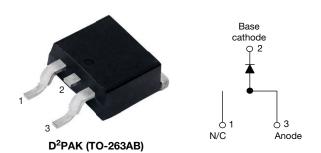
## VS-6TQ35S-M3, VS-6TQ40S-M3, VS-6TQ45S-M3

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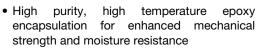
## High Performance Schottky Rectifier, 6 A



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub> 6 A						
$V_{R}$	35 V, 40 V, 45 V					
V <sub>F</sub> at I <sub>F</sub>	0.53 V					
I <sub>RM</sub>	7 mA at 125 °C					
T <sub>J</sub> max.	175 °C					
E <sub>AS</sub>	8 mJ					
Package	D <sup>2</sup> PAK (TO-263AB)					
Circuit configuration	Single					

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- High frequency operation
- Low forward voltage drop





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION**

The VS-6TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES								
I <sub>F(AV)</sub>	Rectangular waveform	6	Α					
$V_{RRM}$	Range	35 to 45	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	690	Α					
V <sub>F</sub>	6 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.53	V					
TJ	Range	-55 to +175	°C					

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-6TQ035S-M3	VS-6TQ040S-M3	VS-6TQ045S-M3	UNITS
Maximum DC reverse voltage	$V_R$	35	40	45	W
Maximum working peak reverse voltage	$V_{RWM}$	33	40	45	V

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS			
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 164 °C	6					
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse Following any rated load		690	Α			
non-repetitive surge current See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	140	ì			
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.20 A, L = 11.10 mH		8	mJ			
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero Frequency limited by $T_J$ maxim	1.20	А				

# VS-6TQ35S-M3, VS-6TQ40S-M3, VS-6TQ45S-M3

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST (	TEST CONDITIONS					
Maximum forward voltage drop See fig. 1		6 A	T <sub>.1</sub> = 25 °C	0.60				
	V (1)	12 A	IJ=25 C	0.73	V			
	V <sub>FM</sub> <sup>(1)</sup>	6 A	T 105 %C	0.53				
		12 A	T <sub>J</sub> = 125 °C	0.64				
Maximum reverse leakage current	ı (1)	T <sub>J</sub> = 25 °C	V Dated V	0.8	mA			
See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	7				
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		0.35	V			
Forward slope resistance	r <sub>t</sub>			18.23	mΩ			
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal r	400	pF				
Typical series inductance	L <sub>S</sub>	Measured lead to lead	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width  $<300~\mu s,$  duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C			
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4	2.2				
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth, and greased	0.50	°C/W			
Approximate weight				2	g			
Approximate weight				0.07	OZ.			
Manustina taurus	minimum			6 (5)	kgf · cm			
Mounting torque	maximum			12 (10)	(lbf·in)			
				6TQ	035S			
Marking device			Case style D <sup>2</sup> PAK (TO-263AB)	6TQ	040S			
				6TQ	045S			

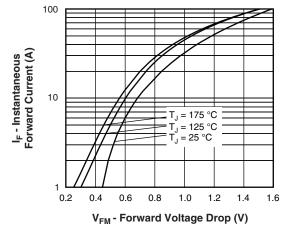


Fig. 1 - Maximum Forward Voltage Drop Characteristics

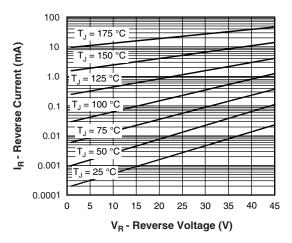


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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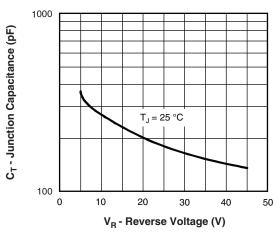


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

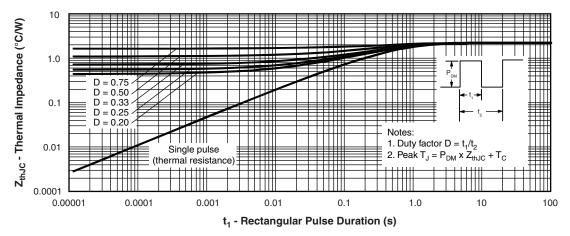


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

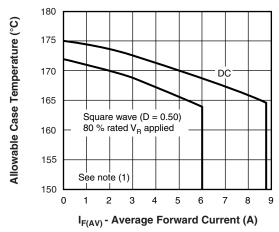


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

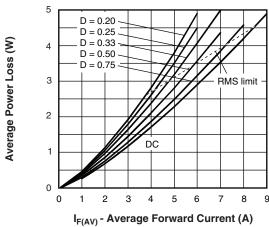
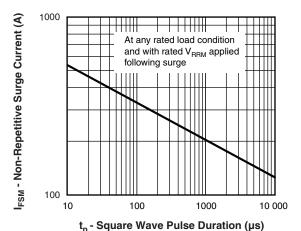


Fig. 6 - Forward Power Loss Characteristics

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Fig. 7 - Maximum Non-Repetitive Surge Current

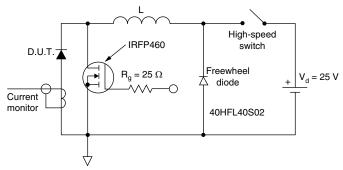
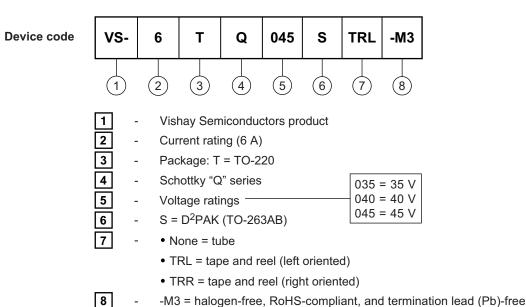


Fig. 8 - Unclamped Inductive Test Circuit

### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

#### **ORDERING INFORMATION TABLE**





# VS-6TQ35S-M3, VS-6TQ40S-M3, VS-6TQ45S-M3

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ORDERING INFORMAT	ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-6TQ035S-M3	50	1000	Antistatic plastic tubes							
VS-6TQ035STRR-M3	800	800	13" diameter reel							
VS-6TQ035STRL-M3	800	800	13" diameter reel							
VS-6TQ040S-M3	50	1000	Antistatic plastic tubes							
VS-6TQ040STRR-M3	800	800	13" diameter reel							
VS-6TQ040STRL-M3	800	800	13" diameter reel							
VS-6TQ045S-M3	50	1000	Antistatic plastic tubes							
VS-6TQ045STRR-M3	800	800	13" diameter reel							
VS-6TQ045STRL-M3	800	800	13" diameter reel							

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96164</u>					
Part marking information	www.vishay.com/doc?95444				
Packaging information	www.vishay.com/doc?96424				



## Vishay Semiconductors

## D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



### Vishay Semiconductors

### **TO-262**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.10	D BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.36	3.71	0.132	0.146	

### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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