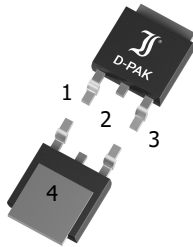


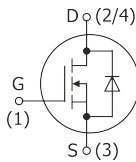
DI020N06D1 N-Channel Power MOSFET N-Kanal Leistungs-MOSFET	I_{D25°C} = 20 A R_{DS(on)} ~ 24 mΩ T_{jmax} = 175°C	V_{DSS} = 60 V P_D = 45 W E_{AS} = 72 mJ
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Version 2021-11-17

TO-252AA
D-PAK



SPICE Model & STEP File ¹⁾



Marking
Type / Typ

HS Code 85412100

Typical Applications

- DC/DC Converters
- Power Supplies
- DC Drives
- Synchronous Rectifiers
- Commercial / industrial grade
- Suffix -Q: AEC-Q101 compliant ¹⁾
- Suffix -AQ: in AEC-Q101 qualification ¹⁾

Features

- Advanced Trench Technology
- Low on state resistance
- Fast switching times
- Low gate charge
- Avalanche rated
- Compliant to RoHS (exemp. 7a), REACH, Conflict Minerals ¹⁾

Mechanical Data ¹⁾

- Taped and reeled 2500 / 13"
- Weight approx. 0.32 g
- Case material UL 94V-0
- Solder & assembly conditions 260°C/10s
- MSL 1



Typische Anwendungen

- Gleichstrom-Wandler
- Stromversorgung
- Gleichstrom-Antriebe
- Synchron-Gleichrichter
- Standardausführung
- Suffix -Q: AEC-Q101 konform ¹⁾
- Suffix -AQ: in AEC-Q101 Qualifikation ¹⁾

Besonderheiten

- Advanced Trench Technologie
- Niedriger Einschaltwiderstand
- Schnelle Schaltzeiten
- Niedrige Gate-Ladung
- Avalanche-Charakteristik
- Konform zu RoHS (Ausn. 7a)
- REACH, Konfliktmineralien ¹⁾

Mechanische Daten ¹⁾

- Gegurtet auf Rolle
- Gewicht ca.
- Gehäusematerial
- Löt- und Einbaubedingungen

Maximum ratings ²⁾

Grenzwerte ²⁾

		DI020N06D1	DI020N06D1-Q
Drain-Source voltage Drain-Source-Spannung	$V_{GS} = 0\text{ V (short)}$	V_{DSS}	60 V
Gate-Source-voltage continuous – Gate-Source-Spannung dauernd		V_{GSS}	± 20 V
Power dissipation – Verlustleistung	$T_C = 25^\circ\text{C}^3)$	P_{tot}	45 W 25 W
Drain current continuous Drainstrom dauernd	$T_C = 25^\circ\text{C}^3)$ $T_C = 100^\circ\text{C}^3)$	I_D	20 A 20 A 14 A 13 A
Peak Drain current – Drain-Spitzenstrom	⁴⁾	I_{DM}	60 A 50 A
Source current continuous Sourcestrom dauernd	$T_C = 25^\circ\text{C}^3)$	I_S	15 A 20 A
Peak Source current – Source-Spitzenstrom	$V_{GS} = 0\text{ V}, t_p = 10\text{ s}$	I_{SM}	20 A 50 A
Single pulse avalanche energy Einzelpuls Avalanche-Energie	(Fig. 1) $V_{DD} = 30\text{ V}, V_G = 10\text{ V}$ $L = 0.5\text{ mH}, R_G = 25\ \Omega$	E_{AS}	72 mJ tbd
Junction temperature – Sperrschichttemperatur		T_j	-55...+175°C -55...+150°C
Storage temperature – Lagerungstemperatur		T_S	-55...+175°C -55...+150°C

1 Please note the [detailed information on our website](#) or at the beginning of the data book
Bitte beachten Sie die [detaillierten Hinweise auf unserer Internetseite](#) bzw. am Anfang des Datenbuches
2 $T_A = 25^\circ\text{C}$, unless otherwise specified – $T_A = 25^\circ\text{C}$, wenn nicht anders angegeben
3 Measured at heat flange – Gemessen an der Kühlfahne
4 Pulse width refer to SOA diagram – Pulsbreite siehe SOA-Diagramm

Characteristics (static)
Kennwerte (statisch)

		$T_j = 25^\circ\text{C}$	Min.	Typ.	Max.
Drain-Source breakdown voltage – Drain-Source-Durchbruchspannung $I_D = 250 \mu\text{A}$ $V_{GS} = 0 \text{ V (short)}$		$V_{(BR)DSS}$	60 V	–	–
Drain-Source leakage current – Drain-Source Leckstrom $V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V (short)}$		I_{DSS}	–	–	1 μA
Gate-Body leakage current – Gate-Substrat Leckstrom $V_{GS} = \pm 20 \text{ V}$ $V_{DS} = 0 \text{ V (short)}$		I_{GSS}	–	–	$\pm 100 \text{ nA}$
Gate-Source threshold voltage – Gate-Source Schwellspannung $V_{GS} = V_{DS}$ $I_D = 250 \mu\text{A}$		$V_{GS(th)}$	1.2 V	1.6 V	2.5 V
Drain-Source on-state resistance – Drain-Source Einschaltwiderstand					
$V_{GS} = 10 \text{ V}$ $I_D = 20 \text{ A}$	DI020N06D1	$R_{DS(on)}$	–	24 m Ω	35 m Ω
$V_{GS} = 4.5 \text{ V}$ $I_D = 20 \text{ A}$			–	30 m Ω	40 m Ω
$V_{GS} = 10 \text{ V}$ $I_D = 15 \text{ A}$	DI020N06D1-Q	$R_{DS(on)}$	–	26 m Ω	34 m Ω
$V_{GS} = 4.5 \text{ V}$ $I_D = 10 \text{ A}$			–	–	38 m Ω

Preliminary
Vorläufig

Characteristics (dynamic)

Kennwerte (dynamisch)

				$T_j = 25^\circ\text{C}$	Min.	Typ.	Max.
Forward Transconductance – Übertragungsteilheit							
$V_{DS} = 5\text{ V}$	$I_D = 5\text{ A}$	DI020N06D1	g_{FS}	11 S	–	–	
$V_{DS} = 5\text{ V}$	$I_D = 15\text{ A}$	DI020N06D1-Q	g_{FS}	–	18 S	–	
Input Capacitance – Eingangskapazität							
$V_{DS} = 15\text{ V}$	$V_{GS} = 0\text{ V}$	$f = 1\text{ MHz}$	DI020N06D1	C_{iss}	–	590 pF	–
$V_{DS} = 30\text{ V}$	$V_{GS} = 0\text{ V}$	$f = 1\text{ MHz}$	DI020N06D1-Q	C_{iss}	–	1260 pF	–
Output Capacitance – Ausgangskapazität							
$V_{DS} = 15\text{ V}$	$V_{GS} = 0\text{ V}$	$f = 1\text{ MHz}$	DI020N06D1	C_{oss}	–	70 pF	–
$V_{DS} = 30\text{ V}$	$V_{GS} = 0\text{ V}$	$f = 1\text{ MHz}$	DI020N06D1-Q	C_{oss}	–	47 pF	–
Reverse Transfer Capacitance – Rückwirkungskapazität							
$V_{DS} = 15\text{ V}$	$V_{GS} = 0\text{ V}$	$f = 1\text{ MHz}$	DI020N06D1	C_{rss}	–	64 pF	–
$V_{DS} = 30\text{ V}$	$V_{GS} = 0\text{ V}$	$f = 1\text{ MHz}$	DI020N06D1-Q	C_{rss}	–	43 pF	–
Turn-On Delay & Rise Time – Einschaltverzögerung und Anstiegszeit							
$V_{DD} = 30\text{ V}$	$I_D = 2\text{ A}$	(Fig. 1)	DI020N06D1	$t_{d(on)}$	–	6 ns	–
$V_{GS} = 10\text{ V}$	$R_G = 3\ \Omega$			t_r	–	6 ns	–
$V_{DD} = 30\text{ V}$	$I_D = 15\text{ A}$	(Fig. 1)	DI020N06D1-Q	$t_{d(on)}$	–	11 ns	–
$V_{GS} = 10\text{ V}$	$R_G = 3.3\ \Omega$			t_r	–	27 ns	–
Turn-Off Delay Time & Fall Time – Ausschaltverzögerung und Abfallzeit							
$V_{DD} = 30\text{ VA}$	$I_D = 2$	(Fig. 1)	DI020N06D1	$t_{d(off)}$	–	17 ns	–
$V_{GS} = 0\text{ V}$	$R_G = 3\ \Omega$			t_f	–	3 ns	–
$V_{DD} = 30\text{ VA}$	$I_D = 15\text{ A}$	(Fig. 1)	DI020N06D1-Q	$t_{d(off)}$	–	10 ns	–
$V_{GS} = 0\text{ V}$	$R_G = 3.3\ \Omega$			t_f	–	2 ns	–
Total Gate Charge – Gesamte Gate-Ladung							
$V_{DD} = 30\text{ V}$	$I_D = 10\text{ A}$	$V_{GS} = 10\text{ V}$	DI020N06D1	Q_g	–	25.3 nC	–
$V_{DD} = 30\text{ V}$	$I_D = 15\text{ A}$	$V_{GS} = 10\text{ V}$	DI020N06D1-Q	Q_g	–	19 nC	–
$V_{DD} = 30\text{ V}$	$I_D = 15\text{ A}$	$V_{GS} = 4.5\text{ V}$		Q_g	–	8.7 nC	–
Gate-Source Charge – Gate-Source-Ladung							
$V_{DD} = 30\text{ V}$	$I_D = 10\text{ A}$	$V_{GS} = 10\text{ V}$	DI020N06D1	Q_{gs}	–	4.7 nC	–
$V_{DD} = 30\text{ V}$	$I_D = 15\text{ A}$	$V_{GS} = 10\text{ V}$	DI020N06D1-Q	Q_{gs}	–	4.3 nC	–
Gate-Drain Charge – Gate-Drain-Ladung							
$V_{DD} = 30\text{ V}$	$I_D = 10\text{ A}$	$V_{GS} = 10\text{ V}$	DI020N06D1	Q_{gd}	–	6.1 nC	–
$V_{DD} = 30\text{ V}$	$I_D = 15\text{ A}$	$V_{GS} = 10\text{ V}$	DI020N06D1-Q	Q_{gd}	–	2.6 nC	–
Intrinsic Gate resistance – Innerer Gatewiderstand							
$f = 1\text{ MHz}$	$D\text{ open}$		R_{Gi}	–	tbd Ω	–	

Fig. 1

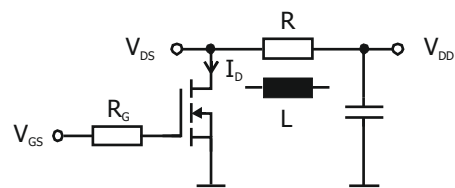
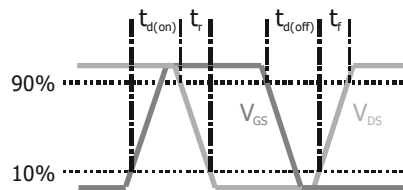
Test circuit for switching times (R) and avalanche energy (L)

("rise" and "fall" refer to I_D)

Testaufbau für Schaltzeiten (R)

und Avalanche-Energie (L)

("rise" and "fall" beziehen sich auf I_D)



Characteristics (diode)

Kennwerte (Diode)

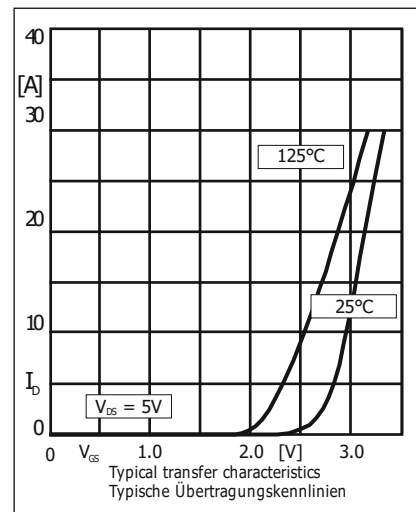
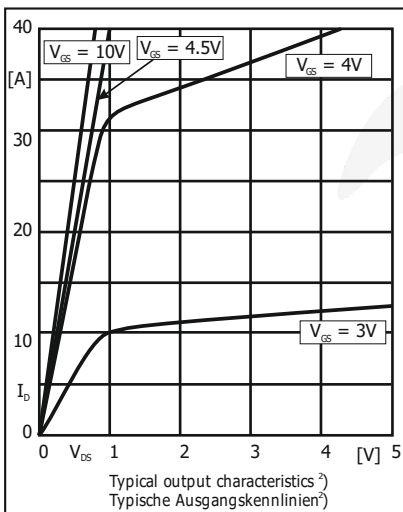
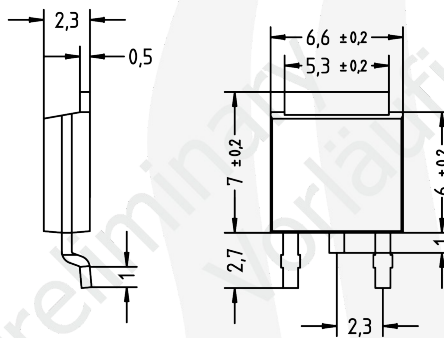
				$T_j = 25^\circ\text{C}$		
				Min.	Typ.	Max.
Forward voltage – Durchlass-Spannung						
$V_{GS} = 0\text{ V}$	$I_S = 20\text{ A}$	DI020N06D1	V_{SD}	–	–	1.2 V
$V_{GS} = 0\text{ V}$	$I_S = 1\text{ A}$	DI020N06D1-Q	V_{SD}	–	–	1.3 V
Reverse recovery time – Sperrverzugszeit						
$I_S = 20\text{ A}$,	$di/dt = -100\text{ A}/\mu\text{s}$	DI020N06D1	t_{rr}	–	30 ns	–
$I_S = 15\text{ A}$,	$di/dt = -100\text{ A}/\mu\text{s}$	DI020N06D1-Q	t_{rr}	–	7.2 ns	–
Reverse recovery charge – Sperrverzugsladung						
$I_S = 20\text{ A}$,	$di/dt = -100\text{ A}/\mu\text{s}$	DI020N06D1	Q_{rr}	–	50 nC	–
$I_S = 15\text{ A}$,	$di/dt = -100\text{ A}/\mu\text{s}$	DI020N06D1-Q	Q_{rr}	–	4.5 nC	–

Characteristics (thermal)

Kennwerte (thermisch)

				Min.	Typ.	Max.	
Thermal resistance junction to case Wärmewiderstand Sperrschicht – Gehäuse				R_{thc}	–	3.3 K/W ¹⁾	–

Dimensions – Maße [mm]



Disclaimer: See data book page 2 or [website](#)
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1 Measured at heat flange – Gemessen an der Kühlfahne

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