

BUK6Y24-40P 40 V, P-channel Trench MOSFET 9 April 2020

Product data sheet

1. General description

P-channel enhancement mode MOSFET in an LFPAK56 (Power SO8) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

This product has been designed and qualified to AEC-Q101 standard for use in high-performance automotive applications such as reverse battery protection.

2. Features and benefits

- High thermal power dissipation capability
- Suitable for thermally demanding environments due to 175 °C rating •
- Trench MOSFET technology •
- AEC-Q101 qualified

3. Applications

- Reverse battery protection
- Power management
- High-side load switch
- Motor drive

4. Quick reference data

Table 1. Quid	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-40	V
V _{GS}	gate-source voltage		[1]	-20	-	20	V
I _D	drain current	V _{GS} = -10 V; T _{mb} = 25 °C		-	-	-39	А
P _{tot}	total power dissipation	T _{mb} = 25 °C		-	-	66	W
Static chara	octeristics	·	•				
R _{DSon}	drain-source on-state resistance	V _{GS} = -10 V; I _D = -8.2 A; T _j = 25 °C		-	19	24	mΩ

[1] V_{GS} = -20 V/+5 V according AEC-Q101 at T_j = 175 °C; V_{GS} = -20 V/+20 V according AEC-Q101 at T_j = 150 °C

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5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	source	mb	D
2	S	source		
3	S	source	a	G (H
4	G	gate		s s
mb	D	mounting base; connected to drain	LFPAK56; Power- SO8 (SOT669)	017aaa094

6. Ordering information

Table 3. Ordering information								
Type number	Package	Package						
	Name	Description	Version					
BUK6Y24-40P	LFPAK56; Power-SO8	plastic, single-ended surface-mounted package; 4 terminals	SOT669					

7. Marking

Table 4. Marking codes	
Type number	Marking code
BUK6Y24-40P	6Y2440P

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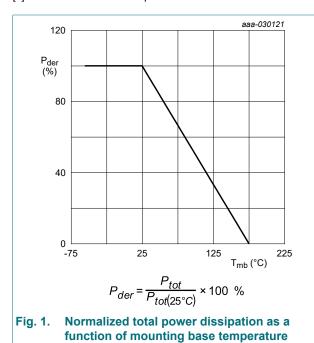
8. Limiting values

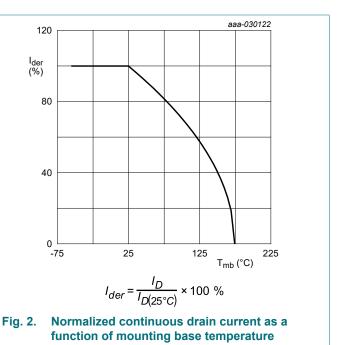
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

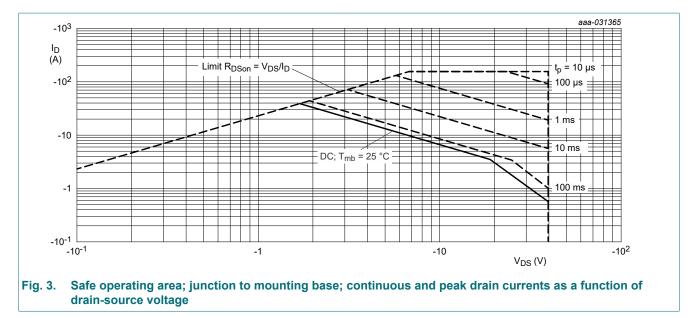
Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-40	V
V _{GS}	gate-source voltage	-	[1]	-20	20	V
I _D	drain current	V _{GS} = -10 V; T _{mb} = 25 °C		-	-39	А
		V _{GS} = -10 V; T _{mb} = 100 °C		-	-27	А
I _{DM}	peak drain current	single pulse; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	-155	А
P _{tot}	total power dissipation	T _{mb} = 25 °C		-	66	W
Tj	junction temperature			-55	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C
Source-drai	in diode					
Is	source current	T _{mb} = 25 °C		-	-39	А
I _{SM}	peak source current	single pulse; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$		-	-155	А
ESD maxim	num rating					
V _{ESD}	electrostatic discharge voltage	НВМ	[2]	-	500	V
Avalanche	ruggedness	1				
E _{DS(AL)S}	non-repetitive drain- source avalanche energy	$T_{j(init)}$ = 25 °C; I_D = -4.6 A; DUT in v avalanche (unclamped)		-	54	mJ

[1] V_{GS} = -20 V/+5 V according AEC-Q101 at T_j = 175 °C; V_{GS} = -20 V/+20 V according AEC-Q101 at T_j = 150 °C [2] Measured between all pins.



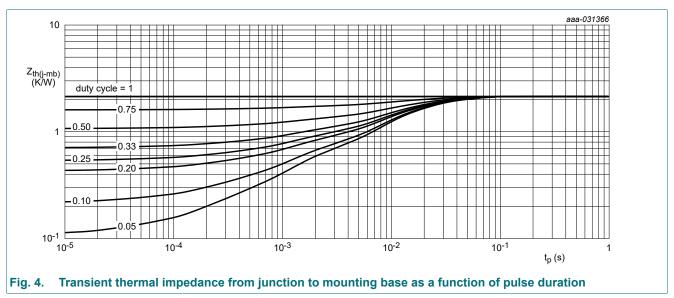


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9. Thermal characteristics

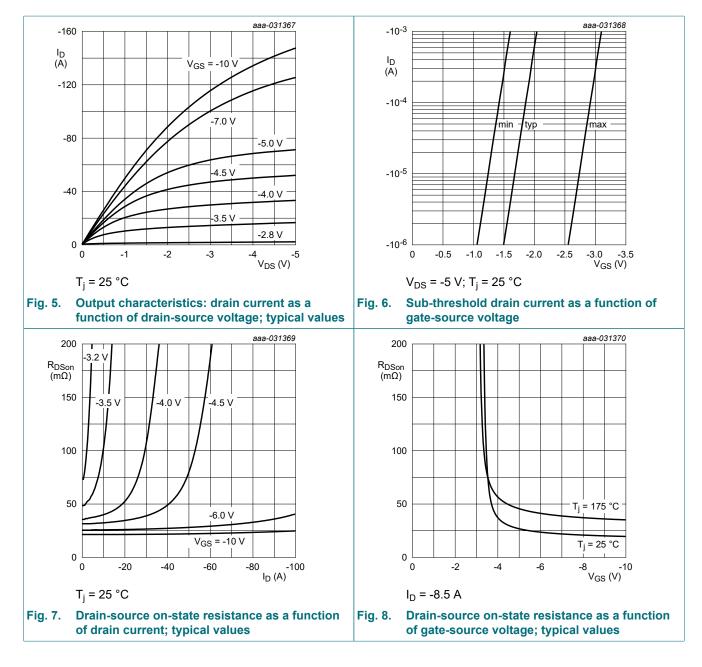
Table 6. Theri	mal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base		-	1.8	2.3	K/W



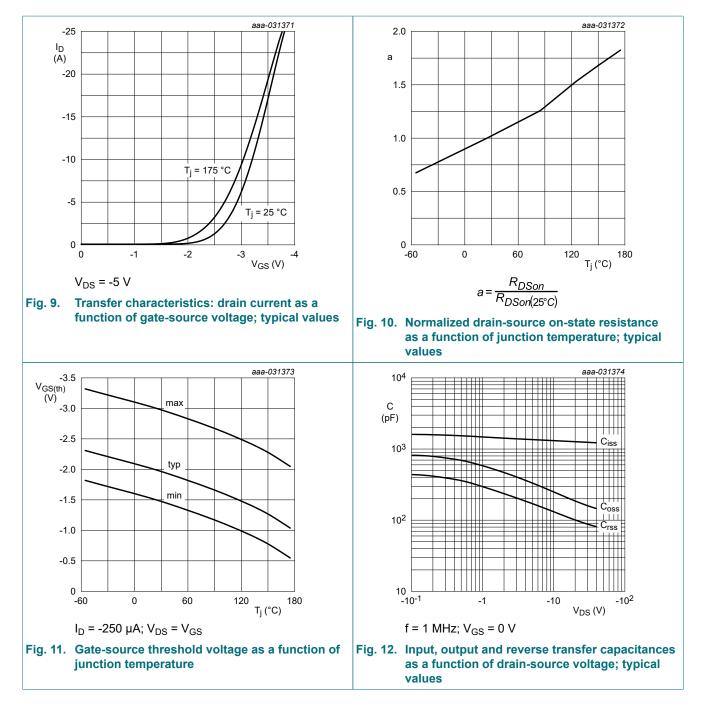
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	octeristics					
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C	-40	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = -250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	-1.5	-2	-3	V
I _{DSS} drain	drain leakage current	V _{DS} = -40 V; V _{GS} = 0 V; T _j = 25 °C	-	-	-1	μA
		V _{DS} = -40 V; V _{GS} = 0 V; T _j = 125 °C	-	-	-10	μA
I _{GSS}	gate leakage current	V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-100	nA
		V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	100	nA
R _{DSon}	drain-source on-state	V _{GS} = -10 V; I _D = -8.2 A; T _j = 25 °C	-	19	24	mΩ
	resistance	V _{GS} = -10 V; I _D = -8.2 A; T _j = 175 °C	-	35	44	mΩ
		V _{GS} = -4.5 V; I _D = -5.6 A; T _j = 25 °C	-	30	50	mΩ
9 _{fs}	forward transconductance	V _{DS} = -10 V; I _D = -4 A; T _j = 25 °C	-	14	-	S
R _G	gate resistance	f = 1 MHz	-	11	-	Ω
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	V _{DS} = -20 V; I _D = -8.2 A; V _{GS} = -10 V;	-	23	35	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	4	-	nC
Q _{GD}	gate-drain charge		-	5	-	nC
C _{iss}	input capacitance	V _{DS} = -20 V; f = 1 MHz; V _{GS} = 0 V;	-	1250	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	184	-	pF
C _{rss}	reverse transfer capacitance		-	100	-	pF
t _{d(on)}	turn-on delay time	V _{DS} = -20 V; I _D = -8.2 A; V _{GS} = -10 V;	-	7	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	25	-	ns
t _{d(off)}	turn-off delay time		-	50	-	ns
t _f	fall time		-	450	-	ns
Source-drai	n diode		I		-	
V _{SD}	source-drain voltage	I _S = -39 A; V _{GS} = 0 V; T _j = 25 °C	-	-0.7	-1.2	V
t _{rr}	reverse recovery time	I _S = -39 A; dI _S /dt = 100 A/µs;	-	21	-	ns
Q _r	recovered charge	V _{GS} = -10 V; V _{DS} = -20 V; T _j = 25 °C	-	18	-	nC

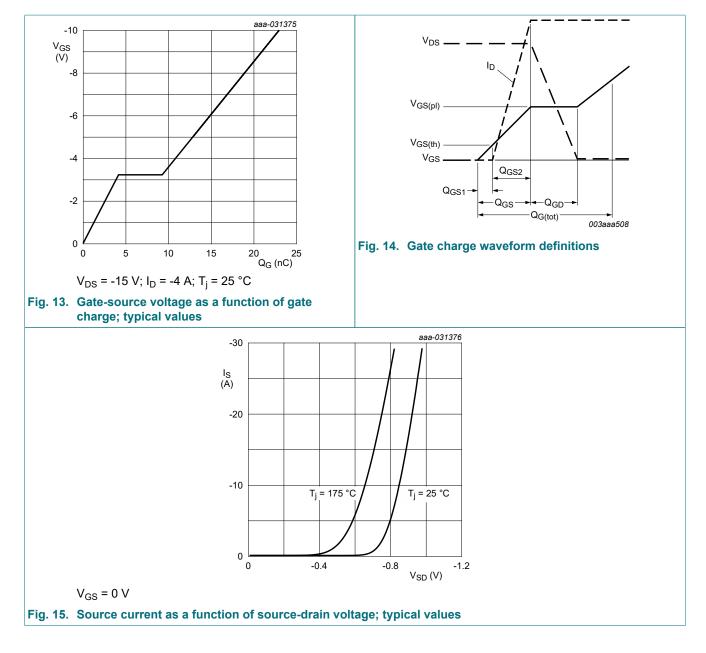
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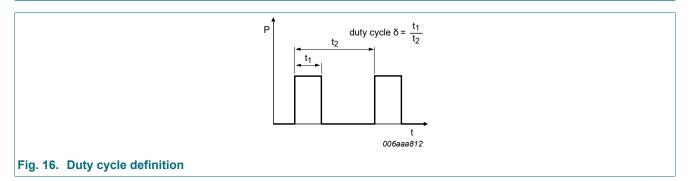
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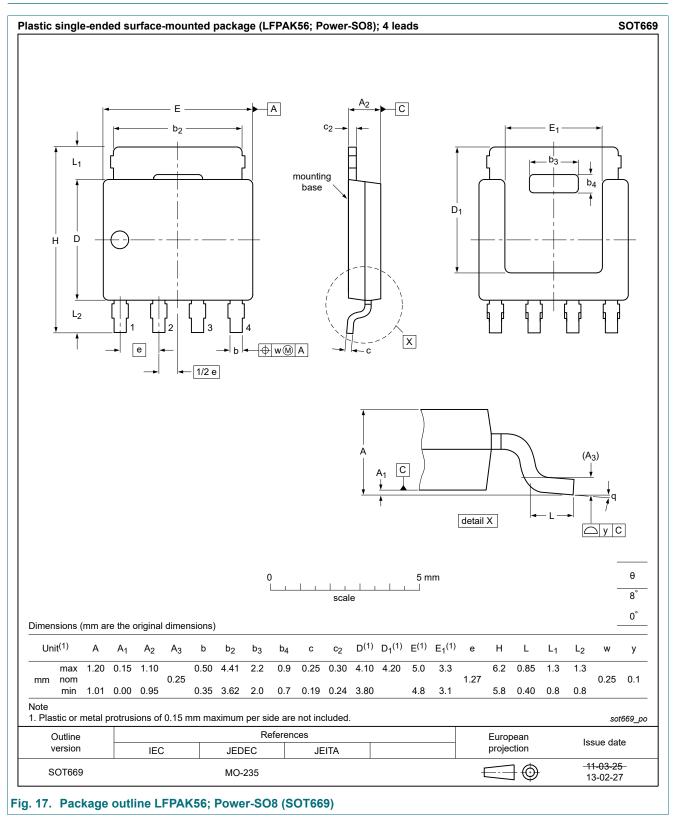
11. Test information



Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
BUK6Y24-40P v.1	20200409	Product data sheet	-	-		

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

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