Evaluates: MAX20340

General Description

The MAX20340 evaluation kit (EV kit) is a fully assembled and tested PCB that evaluates the MAX20340 bidirectional powerline communication (PLC) management integrated circuit. The EV kit obtains power from the MAX32625PICO microcontroller that contains the firmware necessary to use the EV kit GUI program. The EV kit features a master/slave mode PLC with flexible configurations. The EV kit ships with jumpers installed and supply voltages set to typical operating values.

Features

- USB-Powered Operation
- Compact and Simple Solution for PLC
- Flexible Configuration
- On-Board Regulator and Battery-Charging Circuitry
- Windows[®] 7/8/10-Compatible GUI Software
- Fully Assembled and Tested

Evaluation Kit Contents

- MAX20340 EV Kit
- Two USB A to micro-USB Cables

MAX20340 EV Kit Files

FILE	DECRIPTION
MAX20340EVKit.exe	PC GUI Program

Quick Start

Required Equipment

Note: In the following sections, software-related items are identified by **bold** text. Text in **bold** refers to items directly from the install of EV kit software. Text that is **bold and underlined** refers to items from the Windows operating system.

- MAX20340 EV kit
- Two USB A to micro-USB cables
- Windows PC with USB ports

Procedure

The EV kit is fully assembled and tested. Use the following steps to verify board operation. Caution: Do not turn on the power supply until all connections are completed.

- Visit <u>https://www.maximintegrated.com</u> to download the latest version of the EV kit software, MAX20340EVKitSetupV050.ZIP located on the MAX20340 EV kit web page. Download the EV kit software to a temporary folder and uncompress the ZIP file.
- Install the EV kit software on your computer by running the MAX20340EVKitSetupV050.EXE program inside the temporary folder.
- 3) Verify that all jumpers are in their default positions, as shown in <u>Table 1</u>.
- 4) Connect the micro-USB end of a cable to USB1 port of the EV kit and the type-A end to the PC.

Ordering Information appears at end of data sheet.

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Evaluates: MAX20340

- 5) Connect the micro-USB end of a cable to the USB port of the MA32625PICO microcontroller and the type-A end to the PC.
- 6) The LED on the MA32625PICO microcontroller flashes blue.
- 7) Start the MAX20340 EV kit GUI. The EV kit software main window appears, as shown in Figure 1.
- 8) If connection is successfully established, the status bar the bottom displays **Connected**.
- Verify the status of the master: Slave Found Charging on the left Master panel and the status of the slave: Master Found Comm. Enabled on the right Slave panel.
- 10) The EV kit is now ready for additional evaluations.

eral Configure Communication Test	Register Map		
aster		Slave	Read All
Master I ² C Address State Machine Status S Received Data 0 Received Data 1 Received Data 2	0xD4 Slave Found, Charging 0x00 0x00 0x00 Read Data	Slave I ² C Address State Machine Status Master Four Received Data 0 Received Data 1 Received Data 2	0x2A nd, Comm. Enabled 0x00 0x00 0x00 Read Data
PLC Communication Period	24µs *	PLC Communication Period	24µs *
PLC Master RX Wait Timer	10ms •	PLC Sink Current	298mA *
PLC Sink Current	298mA *	Communication Detection Threshold	80mV *
Communication Detection Threshold	80mV *	Parity	Odd •
Parity	Odd •		
Number of Bytes to Transmit	3 *	Echo Enable	
Transmit Data 0	0x00	Echo On	
Transmit Data 1	0x00	Echo Complement	
Transmit Data 2	0x00		
	Transmit	Transa Danati	in OK Esta Orat

Figure 1. The Status of the GUI Shows Connected Ready for Further Evaluations

Detailed Description of Software

Software Startup

Upon starting the program, the EV kit software automatically searches for the USB interface circuit and then for the IC device addresses. The EV kit enters the normal operating mode when the connection is established and addresses are found. If the USB connection is not detected, the status bar displays **Not Connected**. If the USB connection is detected, but the MAX20340 is not found, the **Master** or **Slave** panel in the **General** tab shows **Not Found**.

ToolStrip Menu Bar

The ToolStrip menu bar (<u>Figure 2</u>) is located at the top of the GUI window. This bar comprises **File**, **Device**, **Options**, and **Help** menus whose functions are detailed in the following sections.

File Menu

The **File** menu contains the option to exit out of the GUI program.

Device Menu

The Device menu provides the ability to connect or disconnect the EV kit to the GUI. If a board is disconnected while the GUI is open the GUI displays **Disconnected** in the lower right corner. If the device is then plugged back in, the bottom right corner of the GUI displays **Connected**.

Options Menu

The **Options** menu provides several settings to access more features offered by the GUI. The **Disable polling** option lets the user read the registers manually instead of getting automatically frequent register updates from the IC. **MAX20343** option allows the user to reset, enable, or disable the buck-boost regulator on the EV kit. Also, the user can view the **Serial Log** of reading or writing registers in the **Advanced** option.

Help Menu

The **Help** menu contains the **About** option, which displays the GUI splash screen indicative of the GUI version being used.

🔞 Bi-directional Power Line Communication (MAX20340) EV Kit Tool

File Device Options Help

Figure 2. The ToolStrip Menu Items

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Tab Controls

The MAX20340 EV kit software GUI provides a convenient way to test the features of the MAX20340. Each tab contains controls relevant to various blocks of the device. Changing these interactive controls triggers a write operation to the MAX20340 to update the register contents.

General Tab

The **General** tab (Figure 3) provides all important information and options to set up the MAX20340 master and slave modes. The **Master** block (on the left side) and **Slave** block (on the right side) display I²C addresses and allow the user to choose parameters for each setting in powerline communication.

General Configure Communication Test	Register Map		
Master		Slave	Read All
Master I ² C Address State Machine Status SI Received Data 0 Received Data 1 Received Data 2	0xD4 ave Found, Charging 0x00 0x00 0x00 0x00 Read Data	Slave I ² C Address State Machine Status Master Fou Received Data 0 Received Data 1 Received Data 2	0x2A und, Comm. Enabled 0x00 0x00 0x00 Read Data
PLC Communication Period PLC Master RX Wait Timer PLC Sink Current Communication Detection Threshold Parity	24µs × 10ms × 298mA × 80mV × Odd ×	PLC Communication Period PLC Sink Current Communication Detection Threshold Parity	24µs × 298mA × 80mV × Odd ×
Number of Bytes to Transmit Transmit Data 0 Transmit Data 1 Transmit Data 2	3	Echo Enable Echo Off Echo On Echo Complement	

Figure 3. General Tab

Configure Tab

The **Configure** tab (Figure 4) configures the Master and Slave sides with more unique settings such as **Transmit Filter**, **Charge Timer**, and **LDO Voltage**.

General Configure Communication Test	Register Map		
Master		Slave	Read All
 Device Enable Transmit Filter Enable 		Device Enable Transmit Filter Enable	
Master/Slave Detection Reset	Send	Master/Slave Detection Reset	Send
Transmit/Receive Reset	Send	Transmit/Receive Reset	Send
Charge Timer Setting	120min *	Minimum LDO Voltage	3.5V *
		LDO-BAT Difference	300mV +
		Battery Recharge Threshold	3.6V *
		Slave To Idle Command	Send

Figure 4. Configure Tab

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Communication Test Tab

The **Communication Test** tab (Figure 5) hosts the settings for powerline communication tests. The user can specify transmit data in hex values and run the test with either single or continuous transmission. The **Results** block, on the right side, shows all statistics, specifically errors and error rate, when the test ends.

During a test, each transmission consists of three bytes. It is not possible to send less than three bytes per transmission using the communication test tool. To start a test, input the data to transmit in the **Transmit Data** 0 to 2 text boxes or select **Use Random Data** to use a pseudorandom set of three bytes. Clicking the **Transmit** button transmits a single set of three bytes. To start continuous transmission test, click **Start Test** button. It is not possible to modify settings on other tabs while a transmission is in progress.

Click **Stop Test** to end the communication test. A set of **Pause Test...** buttons can be selected to stop the test whenever a corresponding transmit or receive error is detected. Click the **Clear Results** button any time to clear the results.

Communication Test Configuration Results Transmit Data 0 0x00 Transmit Data 1 0x00 Transmit Data 2 0x00 O Use Random Data 0x00 Transmit 0x00 Start Test Stop Test Pause Test on Master Transmit Error 1 Pause Test on Slave Receive Error 0x00, 0x
Transmit Data 0 0x00 Transmit Data 1 0x00 Transmit Data 1 0x00 Transmit Data 2 0x00 Waster Transmit Errors Slave Receive Errors Use Random Data Slave Transmit Errors Transmit Start Test Start Test Stop Test Pause Test on Master Transmit Error Master Transmitting: 0x00,
Pause Test on Master Receive Error SLAV_TX MAST_RX

Figure 5. Communication Test Tab

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Register Map Tab

The **Register Map** tab (Figure 6) provides all names and values of MAX20340 registers. The user can click **Read All** on the top right corner to perform a burst read of all registers. Master and slave registers can be read and written individually.

The left table shows the register to be read from or written to. The right table contains descriptions for each register field of the selected 8-bit register. All bits, along with their field names, are displayed at the bottom of the page. To set a bit, click the bit label. **Bold** text represents logic 1 and regular text represents logic 0. To configure the changes to the device, click the **Write** button at the bottom right.

When configuring PLC settings through the register map, some settings, such as **PLC Communication Frequency**, must be the same on the master and slave. To simplify writing settings to both devices, toggle **the Duplicate Writes on Both Devices** button. When enabled, all writes are committed to both the master and slave.

				in the second seco						
 Mast 	er Registe	rs Slave R	Registers	O Duplica	ate Writes on Both D	evices		Read All		
Addr	1	Register	Value	Value Field Name				Description		
0x00	DEVICE	ID	0x10		1	LDO Volt	tage Select (2.8V	to 3.5V in 0.1V		
0x01	CONTRO	DL1	0xE0	Bit (7:5)	7:51 V LDO MIN steps). In slave mode			e, if D_LDO_BAT[2:0]		
0x02	CONTRO	DL2	0xF5	Dir [1.0]		!= "000", this sets the minimum allowed LDO voltage overriding D LDO BAT[2:0]				
0x03	CONTRO	DL3	0xA4	-		Degulated LDO BAT Difference In				
0x04	CONTRO	DL4	0x00			mode, th	is sets the regula	ted difference		
0x05	DEV_STA	ATUS1	0x5B			between	between the voltages of LDO and BAT.			
0x06	DEV_STATUS2		0x1D		0 = LDO Bypasse 1 = 100mV		Bypassed	3		
0x07	DEV_ST/	ATUS_IRQ	0x00	Bit [4:2]	D_LDO_BAT	2 = 150n	nV			
0x08	DEV_ST/	ATUS_MASK	0x10			3 = 200n	3 = 200mV 4 = 250mV 5 = 300mV 6 = 350mV			
0x09	PLC_CO	M_CTRL	0x94			5 = 300n				
A0x0	PLC_STA	ATUS	0x00			6 = 350n				
0x0B	PLC_IRG	1	0x00			7 = 400n	nV			
0x0C	PLC_MA	SK	0x00		PLC Master's F transmission		ster's RX wait time	X wait timer after		
0x0D	TX_DATA	40	0x00	Bit [1.0]	THAT THE	0 = 2ms				
0x0E	TX_DATA1		0x00	Bit [1:0]	I WAIT_IMR	1 = 10ms(Default)				
0x0F	TX_DATA	2	0x00			2 = 1000	2 = 100ms 3 = 800ms			
0x10	RX_DATA	40	0x00		80	0.000				
0x11	RX_DATA	41	0x00							
0x12	RX_DATA	42	0x00							
	7	6	5	4	3	2	1	0		
V_LDO	_MIN[2]	V_LDO_MIN[1]	V_LDO_MIN[0]	D_LDO_BAT[2]	D_LDO_BAT[1]	D_LDO_BAT[0]	TWAIT_TMR[1]	TWAIT_TMR[0]		

Figure 6. Register Map Tab

Evaluates: MAX20340

Detailed Description of Hardware

The MAX20340 EV kit evaluates the MAX20340 bidirectional powerline communication management integrated circuit, which communicates over the I^2C interface. The EV kit demonstrates the IC features such as different

charging states, master/slave mode, I²C addresses, dual/ single PLC slave mode, and PLC slave addresses. The EV kit uses the IC in a 9-bump (1.358mm x 1.358mm) wafer-level package (WLP) on a proven, four-layer PCB design. The EV kit operates from the USB +5V DC, and therefore, does not require an external power supply.

Table 1. Jumper Table (JU1-JU26)

JUMPER	SHUNT POSITION	DESCRIPTION
JU1	1-2*	Connect SDA of U2 to SDA of MAX32625PICO. (Assuming a shunt is installed on JU12)
JU2	1-2*	Connect SCL of U2 to SCL of MAX32625PICO. (Assuming a shunt is installed on JU13)
JU3	1-2*	Connect SDA of U1 to SDA of MAX32625PICO. (Assuming a shunt is installed on JU12)
JU4	1-2*	Connect SCL of U1 to SCL of MAX32625PICO. (Assuming a shunt is installed on JU13)
11.15	1-2	Connect VCC2 of U2 to external power supply applied at VEXT.
105	2-3*	Connect VCC2 of U2 to BBOUT of U6.
11.16	Open*	Disconnect VCC1 of U1 to CHGIN of U5.
	Closed	Connect VCC1 of U1 to CHGIN of U5.
11.17	1-2	Connect VBAT1 of U1 to external power supply applied at VEXT.
507	2-3	Connect VBAT1 of U1 to VBAT of U5.
JU8	1-2	Connect VBAT2 of U2 to external power supply applied at VEXT.
	2-3*	Connect VBAT2 of U2 to BBOUT of U6.
JU9	1-2*	Connect VIO to 3.3V of MAX32625PICO.
JU10	1-2*	Connect INT2 of U2 to LED indicator.
JU11	1-2*	Connect INT1 of U1 to LED indicator.
JU12	1-2*	Connect SDA line to MAX32625PICO.
JU13	1-2*	Connect SCL line to MAX32625PICO.
	Open	Pull $\overline{\text{EN}}$ of U1 high to VIO to put it in low power shutdown state.
JU14	1-2	Connect EN of U1 to GPIO of the MAX32625PICO.
	2-3*	Connect $\overline{\text{EN}}$ of U1 to ground to exit low power shutdown state.
	Open	Pull $\overline{\text{EN}}$ of U2 high to VIO to put it in low power shutdown state.
JU15	1-2	Connect $\overline{\text{EN}}$ of U2 to GPIO of the MAX32625PICO.
	2-3*	Connect $\overline{\text{EN}}$ of U2 to ground to exit low power shutdown state.
JU16	1-2*	Connect PLC line between U1 and U2.
	1-2*	Select 19.1KΩ resistor for RSEL of U1.
11 14 7	3-4	Select 24.9KΩ resistor for RSEL of U1.
3017	5-6	Select 31.6KΩ resistor for RSEL of U1.
	7-8	Select 43KΩ resistor for RSEL of U1.

Table 1. Jumper Table (JU1-JU26) (continued)

JUMPER	SHUNT POSITION	DESCRIPTION
	1-2	Select 3.9k Ω resistor for RSEL of U2.
11 14 0	3-4	Select $6.65k\Omega$ resistor for RSEL of U2.
JU18	5-6	Select 10.2kΩ resistor for RSEL of U2.
	7-8*	Select 14.3k Ω resistor for RSEL of U2.
11110	1-2*	Connect BBIN of U6 to external power supply applied at VEXT.
2-3		Connect BBIN of U6 to MAX32625PICO 5V.
JU20	1-2	Connect FAST to CAP pin of U6.
JU21	1-2	Do not install a shunt on JU21. It is for connecting an optional external battery pack to the MAX20335 battery charger.
JU23	1-2*	Connect VBUS of USB1 to external power supply applied at VEXT.
JU24	1-2	Connect 2.2µF capacitor to CHGIN of U5.
11.125	1-2*	Connect the fixed-value R8 to form the bottom resistor of the resistor divider between VBAT of U5 and AIN2 of MAX32625PICO.
3025	2-3	Connect the potentiometer to form the bottom resistor of the resistor divider between VBAT of U5 and AIN2 of MAX32625PICO.
JU26	1-2	Connect output capacitor C1 to VCC1 of U1. Install this shunt if LDO of U1 is enabled.

*Default position.

Ordering Information

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#Denotes RoHS compliance.

Evaluates: MAX20340

MAX20340 EV Kit Bill of Materials

ITEM	REF_DES	DNI/DNP	QTY	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
1	C1, C10-C12,		6	GRM155R601226ME11	ΜΙΙΚΑΤΑ	2211F	CAPACITOR; SMT (0402); CERAMIC CHIP; 22UF; 6.3V;
-	C16, C17		Ŭ	GRINIESSROOJZZOWIETT	Monara	2201	TOL=20%; TC=X5R ;
2	C2-C4, C8	-	4	0603ZC105KAT2A	AVX	1UF	CAP; SMT (0603); 1UF; 10%; 10V; X7R; CERAMIC CHIP
3	C5	-	1	6TPE220MAZB	PANASONIC	220UF	CAP; SMT (CASE_B2); 220UF; 20%; 6.3V; TANTALUM CHIP
4	C6, C20	-	2	ECJ-2FB1A106; CL21A106KPCLQNC; GRM219R61A106KE44	MURATA;PANASONIC; SAMSUNG ELECTRONICS; MURATA	10UF	CAPACITOR; SMT (0805); CERAMIC CHIP; 10UF; 10V; TOL=10%; MODEL=; TG=-55 DEGC TO +85 DEGC; TC=X5R
5	С9	-	1	C1608X5R1H104K080AA	ТДК	0.1UF	CAPACITOR; SMT (0603); CERAMIC CHIP; 0.1UF; 50V; TOL=10%; MODEL=C SERIES; TG=-55 DEGC TO +85 DEGC; TC=X5R
6	C13	-	1	GRM033R61A105ME15	MURATA	1UF	CAPACITOR; SMT (0201); CERAMIC CHIP; 1UF; 10V; TOL=20%; TG=-55 DEGC TO +85 DEGC; TC=X5R
7	C14, C15, C19	-	3	C1005X5R1V225M050BC	ТДК	2.2UF	CAPACITOR; SMT (0402); CERAMIC CHIP; 2.2UF; 35V; TOL=20%; MODEL=C SERIES; TG=-55 DEGC TO +85 DEGC; TC=X5R
8	DS1-DS3	-	3	LG L29K-G2J1-24	OSRAM	LG L29K-G2J1-24	DIODE; LED; SMT (0603); Vf=1.7V; lf(test)=0.002A; -40 DEGC TO +100 DEGC
9	J1	-	1	SSQ-102-02-T-S-RA	SAMTEC	SSQ-102-02-T-S-RA	CONNECTOR; FEMALE; THROUGH HOLE; 0.025IN SQUARE POST SOCKET; SSQ SERIES; RIGHT ANGLE; 2PINS
10	J2	-	1	M20-9960246	HARWIN	M20-9960246	CONNECTOR; MALE; THROUGH HOLE; PIN HEADER ASSEMBLY; M20 SERIES; RIGHT ANGLE; 2PINS
11	J3, JU1-JU4, JU6, JU9-JU13, JU16, JU20, JU21, JU23, JU24, JU26	-	17	PCC02SAAN	SULLINS	PCC02SAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT THROUGH; 2PINS; -65 DEGC TO +125 DEGC
12	JU5, JU7, JU8, JU14, JU15, JU19, JU25	-	7	PEC03SAAN	SULLINS	PEC03SAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 3PINS
13	JU17, JU18	-	2	PEC04DAAN	SULLINS ELECTRONICS CORP.	PEC04DAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 8PINS
14	L1	-	1	DFE252012F-1R0M=P2; DFE252012F-1R0M	MURATA;MURATA	1UH	INDUCTOR; SMT (1008); METAL; 1UH; 20%; 3.3A
15	MH1-MH4	-	4	9032	KEYSTONE	9032	MACHINE FABRICATED; ROUND-THRU HOLE SPACER; NO THREAD; M3.5; 5/8IN; NYLON
16	R1, R2, R25	-	3	CRCW0402499RFK	VISHAY DALE	499	RESISTOR; 0402; 499 OHM; 1%; 100PPM; 0.0625W; THICK FILM
17	R3	-	1	ERA-2AEB392	PANASONIC	3.9К	RESISTOR; 0402; 3.9K OHM; 0.1%; 25PPM; 0.063W; THICK FILM
18	R4	-	1	ERJ-2RKF6651	PANASONIC	6.65K	RESISTOR; 0402; 6.65K OHM; 1%; 100PPM; 0.10W; THICK FILM
19	R5	-	1	CRCW040210K2FK; CR0402-16W-1022	VISHAY DALE;VENKEL LTD	10.2К	RESISTOR; 0402; 10.2K OHM; 1%; 100PPM; 0.063W; THICK FILM
20	R6	-	1	CRCW040214K3FK	VISHAY DALE	14.3K	RESISTOR, 0402, 14.3K OHM, 1%, 100PPM, 0.0625W, THICK FILM
21	R7	-	1	CRCW06031M50FK	VISHAY DALE	1.5M	RESISTOR, 0603, 1.5M OHM, 1%, 100PPM, 0.10W, THICK FILM
22	R8	-	1	ERJ-PB6D4993	PANASONIC	499K	RES; SMT (0805); 499K; 0.5%; +/-50PPM/DEGC; 0.25W
23	R9, R34	-	2	3296Y-1-504LF	BOURNS	500К	RESISTOR; THROUGH-HOLE-RADIAL LEAD; 500K OHM; 10%; 100PPM; 0.5W; SQUARE TRIMMING POTENTIOMETER
24	R15	-	1	CR0402-16W-1912FT; CRCW040219K1FK	VENKEL LTD.;VISHAY DALE	19.1K	RESISTOR; 0402; 19.1K OHM; 1%; 100PPM; 0.063W; THICK FILM
25	R16	-	1	ERJ-2RKF2492	PANASONIC	24.9К	RESISTOR; 0402; 24.9K OHM; 1%; 100PPM; 0.10W; THICK FILM
26	R17	-	1	CRCW040231K6FK	VISHAY DALE	31.6K	RESISTOR; 0402; 31.6K OHM; 1%; 100PPM; 0.063W; THICK FILM
27	R18	-	1	CRCW040243K0FK	VISHAY DALE	43K	RESISTOR; 0402; 43K OHM; 1%; 100PPM; 0.063W; THICK FILM
28	R19-R24	-	6	CRCW04024K70FK; MCR01MZPF4701	VISHAY DALE; ROHM SEMICONDUCTOR	4.7K	RESISTOR, 0402, 4.7K OHM, 1%, 100PPM, 0.0625W, THICK FILM
29	R26, R31	-	2	CRCW040210K0FK; RC0402FR-0710KL	VISHAY DALE;YAGEO PHICOMP	10K	RESISTOR; 0402; 10K; 1%; 100PPM; 0.0625W; THICK FILM
30	R27	-	1	ERA-3AEB104; AT0603BRD07100KL	PANASONIC;YAGEO	100K	RESISTOR; 0603; 100K OHM; 0.1%; 25PPM; 0.1W; THIN FILM

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MAX20340 EV Kit Bill of Materials (continued)

ITEM	REF_DES	DNI/DNP	QTY	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
31	R29, R30, R32, R33	-	4	RC0603FR-0710KL; AC0603FR-0710KL	YAGEO;YAGEO	10К	RESISTOR; 0603; 10K OHM; 1%; 100PPM; 0.1W; THICK FILM
32	R35	-	1	RNCP0603FTD2K00	STACKPOLE ELECTRONICS INC.	2К	RESISTOR; 0603; 2K OHM; 1%; 100PPM; 0.125W; THICK FILM
33	SU1-SU19, SU23-SU25	-	22	S1100-B;SX1100-B; STC02SYAN	KYCON;KYCON; SULLINS ELECTRONICS CORP.	SX1100-B	TEST POINT; JUMPER; STR; TOTAL LENGTH=0.24IN; BLACK; INSULATION=PBT; PHOSPHOR BRONZE CONTACT=GOLD PLATED
34	TP1-TP3, TP13-TP16	-	7	5011	KEYSTONE	N/A	TEST POINT; PIN DIA=0.125IN; TOTAL LENGTH=0.445IN; BOARD HOLE=0.063IN; BLACK; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH;
35	TP4	-	1	5010	KEYSTONE	N/A	TEST POINT; PIN DIA=0.125IN; TOTAL LENGTH=0.445IN; BOARD HOLE=0.063IN; RED; PHOSPHOR BRONZE WIRE SIL;
36	TP5, TP10	-	2	5004	KEYSTONE	N/A	TEST POINT; PIN DIA=0.1IN; TOTAL LENGTH=0.3IN; BOARD HOLE=0.04IN; YELLOW; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH;
37	TP6-TP9	-	4	5000	KEYSTONE	N/A	TEST POINT; PIN DIA=0.1IN; TOTAL LENGTH=0.3IN; BOARD HOLE=0.04IN; RED; PHOSPHOR BRONZE WIRE SILVER PLATE FINISH;
38	U1, U2	-	2	MAX20340	MAXIM	MAX20340	EVKIT PART - IC; MAX20340; BI-DIRECTIONAL POWER LINE COMMUNICATION MANAGEMENT IC; WLP9; PACKAGE OUTLINE DRAWING: 21-100389; PACKAGE CODE: W91R1+1
39	U3	-	1	MAX32625PICO	MAXIM	MAX32625PICO	MODULE; BOARD; MAX32625PICO BOARD DESIGN FOR MAX32625 ARM CORTEX-M4F; BOARD; LAMINATED PLASTIC WITH COPPER CLAD;
40	U4	-	1	NC7WZ07P6X	FAIRCHILD SEMICONDUCTOR	NC7WZ07P6X	IC; BUF; TINY LOGIC ULTRA-HIGH SPEED DUAL BUFFER; SC70-6
41	U5	-	1	MAX20335	MAXIM	MAX20335	EVKIT PART - IC; PWRM; WEARABLE CHARGE MANAGEMENT SOLUTION; WLP36;
42	U6	-	1	MAX20343EEWE+	MAXIM	MAX20343EEWE+	EVKIT PART - IC; ULTRA-LOW QUIESCENT CURRENT; LOW NOISE 3.5W BUCKBOOST REGULATOR; WLP16; PACKAGE OUTLINE: 21-100328; PACKAGE CODE: W161C2+1
43	USB1	-	1	10118192-0001LF	FCI CONNECT	10118192-0001LF	CONNECTOR; FEMALE; SMT; MICRO USB B TYPE RECEPTACLE; RIGHT ANGLE; SPINS
44	PCB	-	1	MAX20340	MAXIM	PCB	PCB:MAX20340
45	PS1-PS3	DNP	0	131-4353-00	TEKTRONICS	131-4353-00	CONNECTOR; WIREMOUNT; CIRCUIT BOARD TEST POINT MINIATURE PROBE; STRAIGHT; 4PINS
TOTAL			127				

MAX20340 EV Kit Schematics





MAX20340 EV Kit Schematics (continued)

Evaluates: MAX20340

MAX20340 EV Kit PCB Layouts



MAX20340 EV Kit PCB Layout—Silk Top



MAX20340 EV Kit PCB Layout—Top

Evaluates: MAX20340



MAX20340 EV Kit PCB Layouts (continued)

MAX20340 EV Kit PCB Layout—Internal 2



MAX20340 EV Kit PCB Layout—Internal 3



MAX20340 EV Kit PCB Layouts (continued)

MAX20340 EV Kit PCB Layout—Bottom



MAX20340 EV Kit PCB Layout—Silk Bottom

Evaluates: MAX20340

Revision History

REVISION	REVISION	DESCRIPTION	PAGES
NUMBER	DATE		CHANGED
0	12/19	Initial release	—

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