

### **Features**

- Halogen Free. "Green" Device (Note 1)
- Glass Passivated Chip Junction
- Super Fast Recovery Times for High Efficciency
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Lead Free Finish/RoHS Compliant (Note 2)("P" Suffix Designates RoHS Compliant. See Ordering Information)

# Maximum Ratings @ 25°C (Unless Otherwise Specified)

Danier dan	Symbol	Value						11!4
Parameter		ES 2A-L	ES 2B-L	ES 2C-L	ES 2D-L	ES 2G-L	ES 2J-L	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>							
Working Peak Reverse Voltage	$V_{RWM}$	50	100	150	200	400	600	V
DC Blocking Voltage	$V_R$							
RMS Reverse Voltage	V <sub>RMS</sub>	35	70	105	140	280	420	V
Average Rectified Forward Current @ T <sub>L</sub> =110°C	I <sub>F(AV)</sub>			:	2			Α
Non-Repetitive Peak Surge Current @8.3ms Half Sine Wave		50				٨		
Non-Repetitive Peak Surge Current @1ms Half Sine Wave	I <sub>FSM</sub>	100					A	
Current Squared Time @1ms≤t≤8.3ms	l <sup>2</sup> t	10.375			A <sup>2</sup> s			

# Marking code

Part Number	Marking code
ES2A-L	ES2A
ES2B-L	ES2B
ES2C-L	ES2C
ES2D-L	ES2D
ES2G-L	ES2G
ES2J-L	ES2J

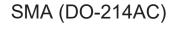
## **Internal Structure**

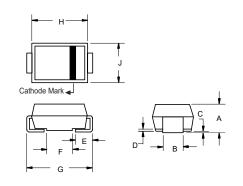
Pin	Description	Simplified outline	Graphic symbol
1	Cathode	1 MCC 2	
2	Anode	XXXX = Marking code	1 0

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

2. High Temperature Solder Exemption Applied, see EU Directive Annex 7a.

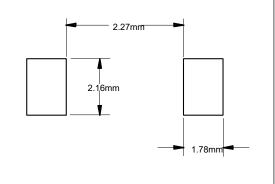
# 2 Amp Super Fast Recovery Rectifier 50 to 600 Volts





DIMENSIONS						
DIM	INCHES		М	M	NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.075	0.096	1.90	2.44		
В	0.050	0.064	1.27	1.63		
С	0.002	0.008	0.051	0.203		
D		0.020		0.51		
Е	0.030	0.060	0.76	1.52		
F	0.065	0.091	1.65	2.32		
G	0.189	0.220	4.80	5.59		
I	0.157	0.187	4.00	4.75		
J	0.090	0.115	2.25	2.92		

### SUGGESTED SOLDER PAD LAYOUT





# Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
TJ	Operating Junction Temperature Range		-65		175	°C
T <sub>stg</sub>	Storage Temperature Range		-65		175	°C
Rth <sub>(J-L)</sub>	Thermal Resistance from Junction to Lead	Note 1		20		°C /W
Rth <sub>(J-A)</sub>	Thermal Resistance from Junction to Ambient	Note 1		65		°C /W

#### Note:

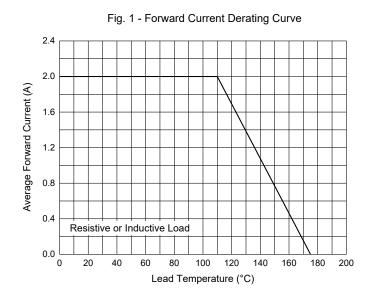
# Electrical Characteristics @ 25°C Unless Otherwise Specified

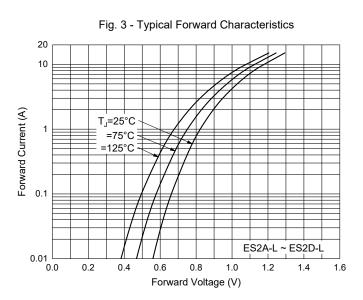
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Forward Voltage						
ES2A-L ~ ES2D-L	V <sub>F</sub>	I <sub>F</sub> =2A;T <sub>J</sub> =25°C			0.95	
ES2G-L					1.25	V
ES2J-L					1.70	
Reverse Current	I <sub>R</sub>	at Rated $V_R$ ; $T_J$ =25°C at Rated $V_R$ ; $T_J$ =125°C			1 100	uA
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =0.5A; I <sub>R</sub> =1.0A; I <sub>rr</sub> =0.25A;T <sub>J</sub> =25°C			35	ns
Junction Capacitance						
ES2A-L ~ ES2D-L	Сл	V <sub>R</sub> =4V;f=1MHz;T <sub>J</sub> =25°C		30		
ES2G-L				18		pF
ES2J-L				12		

<sup>1.</sup>Mounted on P.C.B. with 8mm\*8mm copper pad areas.



### **Curve Characteristics**





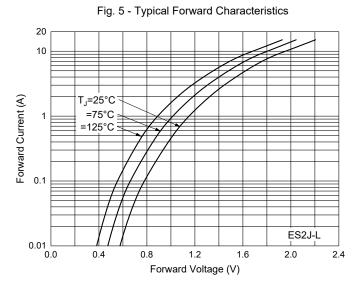
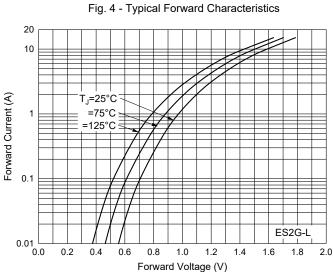
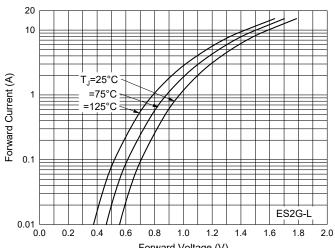
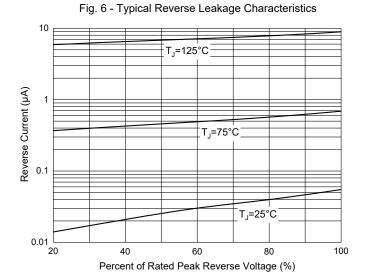


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current 60 50 Peak Forward Surge Current (A) 40 30 20 10 8.3 ms Single Half Sine-Wave 0 10 100 Number of Cycles at 60 Hz





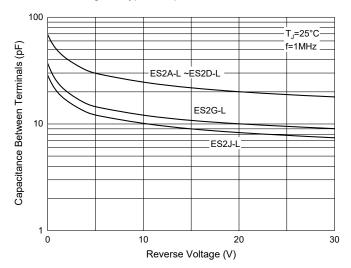


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# **Curve Characteristics**

Fig. 7 - Typical Capacitance Characteristics





### **Ordering Information**

Device	Packing		
ES2A-LTP ~ ES2J-LTP	Tape&Reel:5Kpcs/Reel		

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