

#### STGY50NC60WD

#### 50 A, 600 V, ultra fast IGBT

#### **Features**

- Very high frequency operation
- Low C<sub>RES</sub> / C<sub>IES</sub> ratio (no cross-conduction susceptibility)
- Very soft ultra fast recovery antiparallel diode

#### **Applications**

- Very high frequency inverters, UPS
- HF, SMPS and PFC in both hard switch and resonant topologies
- Motor drivers
- Welding



This IGBT utilizes the advanced Power MESH™ process resulting in an excellent trade-off between switching performance and low on-state behavior.

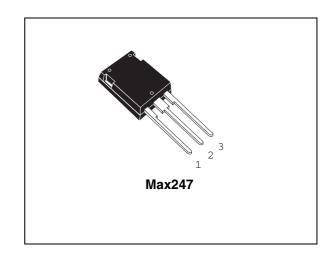


Figure 1. Internal schematic diagram

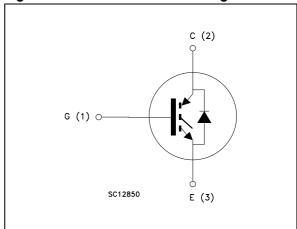


Table 1. Device summary

Order code	Order code Marking		Packaging
STGY50NC60WD	GY50NC60WD	Max247	Tube

Contents STGY50NC60WD

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STGY50NC60WD Electrical ratings

# 1 Electrical ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
$V_{CES}$	Collector-emitter voltage (V <sub>GE</sub> = 0)	600	V
I <sub>C</sub> <sup>(1)</sup>	Collector current (continuous) at T <sub>C</sub> = 25 °C	110	Α
I <sub>C</sub> <sup>(1)</sup>	Collector current (continuous) at T <sub>C</sub> = 100 °C	50	Α
I <sub>CL</sub> (2)	Turn-off latching current	180	Α
I <sub>CP</sub> (3)	Pulsed collector current	180	Α
I <sub>F</sub>	Diode RMS forward current at T <sub>C</sub> = 25 °C	30	Α
I <sub>FSM</sub>	Surge not repetitive forward current (t <sub>p</sub> =10 ms sinusoidal)	120	А
$V_{GE}$	Gate-emitter voltage	±20	V
P <sub>TOT</sub>	Total dissipation at $T_C = 25$ °C 278		W
T <sub>j</sub>	Operating junction temperature	-55 to 150	°C

<sup>1.</sup> Calculated according to the iterative formula:

$$I_{C}(T_{C}) = \frac{T_{j(max)} - T_{C}}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_{C}(T_{C}))}$$

- 2.  $V_{clamp}$  = 80% of  $V_{CES}$ ,  $T_j$  =150 °C,  $R_G$ =10  $\Omega$ ,  $V_{GE}$ =15 V
- 3. Pulse width limited by max. temperature allowed

Table 2. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>thj-case</sub>	Thermal resistance junction-case IGBT max.	0.45	°C/W
R <sub>thj-case</sub>	Thermal resistance junction-case diode max.	1.5	°C/W
R <sub>thj-amb</sub>	Thermal resistance junction-ambient max.	50	°C/W

Electrical characteristics STGY50NC60WD

## 2 Electrical characteristics

 $(T_{CASE} = 25 \, ^{\circ}C \text{ unless otherwise specified})$ 

Table 3. Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)CES</sub>	Collector-emitter breakdown voltage (V <sub>GE</sub> = 0)	I <sub>C</sub> = 1 mA	600			V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	$V_{GE} = 15 \text{ V}, I_{C} = 40 \text{ A}$ $V_{GE} = 15 \text{ V}, I_{C} = 40 \text{ A}, T_{C} = 125 ^{\circ}\text{C}$		2.1 1.9	2.6	V V
V <sub>GE(th)</sub>	Gate threshold voltage	$V_{CE} = V_{GE}, I_{C} = 250 \mu A$	3.75		5.75	V
I <sub>CES</sub>	Collector cut-off current (V <sub>GE</sub> = 0)	V <sub>CE</sub> = 600 V V <sub>CE</sub> = 600 V,T <sub>C</sub> = 125 °C			500 5	μA mA
I <sub>GES</sub>	Gate-emitter leakage current (V <sub>CE</sub> = 0)	V <sub>GE</sub> = ±20 V			±100	nA
9 <sub>fs</sub>	Forward transconductance	V <sub>CE</sub> = 15 V, I <sub>C</sub> = 40 A		25		S

Table 4. Dynamic

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C <sub>ies</sub> C <sub>oes</sub> C <sub>res</sub>	Input capacitance Output capacitance Reverse transfer capacitance	$V_{CE} = 25 \text{ V, f} = 1 \text{ MHz,}$ $V_{GE} = 0$		4700 410 90		pF pF pF
Q <sub>g</sub> Q <sub>ge</sub> Q <sub>gc</sub>	Total gate charge Gate-emitter charge Gate-collector charge	$V_{CE} = 390 \text{ V}, I_{C} = 40 \text{ A},$ $V_{GE} = 15 \text{ V},$ <i>Figure 16</i>		195 32 82		nC nC nC

Table 5. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
t <sub>d(on)</sub> t <sub>r</sub> (di/dt) <sub>on</sub>	Turn-on delay time Current rise time Turn-on current slope	$V_{CC}$ = 390 V, $I_{C}$ = 40 A $R_{G}$ = 10 $\Omega$ , $V_{GE}$ = 15 V, Figure 17, Figure 15		52 17 2400		ns ns A/µs
t <sub>d(on)</sub> t <sub>r</sub> (di/dt) <sub>on</sub>	Turn-on delay time Current rise time Turn-on current slope	$V_{CC} = 390 \text{ V, } I_{C} = 40 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V,}$ $T_{C} = 125 ^{\circ}\text{C}$ Figure 17, Figure 15		50 19 2020		ns ns A/µs
t <sub>r(Voff)</sub> t <sub>d(Voff)</sub> t <sub>f</sub>	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 390 \text{ V, } I_{C} = 40 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V,}$ Figure 17, Figure 15		31 240 35		ns ns ns
t <sub>r(Voff)</sub> t <sub>d(Voff)</sub> t <sub>f</sub>	Off voltage rise time Turn-off delay time Current fall time	$V_{CC} = 390 \text{ V, } I_{C} = 40 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V,}$ $T_{C} = 125 ^{\circ}\text{C}$ Figure 17, Figure 15		59 280 63		ns ns ns

Table 6. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
E <sub>on</sub> <sup>(1)</sup> E <sub>off</sub> <sup>(2)</sup> E <sub>ts</sub>	Turn-on switching losses Turn-off switching losses Total switching losses	$V_{CC}$ = 390 V, $I_{C}$ = 40 A $R_{G}$ = 10 $\Omega$ , $V_{GE}$ = 15 V, Figure 15		365 560 925	470 790 1260	μJ μJ μJ
E <sub>on</sub> <sup>(1)</sup> E <sub>off</sub> <sup>(2)</sup> E <sub>ts</sub>	Turn-on switching losses Turn-off switching losses Total switching losses	$V_{CC} = 390 \text{ V, } I_{C} = 40 \text{ A}$ $R_{G} = 10 \Omega, V_{GE} = 15 \text{ V,}$ $T_{C} = 125 \text{ °C}$ Figure 15		635 910 1545		μJ μJ μJ

Eon is the tun-on losses when a typical diode is used in the test circuit in Figure 18 If the IGBT is offered in a package with a co-pak diode, the co-pack diode is used as external diode. IGBTs & Diode are at the same temperature (25°C and 125°C)

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<sup>2.</sup> Turn-off losses include also the tail of the collector current

Electrical characteristics STGY50NC60WD

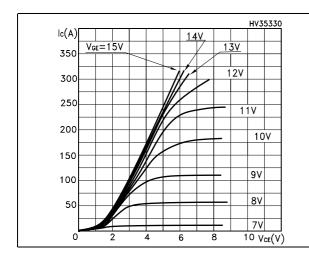
Table 7. Collector-emitter diode

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>F</sub>	Forward on-voltage	I <sub>F</sub> = 40 A I <sub>F</sub> = 40 A, T <sub>C</sub> = 125 °C		3.2 2.2		V V
t <sub>rr</sub> Q <sub>rr</sub> I <sub>rrm</sub>	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F = 40 \text{ A}, V_R = 50 \text{ V},$ di/dt = 100 A/ $\mu$ s Figure 18		55 100 3.6		ns nC A
t <sub>rr</sub> Q <sub>rr</sub> I <sub>rrm</sub>	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F = 40 \text{ A}, V_R = 50 \text{ V},$ $T_C = 125 ^{\circ}\text{C},$ $di/dt = 100 \text{ A}/\mu\text{s} (Figure 18)$		164 525 6.4		ns nC A

## 2.1 Electrical characteristics (curves)

Figure 1. Output characteristics

Figure 2. Transfer characteristics



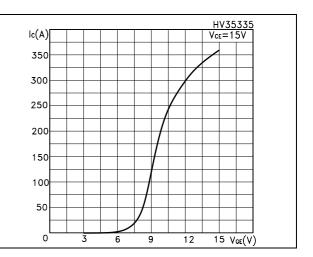
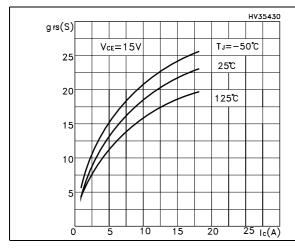


Figure 3. Transconductance

Figure 4. Collector-emitter on voltage vs temperature



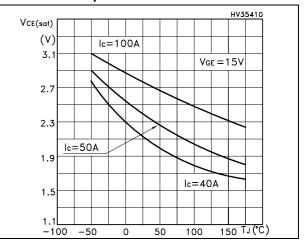
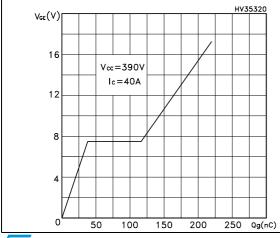
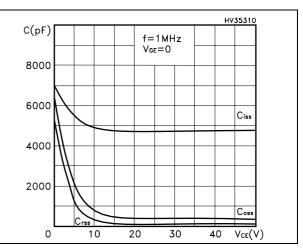


Figure 5. Gate charge vs gate-source voltage Figure 6. Capacitance variations





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Electrical characteristics STGY50NC60WD

Figure 7. Normalized gate threshold voltage Figure 8. Collector-emitter on voltage vs vs temperature collector current

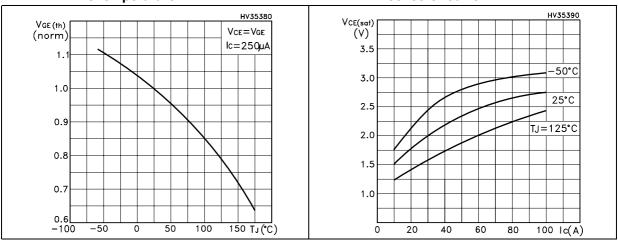


Figure 9. Normalized breakdown voltage vs Figure 10. Switching losses vs temperature temperature

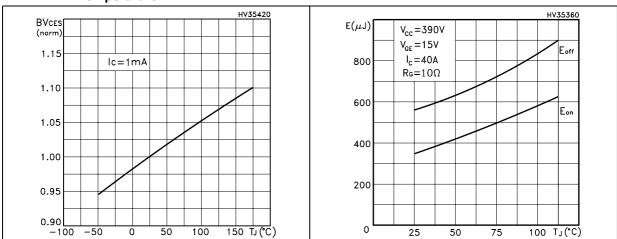
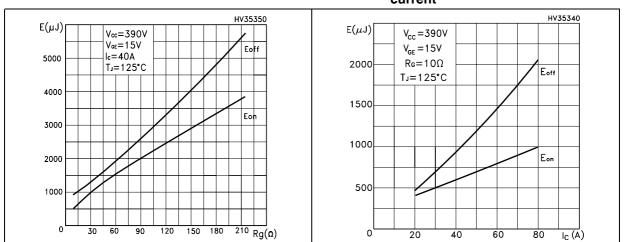


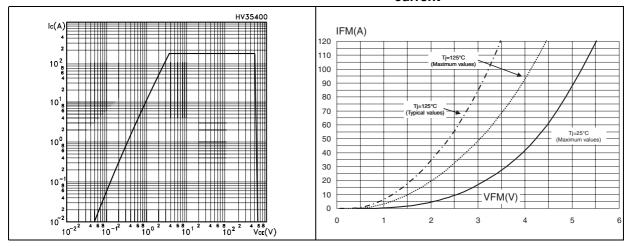
Figure 11. Switching losses vs gate resistance Figure 12. Switching losses vs collector current



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Figure 13. Turn-off SOA

Figure 14. Forward voltage drop vs. forward current



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Test circuit STGY50NC60WD

## 3 Test circuit

Figure 15. Test circuit for inductive load switching

Figure 16. Gate charge test circuit

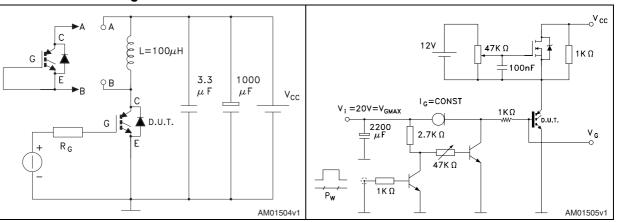
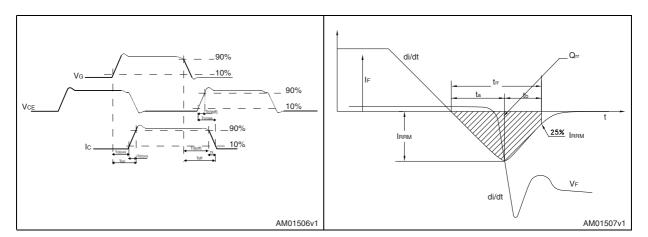


Figure 17. Switching waveform

Figure 18. Diode recovery time waveform



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# 4 Package mechanical data

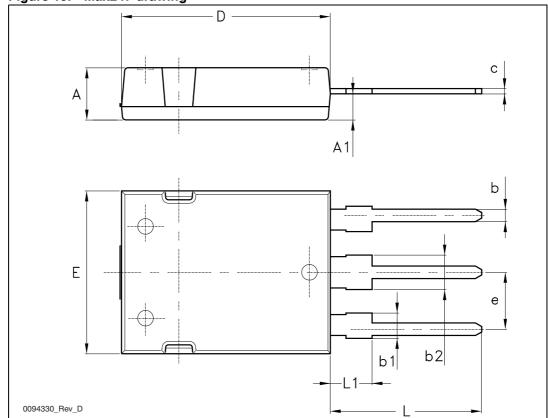
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Table 8. Max247 mechanical data

Direc		mm	
Dim.	Min.	Тур.	Max.
А	4.70		5.30
A1	2.20		2.60
b	1.00		1.40
b1	2.00		2.40
b2	3.00		3.40
С	0.40		0.80
D	19.70		20.30
е	5.35		5.55
E	15.30		15.90
L	14.20		15.20
L1	3.70		4.30

Figure 19. Max247 drawing



STGY50NC60WD Revision history

# 5 Revision history

Table 9. Document revision history

Date	Revision	Changes
09-Oct-2006	1	Initial release.
07-May-2007	2	Complete version
02-Jul-2007	3	Modified value on Table 2: Thermal resistance
04-Nov-2008	4	Table 8: Max247 mechanical data and Figure 19: Max247 drawing have been updated.
09-Jan-2009	5	Figure 13: Turn-off SOA has been updated.

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