

<IGBT Modules>

## CM450DX-13T/CM450DXP-13T

## HIGH POWER SWITCHING USE INSULATED TYPE



- •Flat base type
- Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pin terminals



- Flat base type
- •Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- Tin-plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

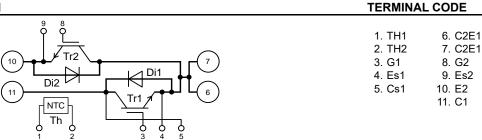
#### APPLICATION

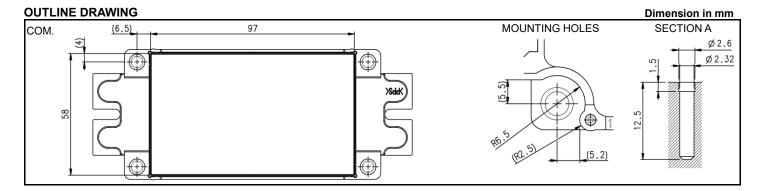
AC Motor Control, Motion/Servo Control, Power supply, etc.

#### OPTION (Below options are available.)

- ●PC-TIM (Phase Change Thermal Interface Material) pre-apply (Note10)
- •V<sub>CEsat</sub> selection for parallel connection

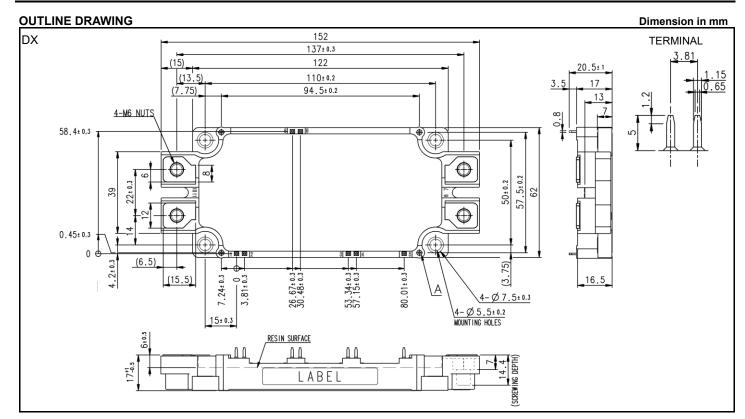
#### **INTERNAL CONNECTION**





HIGH POWER SWITCHING USE

INSULATED TYPE

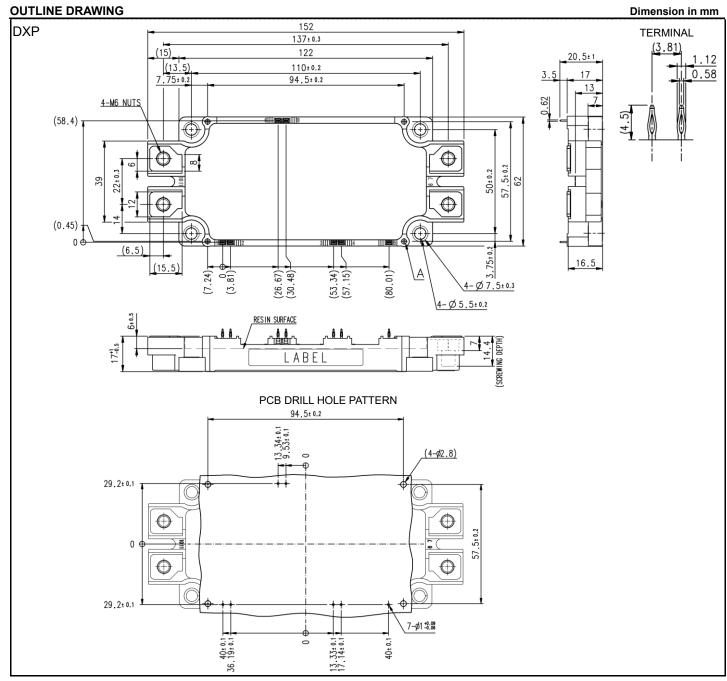


Tolerance otherwise specified

Divisio	n of I	Tolerance		
0.5		to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over 120		to 400		±1.2

HIGH POWER SWITCHING USE

**INSULATED TYPE** 



Tolerance otherwise specified

Divisio	n of I	Tolerance		
	0.5	to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to '	120	±0.8
over	over 120		400	±1.2

HIGH POWER SWITCHING USE

INSULATED TYPE

#### MAXIMUM RATINGS (Tvj=25 °C, unless otherwise specified)

#### INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	650	V
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Calla stan assumant	DC, T <sub>C</sub> =106 °C (Note2, 4)	450	^
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	900	A
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	1685	W
I <sub>E</sub> (Note1)	Citte	DC (Note2)	450	^
I <sub>ERM</sub> (Note1)	Emitter current	Pulse, Repetitive (Note3)	900	A

#### MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
T <sub>vjmax</sub>	Maximum junction temperature	Instantaneous event (overload) (Note10)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note4,10)	125	
T <sub>vjop</sub>	Operating junction temperature	Continuous operation (under switching) (Note10)	-40 ~ +150	°C
T <sub>sta</sub>	Storage temperature	-	-40 ~ +125	

## ELECTRICAL CHARACTERISTICS (T<sub>vj</sub>=25 °C, unless otherwise specified)

Cumbal	Itam	Conditions			Limits		Unit
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I <sub>C</sub> =45 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
V <sub>CEsat</sub> (Terminal)		I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V,	T <sub>vj</sub> =25 °C	-	1.45	1.80	
		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.55	-	V
	Callantan ansistan anti-matian valtana	(Note5)	T <sub>vj</sub> =150 °C	-	1.60	-	
	Collector-emitter saturation voltage	I <sub>C</sub> =450 A,	T <sub>vj</sub> =25 °C	-	1.30	1.55	
V <sub>CEsat</sub>		V <sub>GE</sub> =15 V,	T <sub>vj</sub> =125 °C	-	1.35	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.35	-	
Cies	Input capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	60.1	nF
Coes	Output capacitance			-	-	2.6	
Cres	Reverse transfer capacitance			-	-	1.2	
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =300 V, I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V		-	1.86	-	μC
t <sub>d(on)</sub>	Turn-on delay time	Vcc=300 V, Ic=450 A, V <sub>GE</sub> =±15 V,		-	-	400	ns
tr	Rise time			-	-	200	
t <sub>d(off)</sub>	Turn-off delay time			-	-	400	
t <sub>f</sub>	Fall time	R <sub>G</sub> =1.6 Ω, Inductive load		-	-	400	
(N. t. 4)		I <sub>E</sub> =450 A, G-E short-circuited,	T <sub>vj</sub> =25 °C	-	1.60	2.15	
V <sub>EC</sub> (Note1)		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.65	-	V
(Terminal)	Foreith and a life of a second and	(Note5)	T <sub>vj</sub> =150 °C	-	1.65	-	
(Noted)	Emitter-collector voltage	I <sub>E</sub> =450 A,	T <sub>vj</sub> =25 °C	-	1.45	1.85	
V <sub>EC</sub> (Note1)		G-E short-circuited,	T <sub>vj</sub> =125 °C	-	1.50	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.50	-	
t <sub>rr</sub> (Note1)	Reverse recovery time	V <sub>CC</sub> =300 V, I <sub>E</sub> =450 A, V <sub>GE</sub> =±15 V,			-	400	ns
(Note1)	Reverse recovery charge	R <sub>G</sub> =1.6 Ω, Inductive load		-	31.5	-	μC
Q <sub>rr</sub> (Note1)		V <sub>CC</sub> =300 V, I <sub>C</sub> =I <sub>E</sub> =450 A,			9.5		
E <sub>on</sub> (Note 1)	Turn-on switching energy per pulse	$V_{CC}$ =300 V, $I_{C}$ = $I_{E}$ =450 A,	l	-	9.5	-	
	Turn-on switching energy per pulse Turn-off switching energy per pulse	$V_{\text{CC}}$ =300 V, $I_{\text{C}}$ = $I_{\text{E}}$ =450 A, $V_{\text{GE}}$ =±15 V, $R_{\text{G}}$ =1.6 $\Omega$ , $T_{\text{vj}}$ =150 °C,		-	21.2	-	mJ
Eon	• • • • • • • • • • • • • • • • • • • •						mJ mJ
E <sub>on</sub>	Turn-off switching energy per pulse	$V_{GE}$ =±15 V, $R_{G}$ =1.6 $\Omega$ , $T_{vj}$ =150 °C,	5 °C (Note4)		21.2	-	

HIGH POWER SWITCHING USE

**INSULATED TYPE** 

#### ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

#### NTC THERMISTOR PART

Symbol	lt	Conditions		Unit		
	Item	Conditions	Min.	Тур.	Max.	Onit
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C (Note4)	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note6)	-	3375	-	K
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note4)	-	-	10	mW

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itam	Conditions			Unit		
Symbol	Item			Min.	Тур.	Max.	Offic
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to case, per Inverter IGBT (Note4)		-	-	89	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)		-	-	131	r/KVV
В	Contact thermal resistance	Case to heat sink, Thermal grease applie	d (Note4, 7,10)	-	11.5	-	K/kW
$R_{th(c-s)}$		per 1 module, PC-TIM applied (Note4)	, 8,10)	-	3.1	-	r\/KVV

#### **MECHANICAL CHARACTERISTICS**

Cumbal	Item	Con	Limits			Linit	
Symbol	item	Con	Min.	Тур.	Max.	Unit	
M <sub>t</sub>	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
		Caldennia tura (DV)	Terminal to terminal	17	-	-	
al .	Creepage distance	Solder pin type (DX)	Terminal to base plate	16.4	-	-	mm
ds		Pressfit pin type (DXP)	Terminal to terminal	17	-	-	
			Terminal to base plate	16.8	-	-	mm
		Solder pin type (DX)	Terminal to terminal	10	-	-	mm
al .			Terminal to base plate	16.2	-	-	
d <sub>a</sub>	Clearance	Donas fit win town (DVD)	Terminal to terminal	10	-	-	
		Pressfit pin type (DXP)  Terminal to base plate		16.2	16.2 -	-	mm
ec	Flatness of base plate	On the centerline X, Y	Note9)	±0	-	+200	μm
m	mass	-		-	300	-	g

<sup>\*.</sup> This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

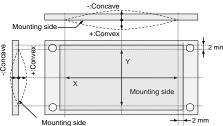
- 2. Junction temperature  $(T_{\nu j})$  should not increase beyond  $T_{\nu j\,m\,a\,x}$  rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (Tvj) dose not exceed Tvjmax rating.
- 4. Case temperature (T<sub>C</sub>) and heat sink temperature (T<sub>S</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6. 
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 $R_{25}$ : resistance at absolute temperature  $T_{25}$  [K];  $T_{25}\text{=-}25$  [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}\!=\!50$  [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K)/D<sub>(C-S)</sub>=50  $\mu$ m.
- 8. Typical value is measured by using PC-TIM of  $\lambda = 3.4~W/(m\cdot K)/D_{\text{(C-S)}} = 50~\mu m.$
- 9. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



10. Long term performance related to thermal conductive grease and PC-TIM (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition (Tv<sub>j max</sub>, Tv<sub>j op</sub>, Tc max) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

#### HIGH POWER SWITCHING USE

INSULATED TYPE

Note11. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness: t1.6

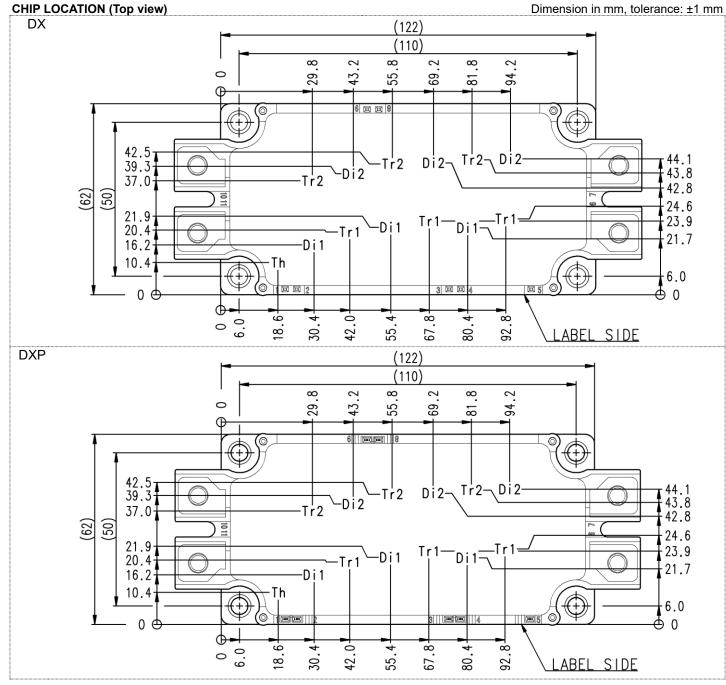
	Туре	Manufacturer	Size	Tightening torque (N·m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.73 ± 0.073 N•III	

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Itama	Conditions		Linit		
Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
V <sub>cc</sub>	(DC) Supply voltage	Applied across C1-E2 terminals	-	300	450	V
$V_{GEon}$	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
R <sub>G</sub>	External gate resistance	Per switch	1.6	-	16	Ω

HIGH POWER SWITCHING USE

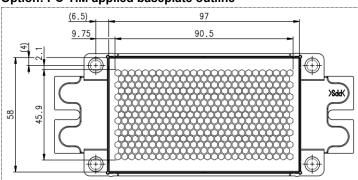
**INSULATED TYPE** 



Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

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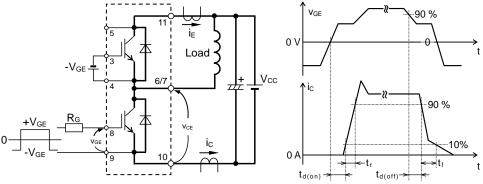
#### Option: PC-TIM applied baseplate outline

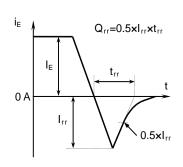


HIGH POWER SWITCHING USE

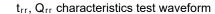
INSULATED TYPE

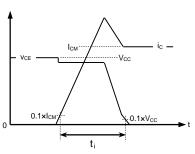
#### **TEST CIRCUIT AND WAVEFORMS**

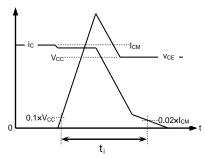


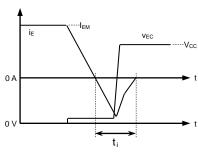


Switching characteristics test circuit and waveforms









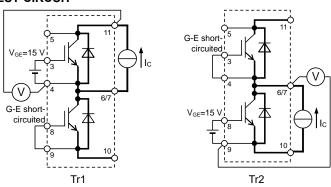
IGBT Turn-on switching energy

IGBT Turn-off switching energy

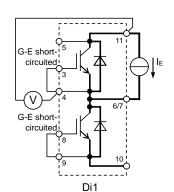
FWD Reverse recovery energy

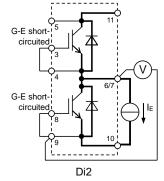
Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

#### **TEST CIRCUIT**









V<sub>EC</sub> characteristics test circuit

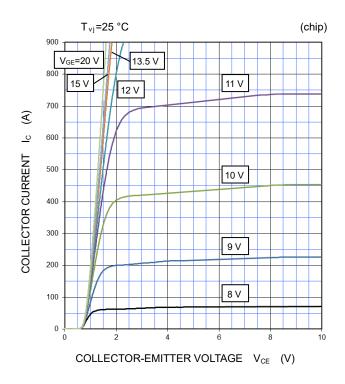
HIGH POWER SWITCHING USE

INSULATED TYPE

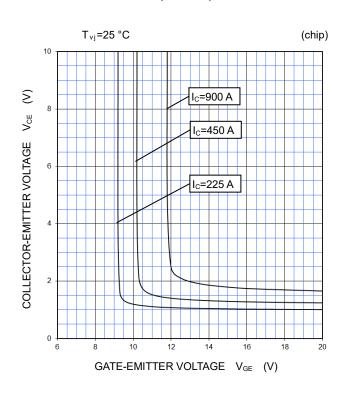
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

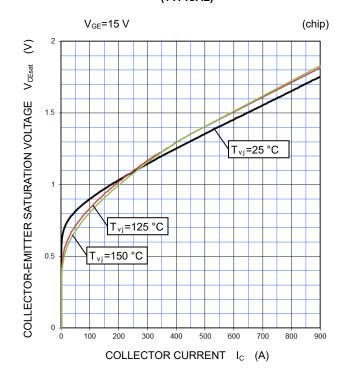
## OUTPUT CHARACTERISTICS (TYPICAL)



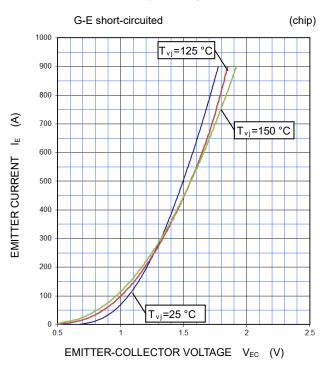
## COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



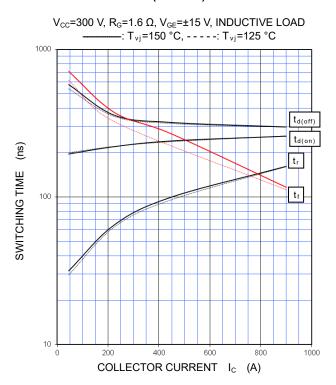
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

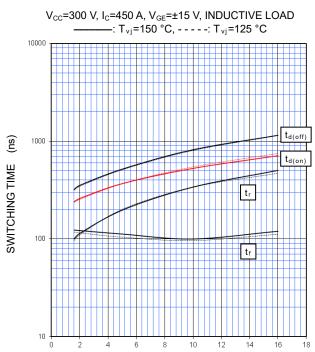
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

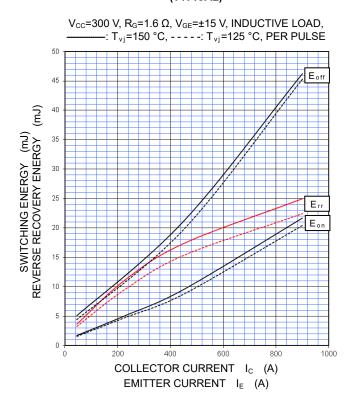
## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



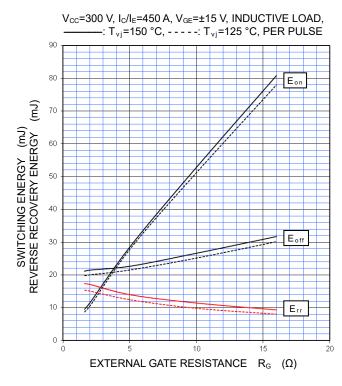
## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

EXTERNAL GATE RESISTANCE RG

 $(\Omega)$ 



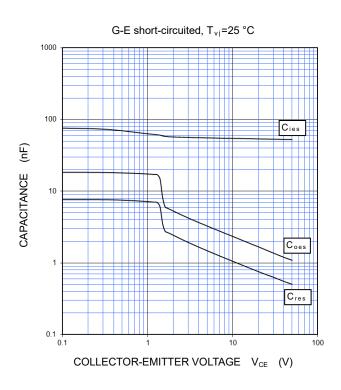
HIGH POWER SWITCHING USE

INSULATED TYPE

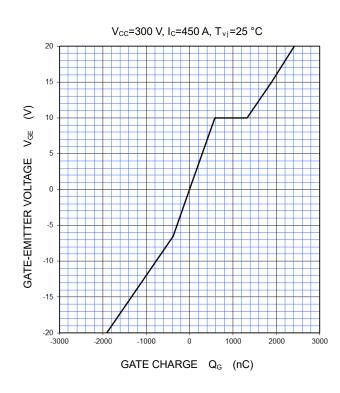
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

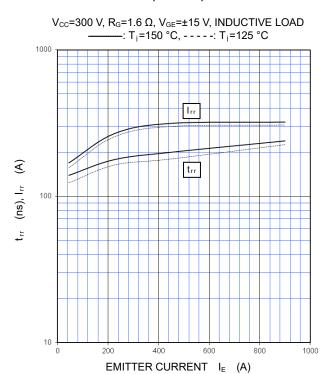
## CAPACITANCE CHARACTERISTICS (TYPICAL)



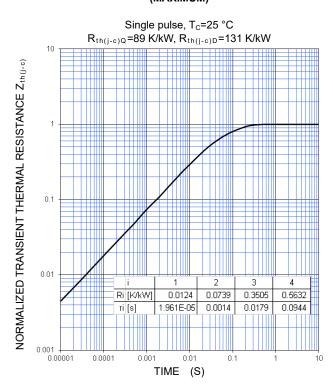
## GATE CHARGE CHARACTERISTICS (TYPICAL)



# FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



HIGH POWER SWITCHING USE

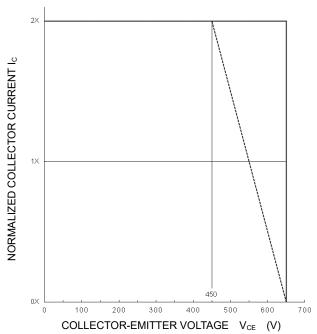
INSULATED TYPE

#### **PERFORMANCE CURVES**

#### **INVERTER PART**

#### TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

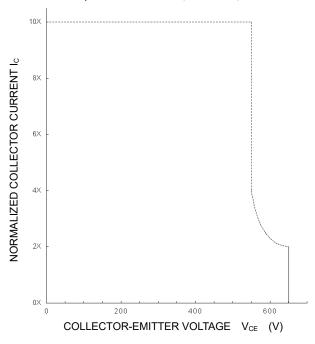
 $V_{\text{CC}} \le 450 \text{ V}$ ,  $R_{\text{G}} = 1.6 \sim 16 \Omega$ ,  $V_{\text{GE}} = \pm 15 \text{ V}$ , ....:  $T_{\text{V}_{\text{I}}} = 25 \sim 150 \,^{\circ}\text{C}$  (Normal load operations (Continuous) ....:  $T_{\text{V}_{\text{J}}} = 175 \,^{\circ}\text{C}$  (Unusual load operations (Limited period)



## (MAXIMUM)

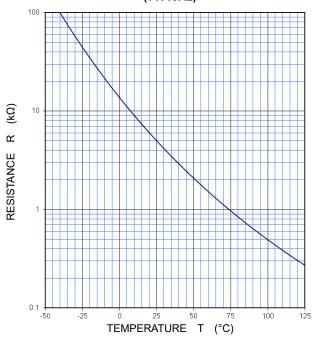
**SHORT-CIRCUIT SAFE OPERATING AREA** 

 $V_{CC} \le 400 \text{ V}$ ,  $R_G = 1.6 \sim 16 \Omega$ ,  $V_{GE} = \pm 15 \text{ V}$ ,  $T_{vj} = 25 \sim 150 \text{ °C}$ ,  $t_W \le 8 \mu s$ , Non-Repetitive



#### NTC thermistor part

## TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

### **Important Notice**

The information contained in this datasheet shall in no event be regarded as a guarantee of conditions or characteristics. This product has to be used within its specified maximum ratings, and is subject to customer's compliance with any applicable legal requirement, norms and standards.

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HIGH POWER SWITCHING USE INSULATED TYPE

### **Keep safety first in your circuit designs!**

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

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