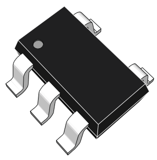
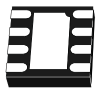
TS3011

Rail-to-rail high-speed comparator

Datasheet - production data

SOT23-5/SC70-5





DFN8 2x2

Features

- Propagation delay: 8 ns
- Low current consumption: 470 μA typ. at 5 V
- Rail-to-rail inputs
- Push-pull outputs
- Supply operation from 2.2 to 5 V
- Wide temperature range: -40 °C to 125 °C
- ESD tolerance: 2 kV HBM/200 V MM
- Latch-up immunity: 200 mA
- SMD packages
- Automotive qualification

Applications

- Telecoms
- Instrumentation
- Signal conditioning
- High-speed sampling systems
- Portable communication systems

Description

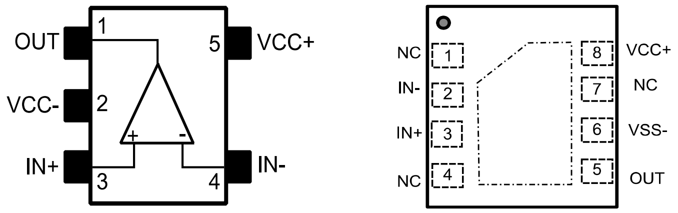
The TS3011 single comparator features a high-speed response time with rail-to-rail inputs. Specified for a supply voltage of 2.2 to 5 V, this comparator can operate over a wide temperature range from -40 °C to 125 °C.

The TS3011 offers micropower consumption as low as a few hundred microamperes, thus providing an excellent ratio of power consumption current versus response time.

The TS3011 includes push-pull outputs and is available in tiny packages to overcome space constraints.

Contents

1 Pin configuration



SOT23-5 and SC70-5

DFN8 2x2

2 Absolute maximum ratings and operating conditions

Table 1: Absolute maximum ratings

<table>
<tr><th>Symbol</th><th colspan="2">Parameter</th><th>Value</th><th>Unit</th></tr>
<tr><td>V_{CC}</td><td colspan="2">Supply voltage (1)</td><td>5.5</td><td rowspan="3">V</td></tr>
<tr><td>V_{ID}</td><td colspan="2">Differential input voltage (2)</td><td>±5</td></tr>
<tr><td>V_{IN}</td><td colspan="2">Input voltage range</td><td>(V_{CC}^-) - 0.3 to (V_{CC}^+) + 0.3</td></tr>
<tr><td rowspan="2">R_{THJA}</td><td rowspan="2">Thermal resistance junction-to-ambient (3)</td><td>SOT23-5</td><td>250</td><td rowspan="4">°C/W</td></tr>
<tr><td>SC70-5</td><td>205</td></tr>
<tr><td rowspan="2">R_{THJC}</td><td rowspan="2">Thermal resistance junction-to-case (3)</td><td>SOT23-5</td><td>81</td></tr>
<tr><td>SC70-5</td><td>172</td></tr>
<tr><td>T_{STG}</td><td colspan="2">Storage temperature</td><td>-65 to 150</td><td rowspan="3">°C</td></tr>
<tr><td>T_J</td><td colspan="2">Junction temperature</td><td>150</td></tr>
<tr><td>T_{LEAD}</td><td colspan="2">Lead temperature (soldering 10 seconds)</td><td>260</td></tr>
<tr><td rowspan="4">ESD</td><td colspan="2">Human body model (HBM) (4)</td><td>2000</td><td rowspan="4">V</td></tr>
<tr><td colspan="2">Machine model (MM) (5)</td><td>200</td></tr>
<tr><td rowspan="2">Charged device model (CDM) (6)</td><td>SOT23-5</td><td>1500</td></tr>
<tr><td>SC70-5</td><td>1300</td></tr>
<tr><td></td><td colspan="2">Latch-up immunity</td><td>200</td><td>mA</td></tr>
</table>

Notes:

(1)All voltage values, except the differential voltage, are referenced to V_{CC}^-.

(2)The magnitude of input and output voltages must never exceed the supply rail ±0.3 V.

(3)Short-circuits can cause excessive heating. These values are typical.

(4)Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.

(5)Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.

(6)Charged device model: all pins and package are charged together to the specified voltage and then discharged directly to ground.

Table 2: Operating conditions

<table>
<tr><th>Symbol</th><th>Parameter</th><th>Value</th><th>Unit</th></tr>
<tr><td>T_{Oper}</td><td>Operating temperature range</td><td>-40 to 125</td><td>°C</td></tr>
<tr><td>V_{CC}</td><td>Supply voltage (V_{CC}^+ - V_{CC}^-), -40 °C < T_{amb} < 125 °C</td><td>2.2 to 5</td><td rowspan="2">V</td></tr>
<tr><td>V_{ICM}</td><td>Common mode input voltage range,
-40 °C < T_{amb} < 125 °C</td><td>(V_{CC}^-) - 0.2 to (V_{CC}^+) + 0.2</td></tr>
</table>

3 Electrical characteristics

In the electrical characteristic tables below, all values over the temperature range are guaranteed through correlation and simulation. No production tests are performed at the temperature range limits.

Table 3: VCC = 2.2 V, VICM = VCC/2,T_{amb} = 25 °C (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{IO}	Input offset voltage (1)		-7	-0.2	7	mV
		-40 °C < T_{amb} < 125 °C	-8		8	
ΔV_{IO}	Input offset voltage drift	-40 °C < T_{amb} < 125 °C		5	20	µV/°C
V_{HYST}	Input hysteresis voltage (2)			2		mV
I_{IO}	Input offset current (3)			1	20	pA
		-40 °C < T_{amb} < 125 °C			100	
I_{IB}	Input bias current			1	20	
		-40 °C < T_{amb} < 125 °C			100	
I_{CC}	Supply current	No load, output high		0.52	0.64	mA
		No load, output high, -40 °C < T_{amb} < 125 °C			0.9	
		No load, output low		0.65	0.88	
		No load, output low, -40 °C < T_{amb} < 125 °C			1.1	
I_{SC}	Short circuit current	Source	14	18		
		Sink	11	14		
V_{OH}	Output voltage high	I_{source} = 4 mA	1.94	1.97		V
		-40 °C < T_{amb} < 125 °C	1.85			
V_{OL}	Output voltage low	I_{sink} = 4 mA		150	190	mV
		-40 °C < T_{amb} < 125 °C			250	
CMRR	Common-mode rejection ratio	0 < V_{ICM} < 2.7 V	50	68		dB
T_{PLH}	Propagation delay, low to high output level (4)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 5 mV		16		ns
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 15 mV		12		
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 50 mV		10	15	
T_{PHL}	Propagation delay, high to low output level (5)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 5 mV		16		
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 15 mV		12		
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 50 mV		10	15	
T_R	Rise time (10 % to 90 %)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 100 mV		3.0		
T_F	Fall time (90 % to 10 %)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 100 mV		2.5		

Notes:

[1]The offset is defined as the average value of positive (V_{TRIP+}) and negative (V_{TRIP-}) trip points (input voltage differences) requested to change the output state in each direction.

[2]Hysteresis is a built-in feature of the TS3011. It is defined as the voltage difference between the trip points.

[3]Maximum values include unavoidable inaccuracies of the industrial tests.

[4]Overdrive is measured with reference to the V_{TRIP+} point.

[5]Overdrive is measured with reference to the V_{TRIP-} point.

Table 4: VCC = 2.7 V, VICM = VCC/2, Tamb = 25 °C (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{IO}	Input offset voltage [1]		-7	-0.1	7	mV
		-40 °C < T_{amb} < 125 °C	-9		9	
ΔV_{IO}	Input offset voltage drift	-40 °C < T_{amb} < 125 °C		5	20	µV/°C
V_{HYST}	Input hysteresis voltage [2]			2		mV
I_{IO}	Input offset current [3]			1	20	pA
		-40 °C < T_{amb} < 125 °C			100	
I_{IB}	Input bias current			1	20	
		-40 °C < T_{amb} < 125 °C			100	
I_{CC}	Supply current	No load, output high		0.52	0.65	mA
		No load, output high, -40 °C < T_{amb} < 125 °C			0.9	
		No load, output low		0.66	0.89	
		No load, output low, -40 °C < T_{amb} < 125 °C			1.1	
I_{SC}	Short circuit current	Source	24	27		
		Sink	19	22		
V_{OH}	Output voltage high	I_{source} = 4 mA	2.48	2.52		V
		-40 °C < T_{amb} < 125 °C	2.40			
V_{OL}	Output voltage low	I_{sink} = 4 mA		130	170	mV
		-40 °C < T_{amb} < 125 °C			220	
CMRR	Common-mode rejection ratio	0 < V_{ICM} < 2.7 V	52	70		dB
T_{PLH}	Propagation delay, low to high output level [4]	C_L = 12 pF, R_L = 1 MΩ, overdrive = 5 mV		16		ns
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 15 mV		11		
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 50 mV		9	13	ns
T_{PHL}	Propagation delay, high to low output level [5]	C_L = 12 pF, R_L = 1 MΩ, overdrive = 5 mV		16		
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 15 mV		11		
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 50 mV		9	13	

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
T_R	Rise time (10 % to 90 %)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 100 mV		2.3		ns
T_F	Fall time (90 % to 10 %)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 100 mV		1.8		

Notes:

(1)The offset is defined as the average value of positive (V_{TRIP+}) and negative (V_{TRIP-}) trip points (input voltage differences) requested to change the output state in each direction.

(2)Hysteresis is a built-in feature of the TS3011. It is defined as the voltage difference between the trip points.

(3)Maximum values include unavoidable inaccuracies of the industrial tests.

(4)Overdrive is measured with reference to the V_{TRIP+} point.

(5)Overdrive is measured with reference to the V_{TRIP-} point.

Table 5: VCC = 5 V, VICM = VCC/2, Tamb = 25 °C (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{IO}	Input offset voltage [1]		-7	-0.4	7	mV
		-40 °C < T_{amb} < 125 °C	-9		9	
ΔV_{IO}	Input offset voltage drift	-40 °C < T_{amb} < 125 °C		10	30	µV/°C
V_{HYST}	Input hysteresis voltage [2]			2		mV
I_{IO}	Input offset current [3]			1	20	pA
		-40 °C < T_{amb} < 125 °C			100	
I_{IB}	Input bias current			1	20	
		-40 °C < T_{amb} < 125 °C			100	
I_{CC}	Supply current	No load, output high		0.47	0.69	mA
		No load, output high, -40 °C < T_{amb} < 125 °C			0.9	
		No load, output low		0.60	0.91	
		No load, output low, -40 °C < T_{amb} < 125 °C			1.1	
I_{SC}	Short circuit current	Source	58	62		
		Sink	58	64		
V_{OH}	Output voltage high	I_{source} = 4 mA	4.84	4.89		V
		-40 °C < T_{amb} < 125 °C	4.80			
V_{OL}	Output voltage low	I_{sink} = 4 mA		90	120	mV
		-40 °C < T_{amb} < 125 °C			180	
CMRR	Common-mode rejection ratio	0 < V_{ICM} < 2.7 V	57	74		dB
SVR	Supply voltage rejection	ΔV_{CC} = 2.2 V to 5 V		79		
T_{PLH}	Propagation delay, low to high output level [4]	C_L = 12 pF, R_L = 1 MΩ, overdrive = 5 mV		14		ns
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 15 mV		10		
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 50 mV		8	11	

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
T_{PHL}	Propagation delay, high to low output level (5)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 5 mV		16		ns
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 15 mV		11		
		C_L = 12 pF, R_L = 1 MΩ, overdrive = 50 mV		9	12	
T_R	Rise time (10 % to 90 %)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 100 mV		1.1		
T_F	Fall time (90 % to 10 %)	C_L = 12 pF, R_L = 1 MΩ, overdrive = 100 mV		1.0		

Notes:

(1)The offset is defined as the average value of positive (V_{TRIP+}) and negative (V_{TRIP-}) trip points (input voltage differences) requested to change the output state in each direction.

(2)Hysteresis is a built-in feature of the TS3011. It is defined as the voltage difference between the trip points.

(3)Maximum values include unavoidable inaccuracies of the industrial tests.

(4)Overdrive is measured with reference to the V_{TRIP+} point.

(5)Overdrive is measured with reference to the V_{TRIP-} point.

4 Electrical characteristic curves

Figure 1: Current consumption vs. power supply voltage - output low

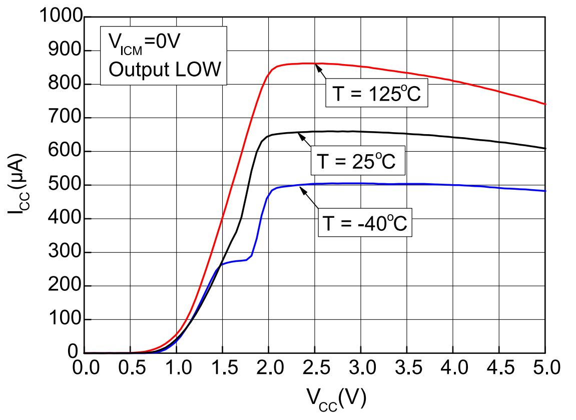


Figure 2: Current consumption vs. power supply voltage - output high

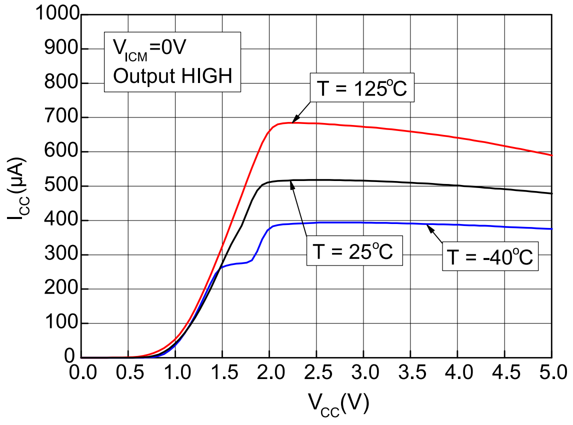


Figure 3: Current consumption vs. temperature

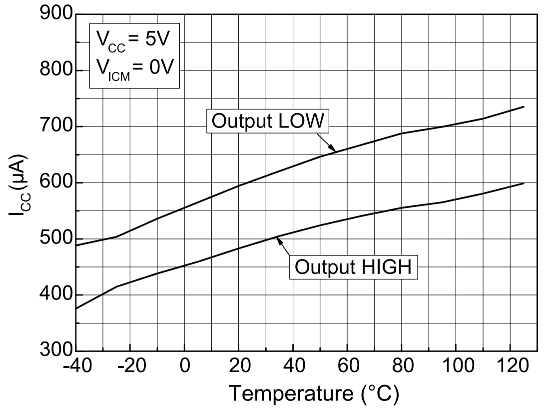


Figure 4: Output voltage vs. sinking current, output low, Vcc = 2.7 V

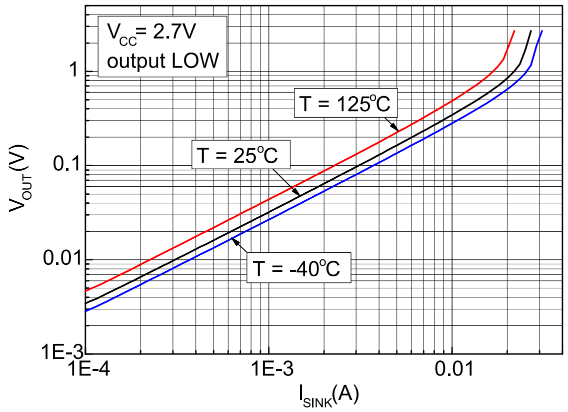


Figure 5: Output voltage vs. sinking current, output low, Vcc = 5 V

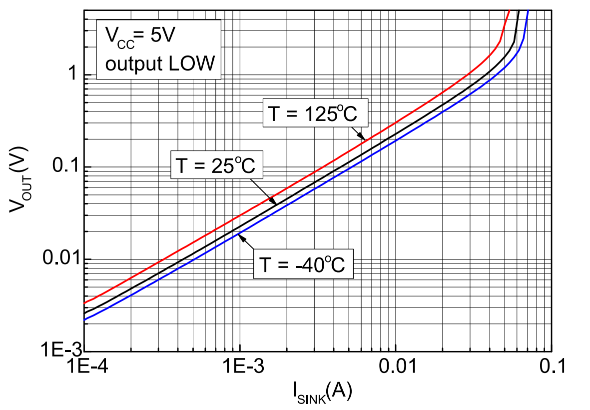


Figure 6: Output voltage drop vs. sourcing current, output high, Vcc = 2.7 V

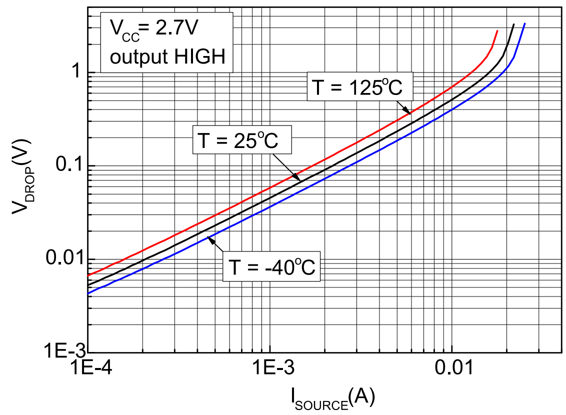


Figure 7: Output voltage drop vs. sourcing current, output high, Vcc = 5 V

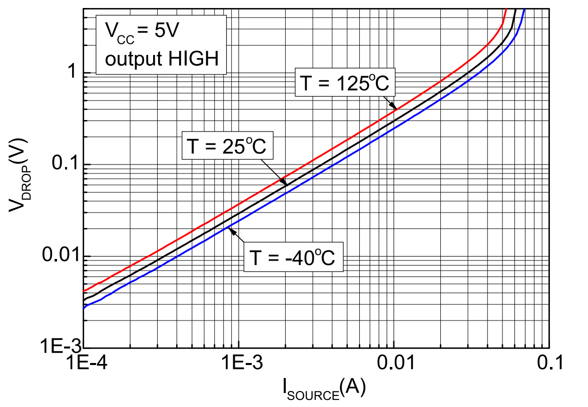


Figure 8: Input offset voltage vs. common mode voltage

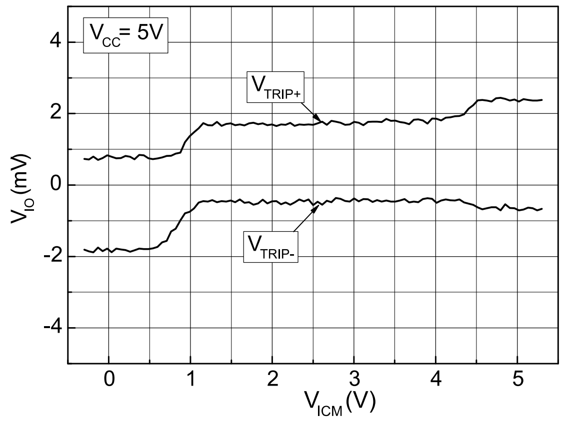


Figure 9: Input offset voltage vs. temperature

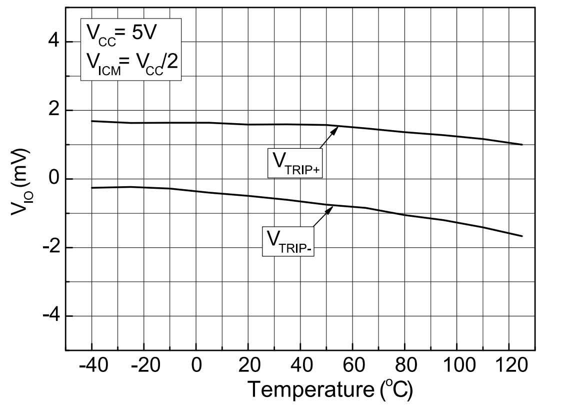


Figure 10: Propagation delay vs. common mode voltage with negative transition

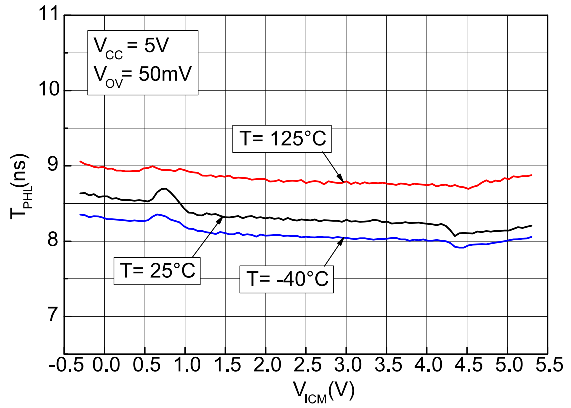


Figure 11: Propagation delay vs. common mode voltage with positive transition

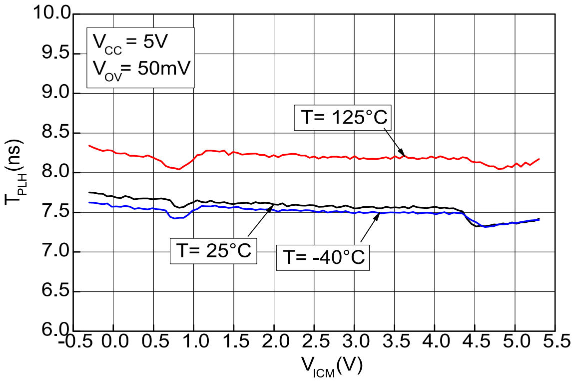


Figure 12: Propagation delay vs. power supply voltage with negative transition

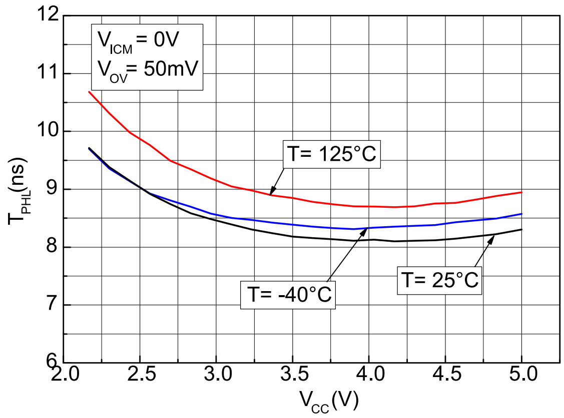


Figure 13: Propagation delay vs. power supply voltage with positive transition

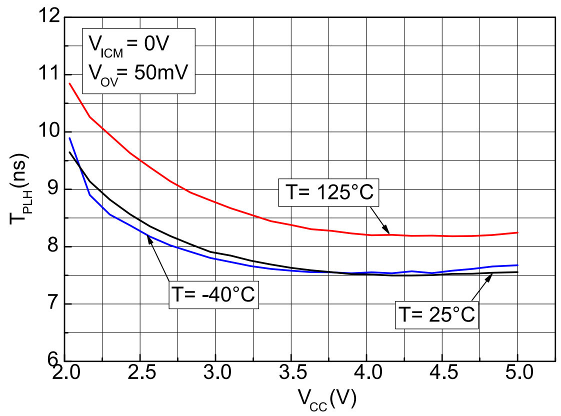


Figure 14: Propagation delay vs. overdrive with negative transition, Vcc = 2.7 V

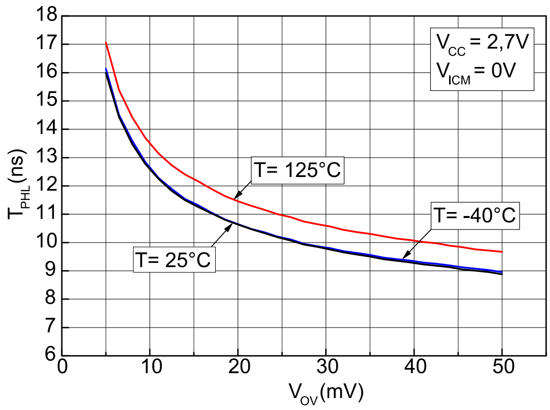


Figure 15: Propagation delay vs. overdrive with positive transition, Vcc = 2.7 V

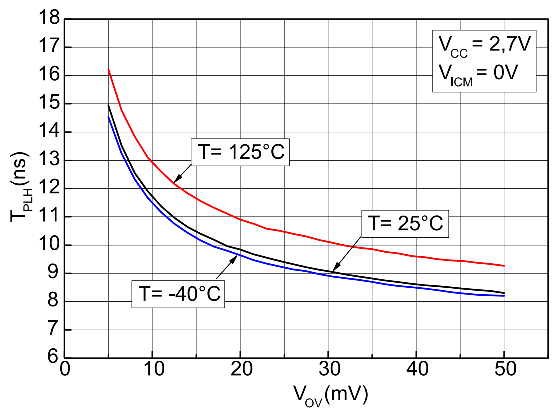


Figure 16: Propagation delay vs. overdrive with negative transition, Vcc = 5 V

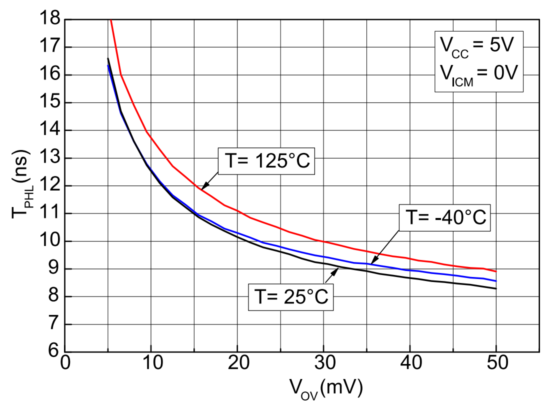


Figure 17: Propagation delay vs. overdrive with positive transition, V_{CC} = 5 V

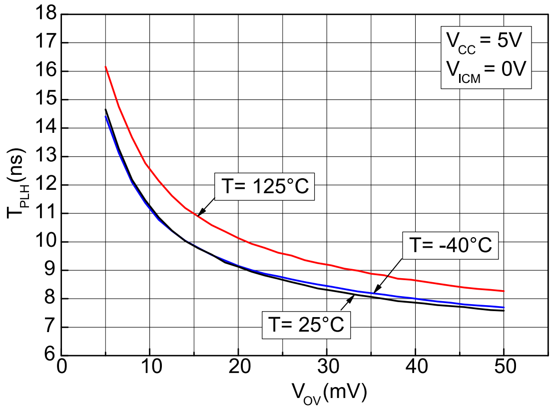
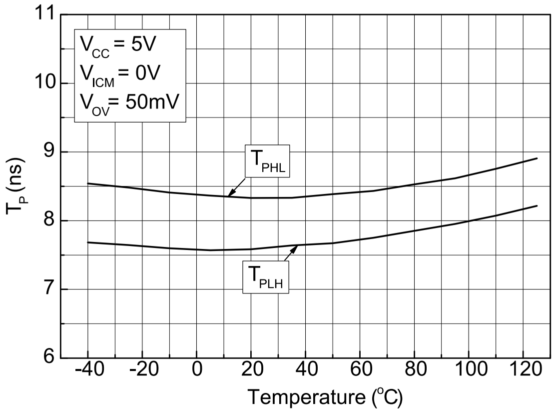


Figure 18: Propagation delay vs. temperature



5 Application recommendation

When high speed comparators are used, it is strongly recommended to place a capacitor as close as possible to the supply pins. Decoupling has two main advantages for this application: it helps to reduce electromagnetic interference and rejects the ripple that may appear on the output.

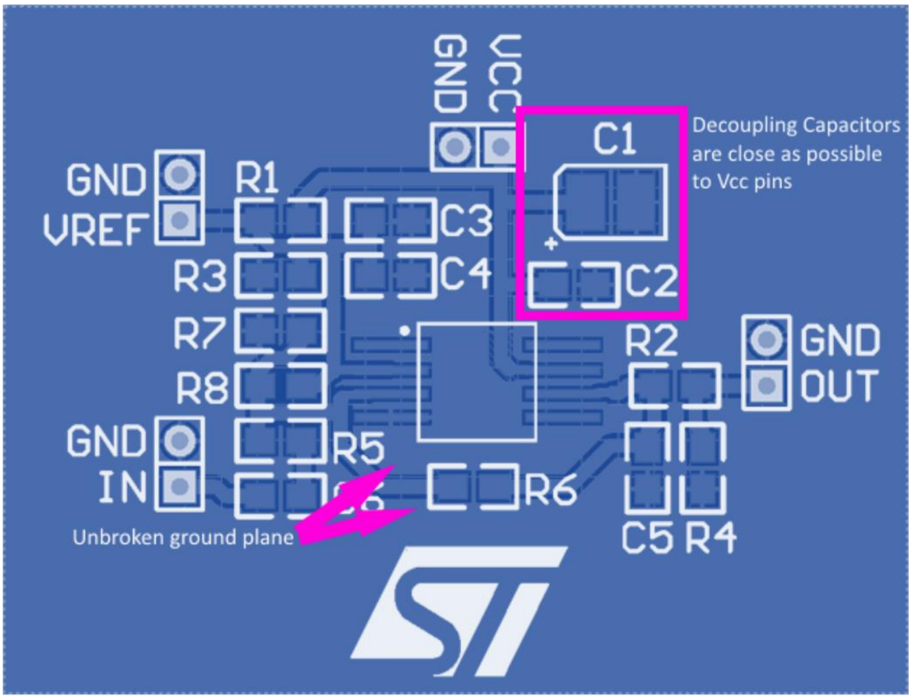
A bypass capacitor combination, composed of 100 nF in addition to 10 nF and 1 nF in parallel is recommended because it eliminates spikes on the supply line better than a single 100 nF capacitor. Each millimeter of the PCB track plays an important role. Bypass capacitors must be placed as close as possible to the comparator supply pin. The smallest value capacitor should be preferably placed closer to the supply pin.

In addition, important values of input impedance in series with parasitic PCB capacity and input comparator capacity create an additional RC filter. It generates an additional propagation delay.

For high speed signal applications, PCB must be designed with great care taking into consideration low resistive grounding, short tracks and quality SMD capacitors featuring low ESR. Bypass capacitor stores energy and provides a complementary energy tank when spikes occur on the power supply line. If the input signal frequency is far from the resonant frequency, impedance strongly increases and the capacitor loses bypassing capability. Placing different capacitors with different resonant frequencies allows a wide frequency bandwidth to be covered.

It is also recommended to implement an unbroken ground plane with low inductance.

Figure 19: High speed layout recommendation



6 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: ***www.st.com***. ECOPACK® is an ST trademark.

6.1 SOT23-5 package information

Figure 20: SOT23-5 package outline

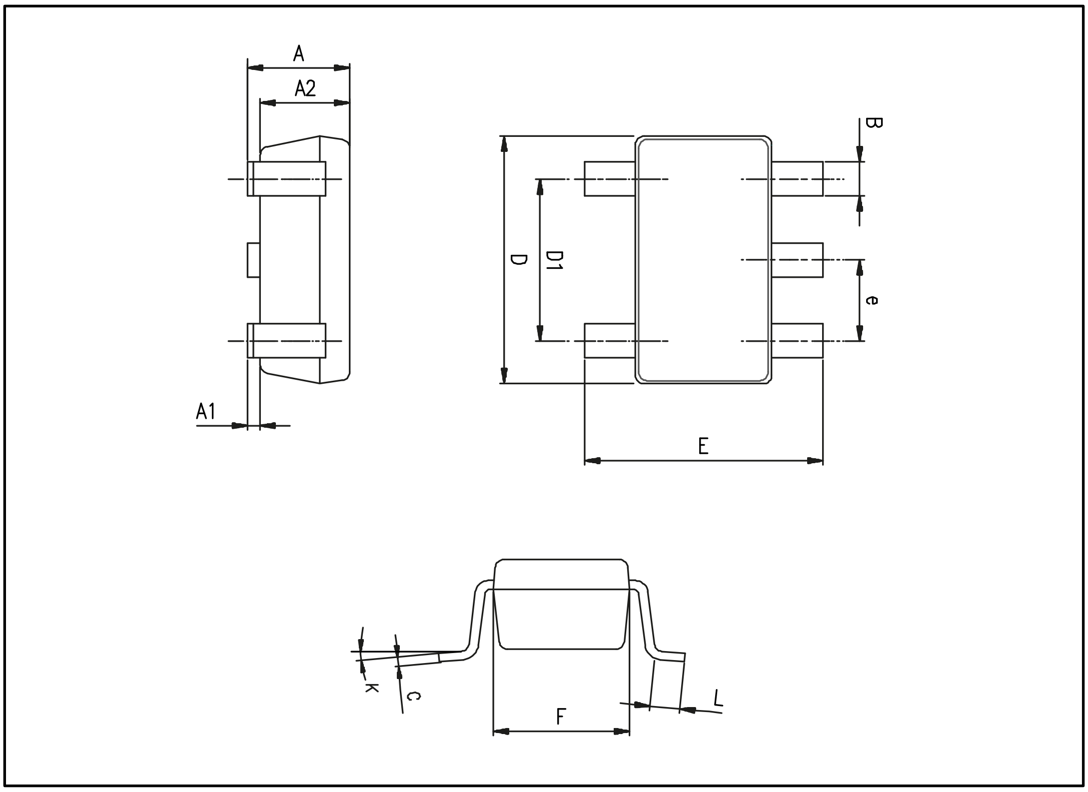


Table 6: SOT23-5 mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.90	1.20	1.45	0.035	0.047	0.057
A1			0.15			0.006
A2	0.90	1.05	1.30	0.035	0.041	0.051
B	0.35	0.40	0.50	0.014	0.016	0.020
C	0.09	0.15	0.20	0.004	0.006	0.008
D	2.80	2.90	3.00	0.110	0.114	0.118
D1		1.90			0.075	
e		0.95			0.037	
E	2.60	2.80	3.00	0.102	0.110	0.118
F	1.50	1.60	1.75	0.059	0.063	0.069
L	0.10	0.35	0.60	0.004	0.014	0.024
K	0 degrees		10 degrees	0 degrees		10 degrees

6.2 SC70-5 (or SOT323-5) package information

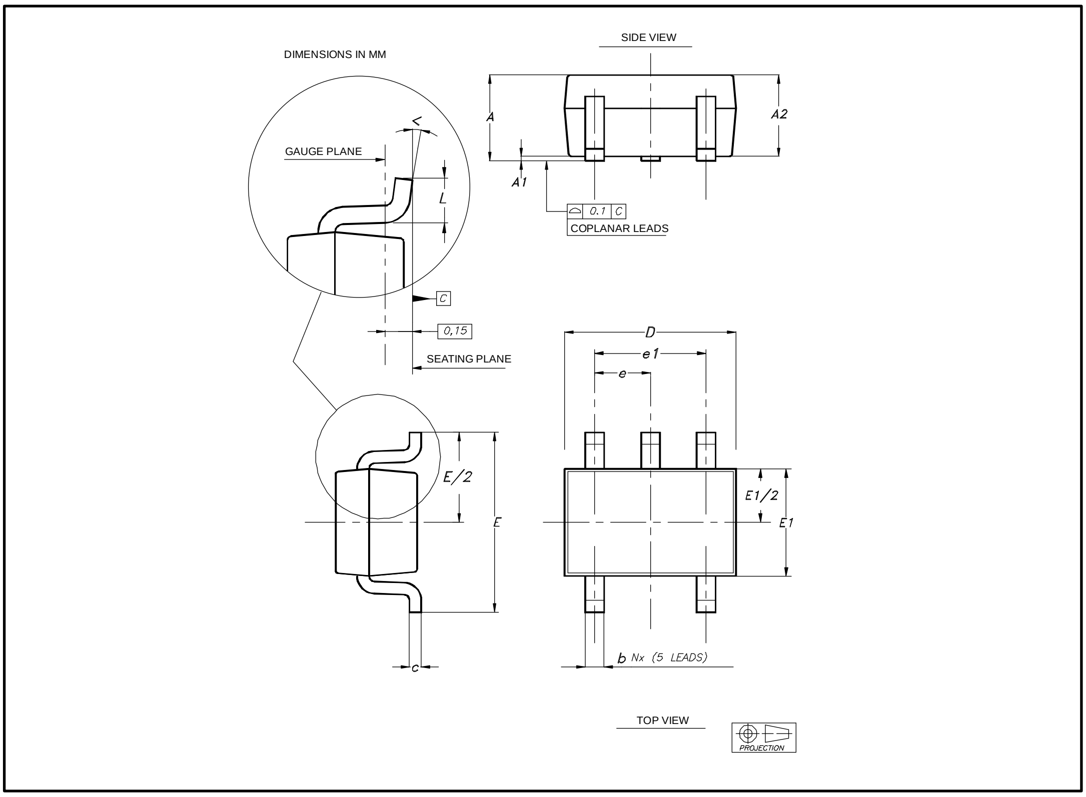


Figure 21: SC70-5 (or SOT323-5) package outline

Table 7: SC70-5 (or SOT323-5) mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.80		1.10	0.032		0.043
A1			0.10			0.004
A2	0.80	0.90	1.00	0.032	0.035	0.039
b	0.15		0.30	0.006		0.012
c	0.10		0.22	0.004		0.009
D	1.80	2.00	2.20	0.071	0.079	0.087
E	1.80	2.10	2.40	0.071	0.083	0.094
E1	1.15	1.25	1.35	0.045	0.049	0.053
e		0.65			0.025	
e1		1.30			0.051	
L	0.26	0.36	0.46	0.010	0.014	0.018
<	0°		8°	0°		8°

6.3 DFN8 2x2 mm package information

Figure 22: DFN8 2x2 mm package outline

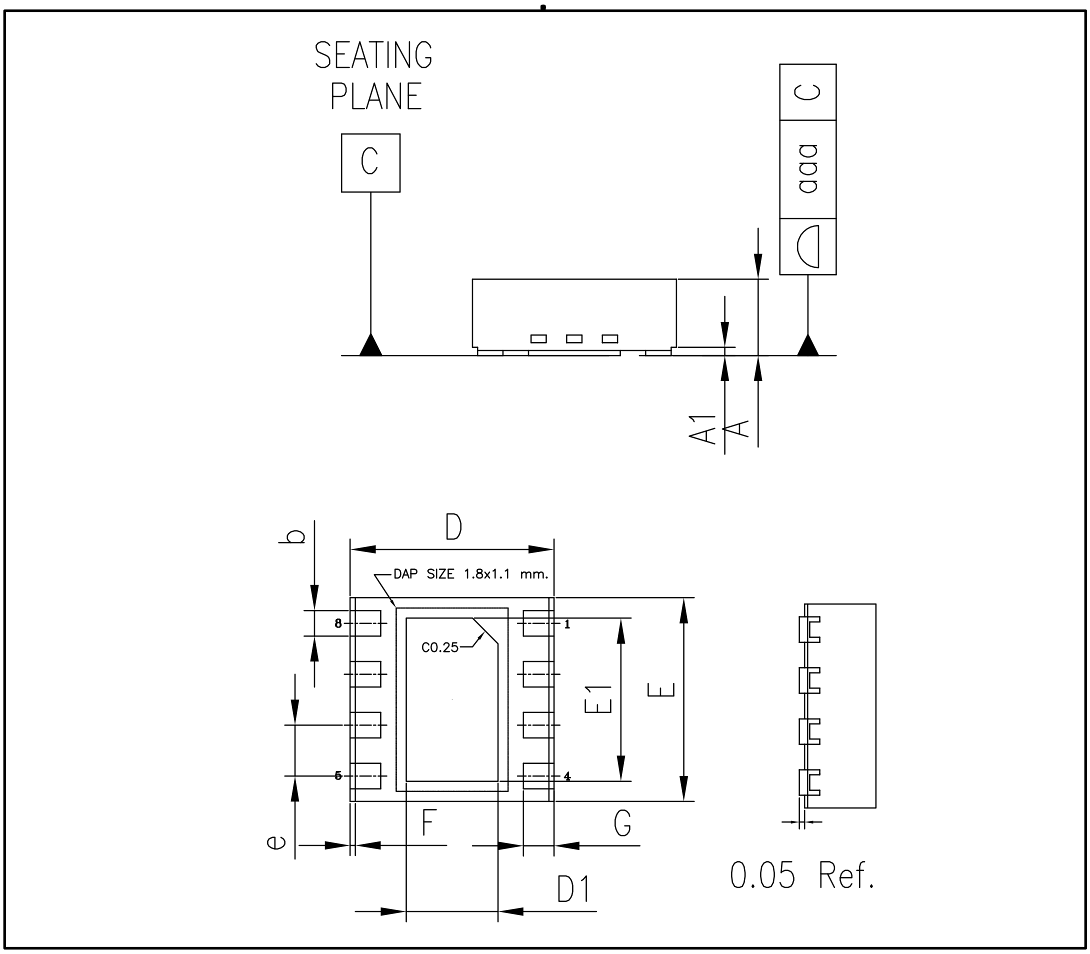


Table 8: DFN8 2x2 mm package mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.70	0.75	0.80	0.027	0.029	0.031
A1		0.10			0.003	
b	0.20	0.25	0.30	0.007	0.009	0.011
D	1.95	2.00	2.05	0.076	0.078	0.080
D1	0.80	0.90	1.00	0.031	0.035	0.039
E	1.95	2.00	2.05	0.076	0.078	0.080
E1	1.50	1.60	1.70	0.059	0.062	0.066
e		0.50			0.019	
F		0.05			0.001	
G	0.25	0.30	0.35	0.009	0.011	0.013
aaa		0.10			0.003	

7 Ordering information

Table 9: Order code

Order code	Temperature range	Package	Packaging	Marking
TS3011ILT	-40 °C to 125 °C	SOT23-5	Tape and reel	K540
TS3011IYLT [(1)]				K541
TS3011ICT		SC70-5		K54
TS3011IYQ3T[(1)]		DFN8 2x2		K5N

Notes:

[(1)] Qualified and characterized according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 and Q 002 or equivalent.

8 Revision history

Table 10: Document revision history

Date	Revision	Changes
03-Oct-2011	1	Initial release.
18-Feb-2014	2	Updated Table 8: Order codes to add the order code TS3011IYLT. Added: Automotive qualification among the Features in the cover page.
27-May-2016	3	Updated document layout Section 3: "Electrical characteristics": updated unit of "Input offset voltage drift" parameter to µV/°C (not mV/°C). Section 4: "Electrical characteristic curves": X-axes changed to mV (not V) in figures 15, 16, 17, and 18. Table 6: added “K” values for inches Table 7: updated A and A2 min values for inches and added "<" values for inches.
25-Aug-2017	4	Updated cover page image and description. Updated Figure 1: "Pin connections (top view)" and Table 9: "Order codes". Added Section 5.3: "TS3011 DFN package information".
07-Dec-2017	5	Updated *Section 1: "Pin configuration"*. Added *Section 5: "Application recommendation"*.