Inverting Schmitt trigger Rev. 10 — 12 January 2022

1. General description

The 74AHC1G14 and 74AHCT1G14 are single inverters with Schmitt-trigger inputs. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

The AHC device has CMOS input switching levels and supply voltage range 2 V to 5.5 V.

The AHCT device has TTL input switching levels and supply voltage range 4.5 V to 5.5 V.

2. Features and benefits

- Wide supply voltage range from 2.0 V to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- CMOS low power dissipation
- Symmetrical output impedance
- High noise immunity
- Latch-up performance exceesds 100 mA per JESD78 Class II Level A
- ESD protection:
 - HBM JESD22-A114E: exceeds 2000 V
 - MM JESD22-A115-A: exceeds 200 V
 - CDM JESD22-C101C: exceeds 1000 V
 - Specified from -40 °C to +125 °C

3. Applications

- Wave and pulse shapers
- Astable multivibrators
- Monostable multivibrators

4. Ordering information

Table 1. Ordering information

Type number	Package								
	Temperature range	Name	Description	Version					
74AHC1G14GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package;	SOT353-1					
74AHCT1G14GW			5 leads; body width 1.25 mm						
74AHC1G14GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753					
74AHCT1G14GV									

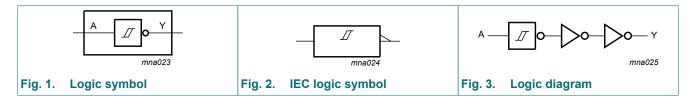
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5. Marking

Table 2. Marking codes						
Type number	Marking code[1]					
74AHC1G14GW	AF					
74AHCT1G14GW	CF					
74AHC1G14GV	A14					
74AHCT1G14GV	C14					

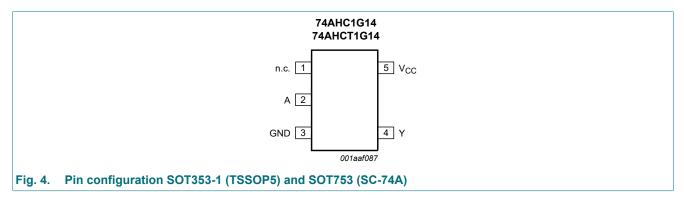
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

6. Functional diagram



7. Pinning information

7.1. Pinning



7.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
n.c.	1	not connected
A	2	data input
GND	3	ground (0 V)
Y	4	data output
V _{CC}	5	supply voltage

74AHC_AHCT1G14

2/14

8. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level

Input	Output
Α	Y
L	Н
Н	L

9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I _{IK}	input clamping current	V _I < -0.5 V	-20	-	mA
I _{OK}	output clamping current	$V_{\rm O} < -0.5 \text{ V or } V_{\rm O} > V_{\rm CC} + 0.5 \text{ V}$ [1]	l -	±20	mA
I _O	output current	$-0.5 V < V_O < V_{CC} + 0.5 V$	-	±25	mA
I _{CC}	supply current		-	75	mA
I _{GND}	ground current		-75	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \ to + 125 \ ^{\circ}C$ [2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C. For SOT753 (SC-74A) package: P_{tot} derates linearly with 3.8 mW/K above 85 °C.

10. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	74AHC1G14			74AHCT1G14			Unit	
			Min	Тур	Max	Min	Тур	Max		
V _{CC}	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V	
VI	input voltage		0	-	5.5	0	-	5.5	V	
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V	
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C	

11. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Мах	Min	Max	1
74AHC1	G14	·								
V _{OH}	HIGH-level	$V_{I} = V_{T+}$ or V_{T-}								
	output voltage	I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I _O = -8.0 mA; V _{CC} = 4.5 V	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level	$V_{I} = V_{T+} \text{ or } V_{T-}$								
	output voltage	I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I _O = 8.0 mA; V _{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
l	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	1.0	-	10	-	40	μA
CI	input capacitance		-	1.5	10	-	10	-	10	pF
74AHCT	1G14	I		1		1	1			-
V _{OH}	HIGH-level	$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	output voltage	Ι _Ο = -50 μΑ	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -8.0 mA	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level	$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 8.0 mA	-	-	0.36	-	0.44	-	0.55	V
lı	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	1.0	-	10	-	40	μA
ΔI _{CC}	additional supply current	per input pin; V _I = 3.4 V; other inputs at V _{CC} or GND; I _O = 0 A; V _{CC} = 5.5 V	-	-	1.35	-	1.5	-	1.5	mA
CI	input capacitance		-	1.5	10	-	10	-	10	pF

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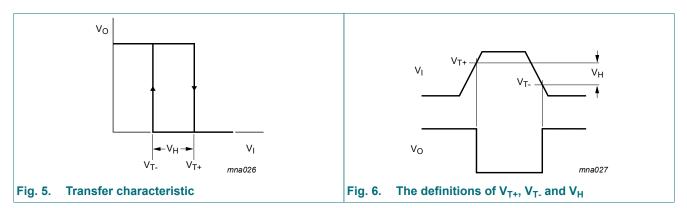
11.1. Transfer characteristics

Table 8. Transfer characteristics

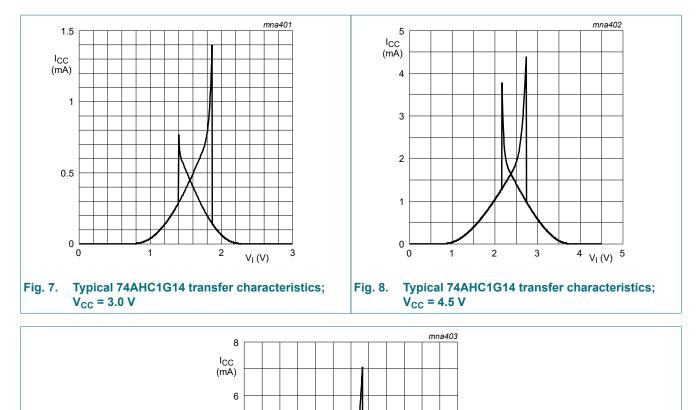
At recommended operating conditions; voltages are referenced to GND (ground = 0 V). See Fig. 5 and Fig. 6.

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
74AHC1	G14					1				
V _{T+} po	positive-going	V _{CC} = 3.0 V	-	-	2.2	-	2.2	-	2.2	V
	threshold voltage	V _{CC} = 4.5 V	-	-	3.15	-	3.15	-	3.15	V
	Voltage	V _{CC} = 5.5 V	-	-	3.85	-	3.85	-	3.85	V
V _{T-}	negative-going	V _{CC} = 3.0 V	0.9	-	-	0.9	-	0.9	-	V
	threshold voltage	V _{CC} = 4.5 V	1.35	-	-	1.35	-	1.35	-	V
	Voltage	V _{CC} = 5.5 V	1.65	-	-	1.65	-	1.65	-	V
V _H	hysteresis	V _{CC} = 3.0 V	0.3	-	1.2	0.3	1.2	0.25	1.2	V
	voltage	V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.35	1.4	V
		V _{CC} = 5.5 V	0.5	-	1.6	0.5	1.6	0.45	1.6	V
74AHCT	1G14		I							
V _{T+}	positive-going	V _{CC} = 4.5 V	-	-	2.0	-	2.0	-	2.0	V
	threshold voltage	V _{CC} = 5.5 V	-	-	2.0	-	2.0	-	2.0	V
V _{T-}	negative-going	V _{CC} = 4.5 V	0.5	-	-	0.5	-	0.5	-	V
	threshold voltage	V _{CC} = 5.5 V	0.6	-	-	0.6	-	0.6	-	V
V _H	hysteresis	V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.35	1.4	V
ľ	voltage	V _{CC} = 5.5 V	0.4	-	1.6	0.4	1.6	0.35	1.6	V

11.2. Transfer characteristic waveforms



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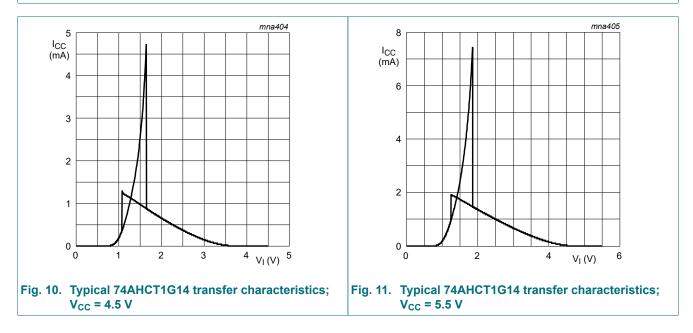




4

2

0



2

4

 $V_{I}(V)$

6

12. Dynamic characteristics

Table 9. Dynamic characteristics

GND = 0 V; $t_r = t_f \le 3.0$ ns. For waveform see Fig. 12. For test circuit see Fig. 13.

Symbol	Parameter	Conditions			25 °C		-40 °C	to +85 °C	-40 °C t	o +125 °C	Unit
			Min	Тур	Max	Min	Мах	Min	Мах		
74AHC1	G14	1					1		1		
t _{pd} propaga	propagation	A to Y;	[1]								
	delay	V _{CC} = 3.0 V to 3.6 V	[2]								
		C _L = 15 pF		-	4.2	12.8	1.0	15.0	1.0	16.5	ns
		C _L = 50 pF		-	6.0	16.3	1.0	18.5	1.0	20.5	ns
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	3.2	8.6	1.0	10.0	1.0	11.0	ns
		C _L = 50 pF		-	4.6	10.6	1.0	12.0	1.0	13.5	ns
C _{PD}	power dissipation capacitance	per buffer; C_L = 50 pF; f = 1 MHz; V _I = GND to V _{CC}	[4]	-	12	-	-	-	-	-	pF
74AHCT	1G14										
t _{pd}	propagation delay	A to Y; V _{CC} = 4.5 V to 5.5 V	[1][3]								
		C _L = 15 pF		-	4.1	7.0	1.0	8.0	1.0	9.0	ns
		C _L = 50 pF		-	5.9	8.5	1.0	10.0	1.0	11.0	ns
C _{PD}	power dissipation capacitance	per buffer; $V_I = GND$ to V_{CC}	[4]	-	13	-	-	-	-	-	pF

[1]

 t_{pd} is the same as t_{PLH} and $t_{PHL}.$ Typical values are measured at V_{CC} = 3.3 V. Typical values are measured at V_{CC} = 5.0 V. [2] [3]

[4] C_{PD} is used to determine the dynamic power dissipation P_D (μ W).

 $P_{D} = C_{PD} \times V_{CC}^{2} \times f_{i} + \sum (C_{L} \times V_{CC}^{2} \times f_{o}) \text{ where:}$

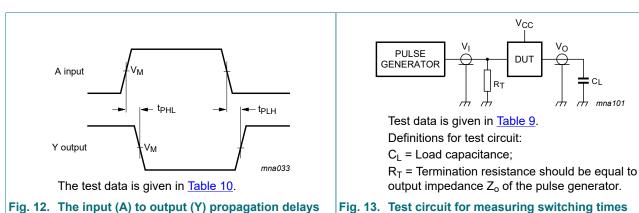
 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in Volts.

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12.1. Waveform and test circuit

Table 10. Test data

Type number	Input	Input			
	VI	V _M	V _M		
74AHC1G14	GND to V _{CC}	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$		
74AHCT1G14	GND to 3.0 V	1.5 V	0.5 × V _{CC}		

13. Application information

The slow input rise and fall times cause additional power dissipation, which can be calculated using the following formula:

 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC}$ where:

- P_{add} = additional power dissipation (μW);
- f_i = input frequency (MHz);
- t_r = input rise time (ns); 10 % to 90 %;
- t_f = input fall time (ns); 90 % to 10 %;
- $\Delta I_{CC(AV)}$ = average additional supply current (µA).

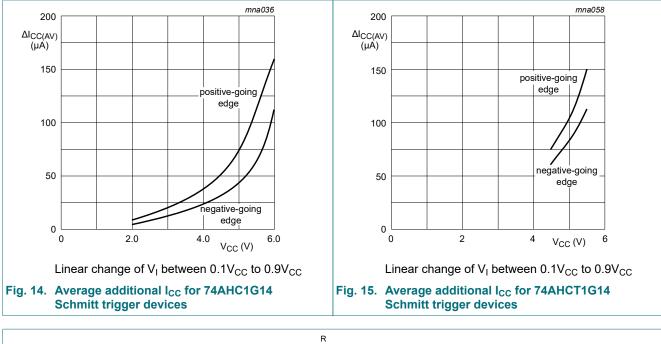
Average additional I_{CC} differs with positive or negative input transitions, as shown in Fig. 14 and Fig. 15.

For 74AHC1G14 and 74AHCT1G14 used in relaxation oscillator circuit, see Fig. 16.

Note to the application information:

• All values given are typical unless otherwise specified.

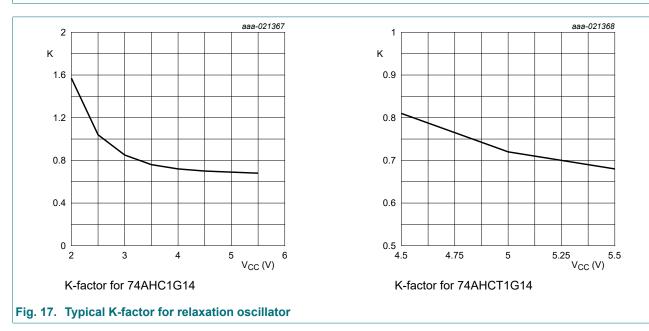
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 $f = \frac{1}{T} \approx \frac{1}{K \times \text{RC}}$
For K-factor, see Fig. 17.





74AHC_AHCT1G14

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14. Package outline

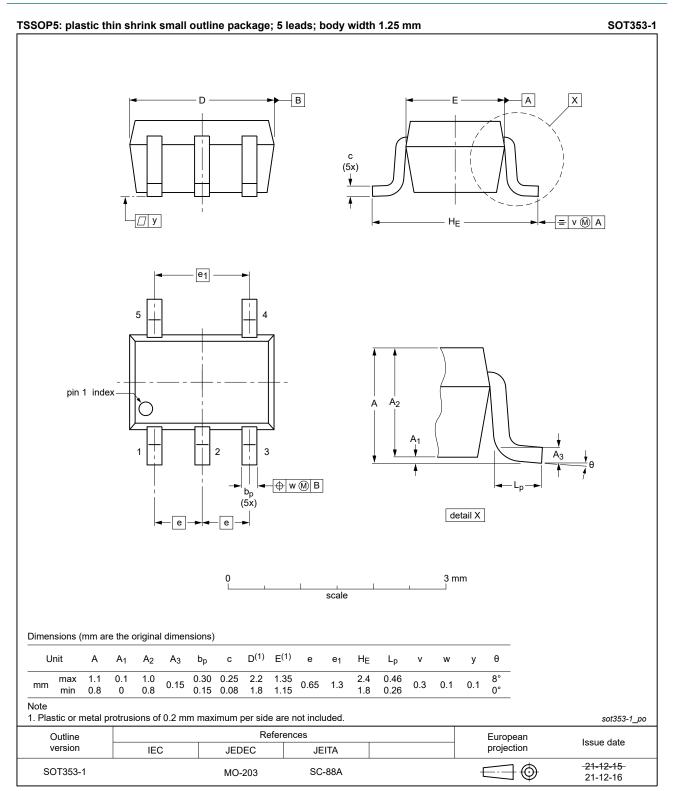


Fig. 18. Package outline SOT353-1 (TSSOP5)

74AHC_AHCT1G14

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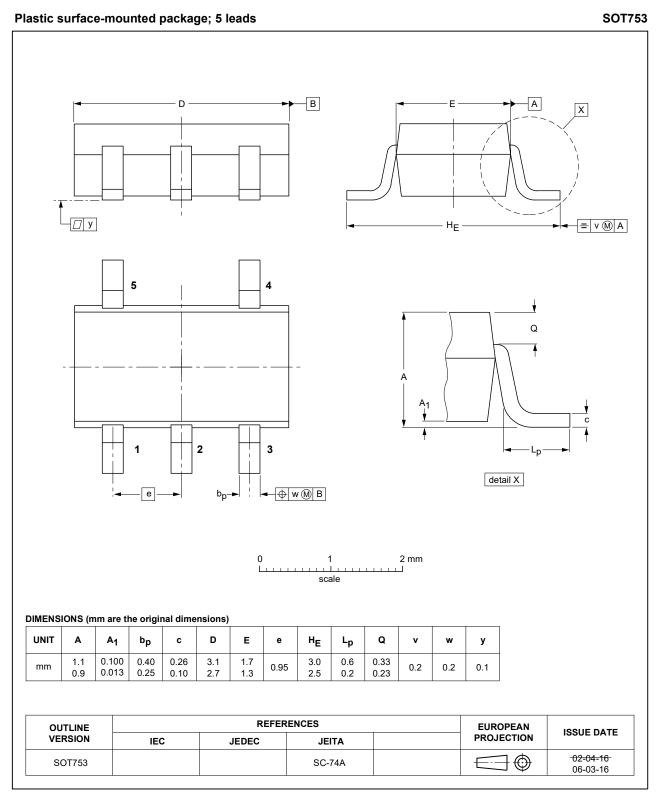


Fig. 19. Package outline SOT753 (SC-74A)

15. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

16. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes				
74AHC_AHCT1G14 v.10	20220112	Product data sheet	-	74AHC_AHCT1G14 v.9				
Modifications:	 <u>Section 1</u> and <u>Section 2</u> updated. <u>Fig. 18</u>: Package outline drawing for SOT353-1 (TSSOP5) has changed. 							
74AHC_AHCT1G14 v.9	20200403	Product data sheet	-	74AHC_AHCT1G14 v.8				
Modifications:	Nexperia. Legal texts have be 		company name where	n the identity guidelines of appropriate.				
74AHC_AHCT1G14 v.8	20160113	Product data sheet	-	74AHC_AHCT1G14 v.7				
Modifications:	• Fig. 17 added (typi	cal K-factor for relaxation	on oscillator).					
74AHC_AHCT1G14 v.7	20141118	Product data sheet	-	74AHC_AHCT1G14 v.6				
Modifications:	• <u>Table 2</u> : table note	added.						
74AHC_AHCT1G14 v.6	20090518	Product data sheet	-	74AHC_AHCT1G14 v.5				
Modifications:	• <u>Table 7</u> : the conditi been changed.	ons for HIGH-level out	but voltage and LOW-le	vel output voltage have				
74AHC_AHCT1G14 v.5	20070629	Product data sheet	-	74AHC_AHCT1G14 v.4				
74AHC_AHCT1G14 v.4	20020528	Product specification	-	74AHC_AHCT1G14 v.3				
74AHC_AHCT1G14 v.3	20020218	Product specification	-	74AHC_AHCT1G14 v.2				
74AHC_AHCT1G14 v.2	20010222	Product specification	-	74AHC_AHCT1G14 v.1				
74AHC_AHCT1G14 v.1	19990805	Product specification	-	-				

17. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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