74AUP1G332

Low-power 3-input OR-gate

Rev. 6 — 3 December 2020

Product data sheet

1. General description

The 74AUP1G332 provides a single 3-input OR gate.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- · High noise immunity
- · Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; I_{CC} = 0.9 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- · Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



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3. Ordering information

Table 1. Ordering information

Type number	Package								
	Temperature range	Name	Description	Version					
74AUP1G332GW	-40 °C to +125 °C	SC-88	plastic surface-mounted package; 6 leads	SOT363					
74AUP1G332GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886					
74AUP1G332GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115					
74AUP1G332GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202					

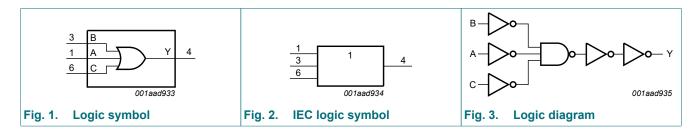
4. Marking

Table 2. Marking

Type number	Marking code [1]
74AUP1G332GW	aG
74AUP1G332GM	aG
74AUP1G332GN	aG
74AUP1G332GS	aG

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

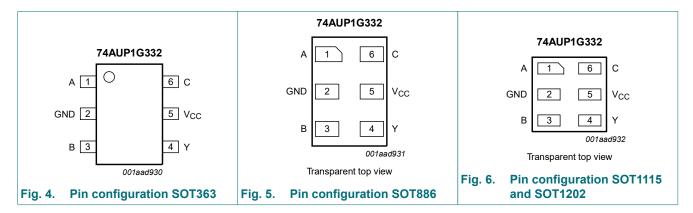
5. Functional diagram



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6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
A	1	data input A
GND	2	ground (0 V)
В	3	data input B
Υ	4	data output Y
V _{CC}	5	supply voltage
С	6	data input C

7. Functional description

Table 4. Function table

 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level; \ X = don't \ care.$

Input	Output		
Α	В	С	Υ
Н	X	X	Н
X	Н	X	Н
X	X	Н	Н
L	L	L	L

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
Io	output current	$V_O = 0 V \text{ to } V_{CC}$	-	±20	mA
I _{CC}	supply current		-	50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$ [2]	-	250	mW

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

9. Recommended operating conditions

Table 6. Recommended operating conditions

auto of Roseminiana operating containing										
Parameter	Conditions	Min	Max	Unit						
supply voltage		0.8	3.6	V						
input voltage		0	3.6	V						
output voltage	Active mode	0	V _{CC}	V						
	Power-down mode; V _{CC} = 0 V	0	3.6	V						
ambient temperature		-40	+125	°C						
input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	0	200	ns/V						
	Parameter supply voltage input voltage output voltage ambient temperature	Parameter Conditions supply voltage Input voltage output voltage Active mode Power-down mode; V _{CC} = 0 V	ParameterConditionsMinsupply voltage 0.8 input voltage 0 output voltageActive mode 0 Power-down mode; $V_{CC} = 0 V$ 0 ambient temperature -40	ParameterConditionsMinMaxsupply voltage 0.8 3.6 input voltage 0 3.6 output voltageActive mode 0 V_{CC} Power-down mode; $V_{CC} = 0 \text{ V}$ 0 3.6 ambient temperature -40 $+125$						

^[2] For SOT363 (SC-88) package: Ptot derates linearly with 3.7 mW/K above 83 °C.

For SOT886 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

For SOT1115 (XSON6) package: Ptot derates linearly with 3.2 mW/K above 71 °C.

For SOT1202 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

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10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	25 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
l _l	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μΑ
I _{OFF}	power-off leakage current	V_I or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μA
Δl _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μΑ
I _{CC}	supply current	V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.5	μΑ
ΔI _{CC}	additional supply current	V_I = V_{CC} - 0.6 V; I_O = 0 A; V_{CC} = 3.3 V; One input at V_{CC} - 0.6 V, other inputs at V_{CC} or GND	-	-	40	μΑ
Cı	input capacitance	V_{CC} = 0 V to 3.6 V; V_{I} = GND or V_{CC}	-	8.0	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	pF

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +85 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
I _I	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.5	μΑ
I _{OFF}	power-off leakage current	V_I or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μΑ
Δl _{OFF}	additional power-off leakage current	V_{I} or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.6	μΑ
I _{CC}	supply current	V_{I} = GND or V_{CC} ; I_{O} = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	0.9	μΑ
ΔI _{CC}	additional supply current	V_I = V_{CC} - 0.6 V; I_O = 0 A; V_{CC} = 3.3 V; One input at V_{CC} - 0.6 V, other inputs at V_{CC} or GND	-	-	50	μΑ

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.75 × V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70 × V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25 × V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.30 × V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I_{O} = -20 μ A; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6 × V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I_{O} = -2.3 mA; V_{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = 20 μA; V _{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33 × V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
I _I	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.75	μΑ
I _{OFF}	power-off leakage current	V_{I} or $V_{O} = 0 V$ to 3.6 V; $V_{CC} = 0 V$	-	-	±0.75	μΑ
Δl _{OFF}	additional power-off leakage current	V_1 or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.75	μΑ
I _{CC}	supply current	V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	1.4	μΑ
ΔI _{CC}	additional supply current	V_I = V_{CC} - 0.6 V; I_O = 0 A; V_{CC} = 3.3 V; One input at V_{CC} - 0.6 V, other inputs at V_{CC} or GND	-	-	75	μΑ

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11. Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 8.

Symbol	Parameter	Conditions	T	_{amb} = 25	°C	T _{an}	_{nb} = o +85 °C	T _{ar} -40 °C to	_{nb} = 0 +125 °C	Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C _L = 5 p	F									
t _{pd}	propagation	A, B and C to Y; see Fig. 7	2]							
	delay	V _{CC} = 0.8 V	-	17.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.3	5.2	10.2	2.0	10.3	2.0	10.3	ns
		V _{CC} = 1.4 V to 1.6 V	1.7	3.7	6.0	1.9	6.4	1.9	6.6	ns
		V _{CC} = 1.65 V to 1.95 V	1.6	3.0	4.7	1.4	5.2	1.4	5.4	ns
		V _{CC} = 2.3 V to 2.7 V	1.4	2.3	3.3	1.2	3.7	1.2	3.9	ns
		V _{CC} = 3.0 V to 3.6 V	1.2	2.1	2.9	1.1	3.1	1.1	3.3	ns
C _L = 10	pF		·							
t _{pd}	propagation	A, B and C to Y; see Fig. 7	2]							
	delay	V _{CC} = 0.8 V	-	17.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.5	6.1	11.9	2.4	12.0	2.4	12.0	ns
		V _{CC} = 1.4 V to 1.6 V	2.2	4.3	7.1	2.0	7.3	2.0	7.6	ns
		V _{CC} = 1.65 V to 1.95 V	2.1	3.5	5.4	1.9	5.8	1.9	6.1	ns
		V _{CC} = 2.3 V to 2.7 V	1.7	2.9	4.0	1.5	4.5	1.5	4.7	ns
		V _{CC} = 3.0 V to 3.6 V	1.5	2.6	3.7	1.4	3.9	1.4	4.1	ns
C _L = 15	pF									
t _{pd}	propagation	A, B and C to Y; see Fig. 7	2]							
	delay	V _{CC} = 0.8 V	-	23.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.9	6.9	13.5	2.7	13.6	2.7	13.6	ns
		V _{CC} = 1.4 V to 1.6 V	2.5	4.9	7.8	2.4	8.5	2.4	8.8	ns
		V _{CC} = 1.65 V to 1.95 V	2.2	4.0	6.2	2.1	6.8	2.1	7.1	ns
		V _{CC} = 2.3 V to 2.7 V	2.0	3.3	4.7	1.6	5.2	1.6	5.4	ns
		V _{CC} = 3.0 V to 3.6 V	1.9	3.1	4.2	1.7	4.5	1.7	4.8	ns
C _L = 30	pF									
t _{pd}	propagation	A, B and C to Y; see Fig. 7	2]							
	delay	V _{CC} = 0.8 V	-	36.3	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.6	9.2	17.9	3.5	18.4	3.5	18.7	ns
		V _{CC} = 1.4 V to 1.6 V	3.2	6.4	10.4	3.3	11.4	3.3	11.9	ns
		V _{CC} = 1.65 V to 1.95 V	3.0	5.3	8.3	2.9	9.1	2.9	9.6	ns
		V _{CC} = 2.3 V to 2.7 V	2.8	4.4	6.2	1.6	6.7	1.6	7.1	ns
		V _{CC} = 3.0 V to 3.6 V	2.6	4.2	5.5	1.4	6.4	1.4	6.7	ns

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Symbol	Parameter	Conditions	T _{amb} = 25 °C		Conditions $T_{amb} = 25 ^{\circ}\text{C}$ $T_{amb} = -40 ^{\circ}\text{C to } +85 ^{\circ}\text{C}$		_{lb} = o +85 °C	T _{an} -40 °C to	_{nb} = 0 +125 °C	Unit
			Min	Typ [1]	Max	Min	Max	Min	Max	
C _L = 5 p	F, 10 pF, 15 pF	and 30 pF								
C _{PD}	1 -	$f_i = 1 \text{ MHz}; V_I = \text{GND to } V_{CC}$ [3]								
	dissipation capacitance	V _{CC} = 0.8 V	-	2.5	-	-	-	-	-	pF
	capacitarioc	V _{CC} = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.8	-	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.0	-	-	-	-	-	pF
		V _{CC} = 2.3 V to 2.7 V	-	3.5	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.0	-	-	-	-	-	pF

- [1] All typical values are measured at nominal V_{CC}.
- [2] t_{pd} is the same as t_{PLH} and t_{PHL}.
- [3] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

11.1. Waveform and test circuit

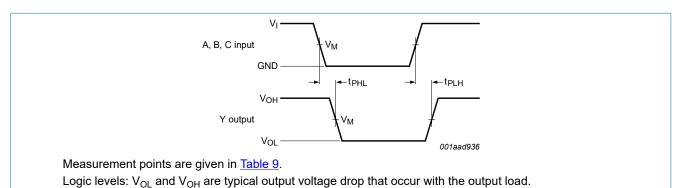
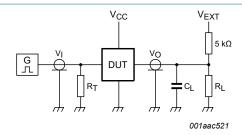


Fig. 7. Input A, B and C to output Y propagation delay times

Table 9. Measurement points

Supply voltage	Output	Input				
V _{CC}	V _M	$V_{\rm M}$ $V_{\rm I}$ $t_{\rm r}=t_{\rm f}$				
0.8 V to 3.6 V	0.5 × V _{CC}	0.5 × V _{CC}	V _{CC}	≤ 3.0 ns		

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Test data is given in Table 10.

Definitions for test circuit:

 R_L = Load resistance.

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to the output impedance Z_o of the pulse generator.

 V_{EXT} = External voltage for measuring switching times.

Fig. 8. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load	V _{EXT}			
V _{CC}	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V _{CC}

[1] For measuring enable and disable times R_L = 5 k Ω . For measuring propagation delays, setup and hold times and pulse width R_L = 1 M Ω .

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12. Package outline

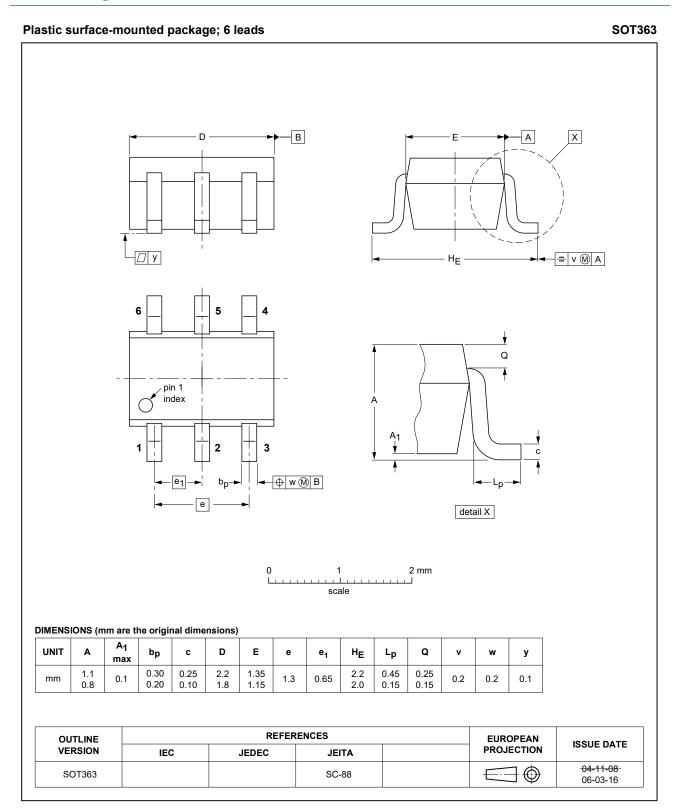


Fig. 9. Package outline SOT363 (SC-88)

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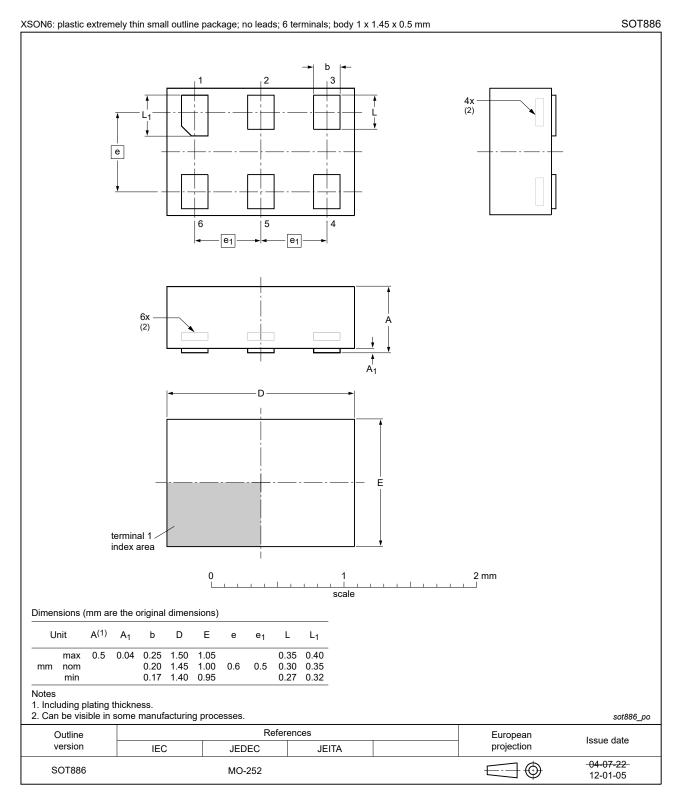


Fig. 10. Package outline SOT886 (XSON6)

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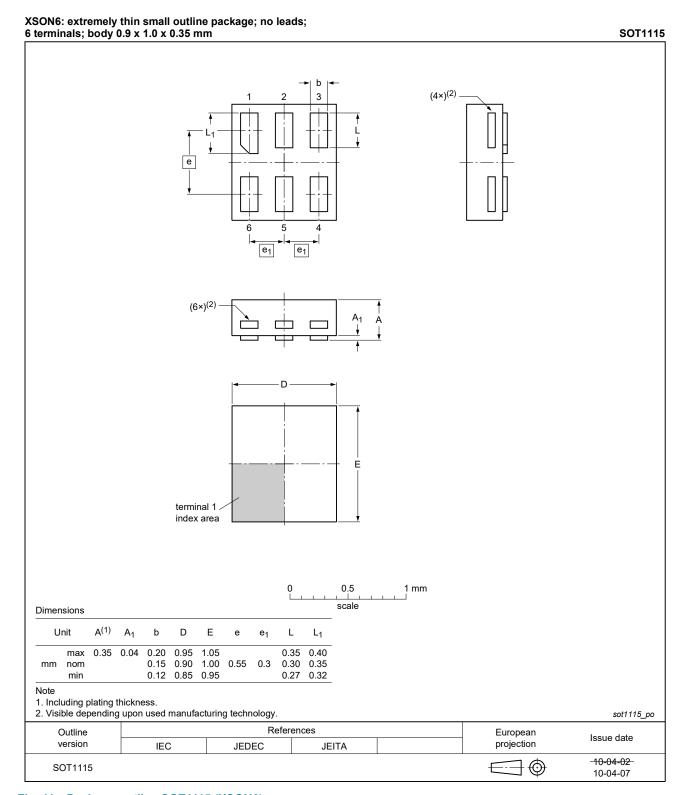


Fig. 11. Package outline SOT1115 (XSON6)

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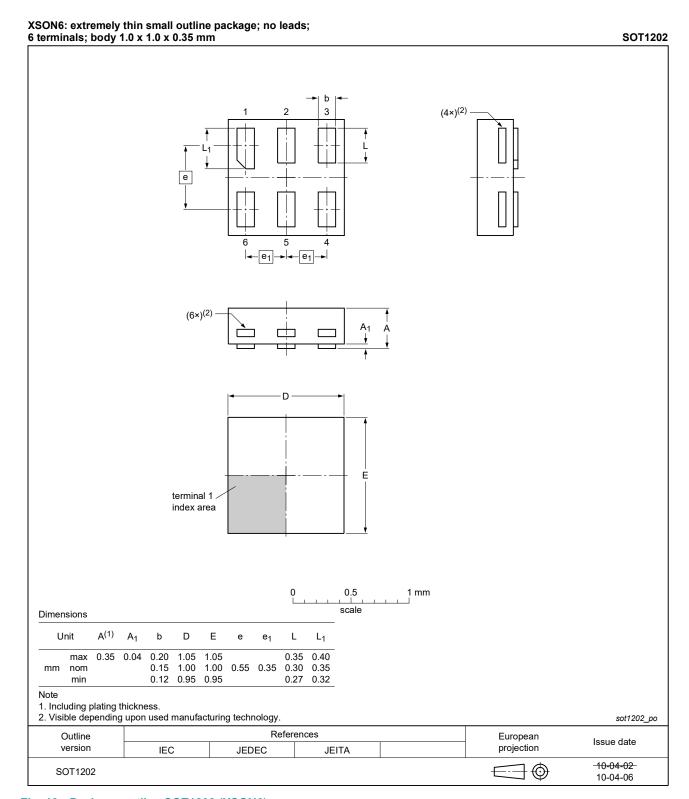


Fig. 12. Package outline SOT1202 (XSON6)

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13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AUP1G332 v.6	20201203	Product data sheet	-	74AUP1G332 v.5	
Modifications:	guidelines Legal texts Type numl	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type number 74AUP1G332GF (SOT891 / XSON6) removed. Table 5: Derating values for Ptot total power dissipation updated. 			
74AUP1G332 v.5	20120704	Product data sheet	-	74AUP1G332 v.4	
Modifications:	Package contacts	Package outline drawing of SOT886 (Fig. 10) modified.			
74AUP1G332 v.4	20111125	Product data sheet	-	74AUP1G332 v.3	
Modifications:	Legal page	es updated.			
74AUP1G332 v.3	20101007	Product data sheet	-	74AUP1G332 v.2	
74AUP1G332 v.2	20080229	Product data sheet	-	74AUP1G332 v.1	
74AUP1G332 v.1	20061113	Product data sheet	-	-	

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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