LM3519 High Frequency Boost White LED Driver with High-Speed PWM Brightness Control

Check for Samples: LM3519

FEATURES

- Drives 2 to 4 LEDs at 20mA
- Up to 30kHz PWM Dimming Control Capability
- >80% Peak Efficiency
- Up to 8MHz Switching Frequency
- Small External Components: 1µH - 3.3µH(typ.2.2µH) Inductor and 1µF Output Capacitor
- True Shutdown Isolation
- Over-Voltage Protection
- Wide Input Voltage Range: 2.7V to 5.5V
- Small Footprint SOT-23 Package

APPLICATIONS

- LCD, White LED Backlighting on Mobile Phones
- Digital Still Cameras and PDAs
- General Purpose LED Lighting in Handheld Devices

DESCRIPTION

The LM3519 drives up to 4 white LEDs with constant current to provide LCD backlighting in handheld devices. The LED current is internally set to 20mA. The series connection allows the LED current to be identical for uniform brightness and minimizes the number of traces to the LEDs. Brightness control is achieved by applying a PWM signal on enable with frequencies up to 30kHz.

The LM3519 features a proprietary PFM regulation architecture with switching frequencies between 2MHz to 8MHz, minimizing inductor size.

Over-voltage protection circuitry and high frequency operation permit the use of low-cost small output capacitors. During shutdown, the output is disconnected from the input in order to avoid leakage current path through the LEDs to ground.

The LM3519 is available in a tiny 6-pin SOT-23 package.

Typical Application

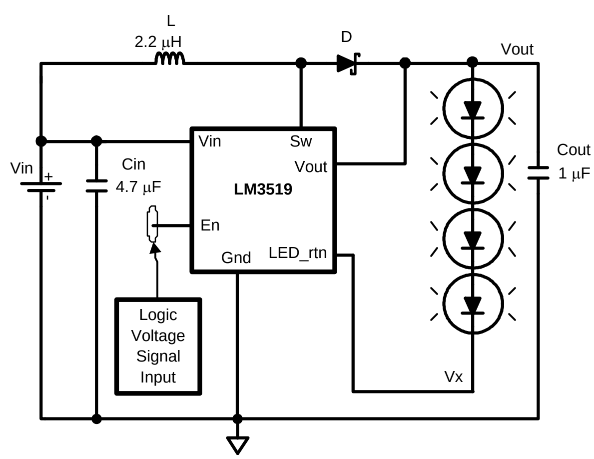
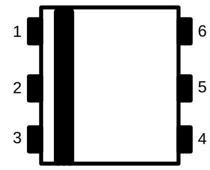


Figure 1. Typical Application Circuit

Connection Diagram

6-Lead SOT-23 Package



Top View

PIN DESCRIPTIONS

Pin #	Name	Description
1	En	Device Enable Connection
2	Gnd	Ground Connection
3	V_{OUT}	Output Voltage Connection
4	LED_rtn	White LED Current Sensing Input Connection
5	S_W	Drain Connection of the Internal Power Field Effect Transistor (FET) Switch
6	V_{IN}	Input or Supply Voltage Connection

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings(1)

V_{IN} , En, & LED_rtn Pin	−0.3V to +6.5V
V_{OUT} , Sw Pin	−0.3V to +21V
Maximum Junction Temperature, ($T_{J\text{-}MAX}$)	+150°C
Storage Temperature Range	−65°C to +150°C
ESD Rating(2) Human Body Model: Machine Model:	 2kV 200V

(1) Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating Ratings are conditions under which operation of the device is ensured. Operating Ratings do not imply ensured limits. For ensured performance limits and associated test conditions, see the Electrical Characteristics table.
(2) The human body model is a 100pF capacitor discharged through a 1.5kΩ resistor into each pin. The machine model is a 200pF capacitor discharged directly into each pin.

Operating Ratings(1)

Junction Temperature (T_J) Range	−40°C to +125°C
Ambient Temperature (T_A) Range	−40°C to +85°C
Input Voltage Range	2.7V to 5.5V

(1) Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating Ratings are conditions under which operation of the device is ensured. Operating Ratings do not imply ensured limits. For ensured performance limits and associated test conditions, see the Electrical Characteristics table.

Thermal Properties(1)

Junction-to-Ambient Thermal Resistance (θ_{JA})	220°C/W

(1) The maximum allowable power dissipation is a function of the maximum junction temperature, T_J(MAX), the junction-to-ambient thermal resistance, θ_{JA}, and the ambient temperature, T_A. See Thermal Properties for the thermal resistance. The maximum allowable power dissipation at any ambient temperature is calculated using: $P_D(MAX) = (T_J(MAX) – T_A)/\theta_{JA}$. Exceeding the maximum allowable power dissipation will cause excessive die temperature.

Electrical Characteristics (1)(2)

Limits in standard typeface are for T_J = +25°C. Limits in **bold typeface** apply over the full operating junction temperature range (−40°C ≤ T_J ≤ +125°C). V_{IN} = 3.6V, unless otherwise stated.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
I_Q	Supply Current	Shutdown: V_{EN} = 0V		0.1		µA
		Not Switching: V_{EN} = 1.8V		360	**500**	
		Switching: V_{EN} = 1.8V, LED_rtn current = 30mA		550	**900**	
$I_{LED(TOL)}$	LED Current Tolerance/Variation	V_{IN} = 3.6V, 2.2µH, 4LEDs	**–10**	5.5	**10**	%
OVP	Over-Voltage Protection Threshold	OVP ON OVP OFF	**18** **17.8**	18.9 18.6	**20** **19.8**	V
I_{LIM}	Switch Current Limit	L = 2.2µH		750		mA
$R_{DS(ON)}$	Power NMOS Switch ON Resistance			455		mΩ
$I_{LEAKAGE}$	Switch Leakage	V_{SW} = 3.6V, V_{EN} = 0V		0.1	**2**	µA
$R_{LED\_rtn(ON)}$	LED_rtn NMOS Switch ON Resistance			8.0		Ω
F_S	Switching Frequency	I_{LED} = 20 mA , L = 1µH 4LEDs		5.4		MHz
I_{EN}	Enable Pin Bias Current (3)	V_{EN} = 0V V_{EN} = 1.8V		0.1 1.1	 **2**	µA
En	Enable Threshold	Device On Device Off	**0.9** 		 **0.3**	V

(1) Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating Ratings are conditions under which operation of the device is ensured. Operating Ratings do not imply ensured limits. For ensured performance limits and associated test conditions, see the Electrical Characteristics table.
(2) Min and max limits are ensured by design, test, or statistical analysis. Typical numbers are not ensured, but do represent the most likely norm.
(3) Current flows into the pin.

BLOCK DIAGRAM

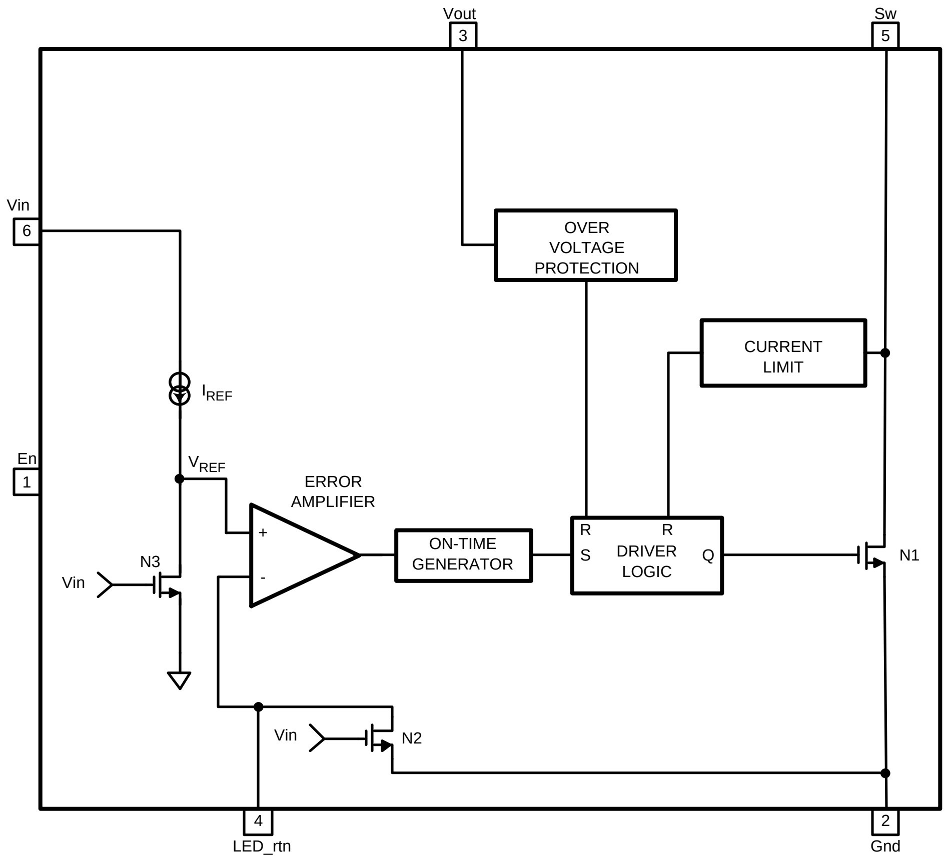


Figure 2. Block Diagram

Circuit Description

The LM3519 is a step-up converter for white LED applications that uses a unique and proprietary pulse frequency modulation (PFM) architecture to optimize high efficiency at high frequency operation. Unlike most PFM architecture implementations, the LM3519's unique architectural implementation results in non-pulse skipping variable frequency operation. The regulator is forced to operate at the edge of Continous Conduction Mode (CCM). The error amplifier will set the end of the on-time (I_{PEAK} of inductor) based on the load (LEDs) current. During this operation, the inductor current ramps up and reaches a peak current at end of the on-time. At this point, the internal power switch is turned off until the inductor current reaches zero, and the cycle repeats again. The switching frequency is set based on the charge (on-time) and discharge(off-time) of the inductor current. The frequency can range between 2MHz to 8MHz over the operating input range.

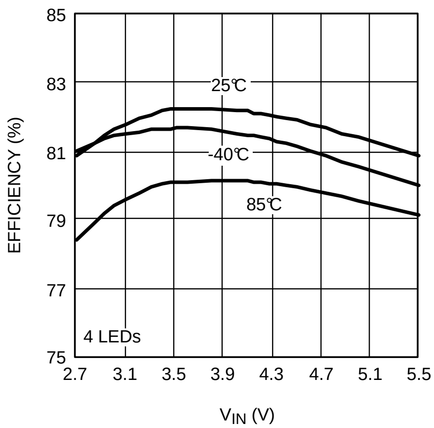
The LM3519 operation can be best understood through an examination of the block diagram in Figure 2. When LED current is out of regulation, the LED_rtn voltage falls below or rises above the internal reference voltage (VREF). The error amplifier will output a signal to increase or decrease the proper on-time duration of N1 power FET. This correction allows the inductor's stored energy to increase or decrease to a sufficient level that when transferred to the load will bring the LED_rtn current back into regulation.

During steady-state operation for a typical switching cycle, the oscillator sets the driver logic and turns on N1 power device. N1 conducts current through the inductor and reverse biases the external diode. The LED current is supplied by the output capacitor when N1 is conducting. Once N1 on-time period is concluded, the internal power device is turned off and the external diode is forward baised. The inductor current then flows through the diode to the LED load to replenish the output capacitor and keep the LED current regulated at the trimmed target.

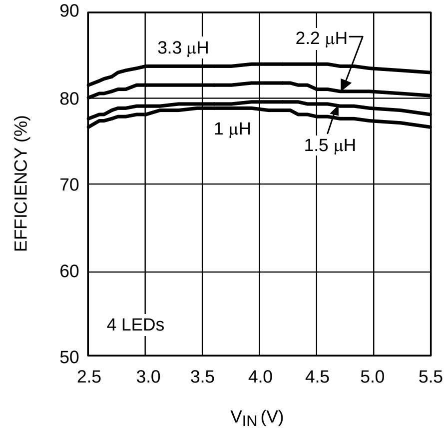
Typical Performance Characteristics

(See Figure 1: V_{IN} = 3.6V, C_{IN} = 4.7µF and C_{OUT} = 1µF, L = 2.2µH and 4 LEDs. T_A = +25°C, unless otherwise stated.)

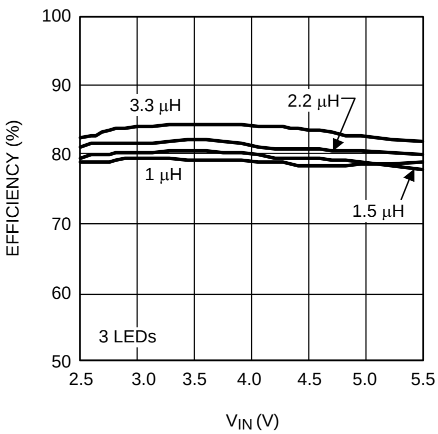
Efficiency vs V_{IN}



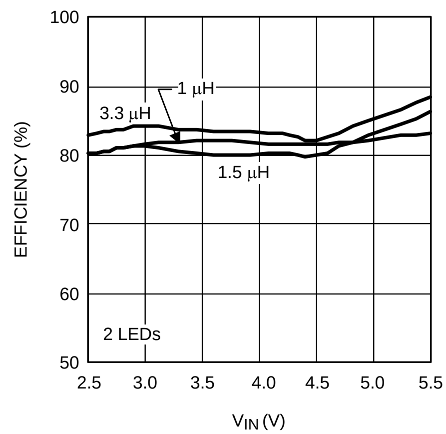
Efficiency vs V_{IN}



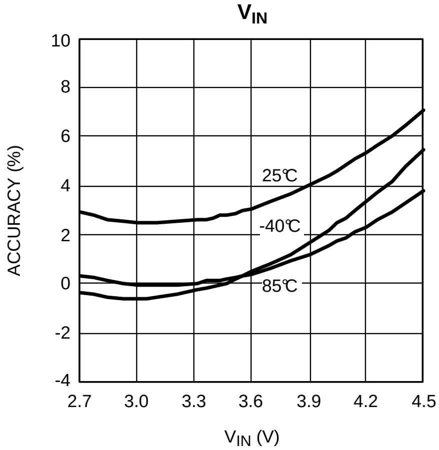
Efficiency vs V_{IN}



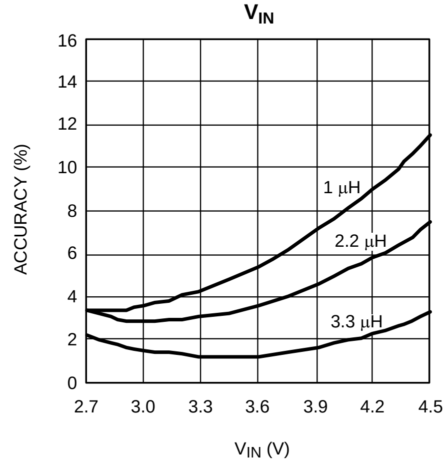
Efficiency vs V_{IN}



$I_{OUT\_ACCURACY}$ vs V_{IN}



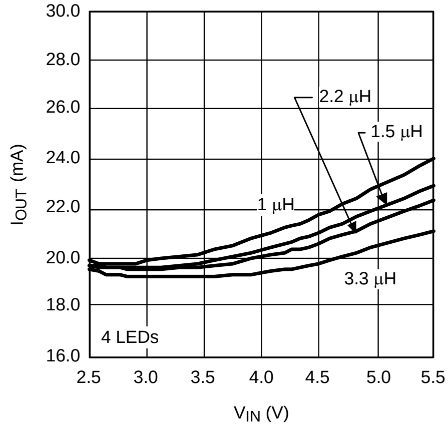
$I_{OUT\_ACCURACY}$ vs V_{IN}



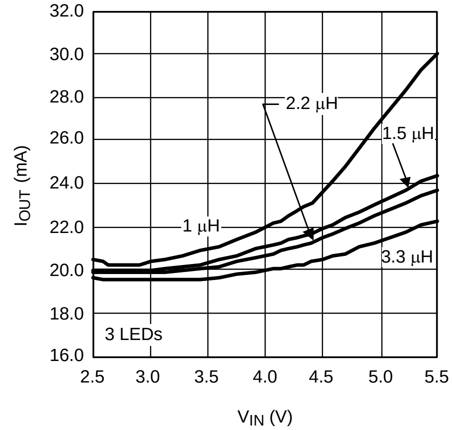
Typical Performance Characteristics (continued)

(See Figure 1: V_{IN} = 3.6V, C_{IN} = 4.7µF and C_{OUT} = 1µF, L = 2.2µH and 4 LEDs. T_A = +25°C, unless otherwise stated.)

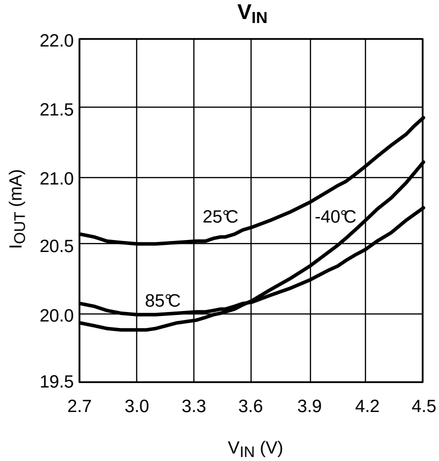
I_{OUT} vs V_{IN}



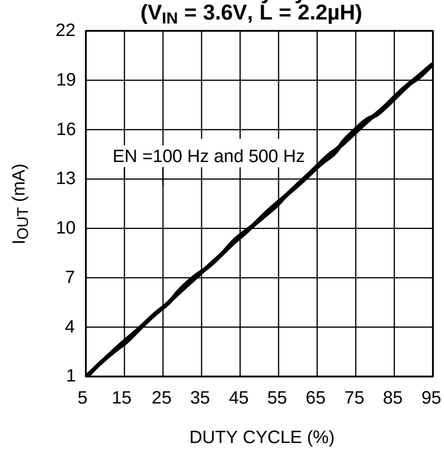
I_{OUT} vs V_{IN}



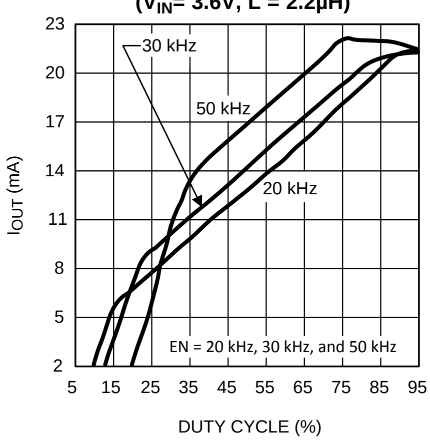
I_{OUT} vs V_{IN}



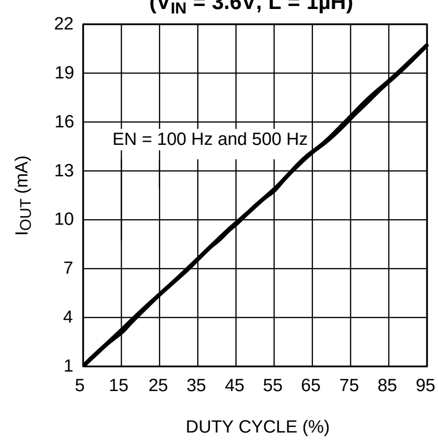
I_{OUT} vs PWM Duty Cycle (V_{IN} = 3.6V, L = 2.2µH)



I_{OUT} vs PWM Duty Cycle (V_{IN}= 3.6V, L = 2.2µH)



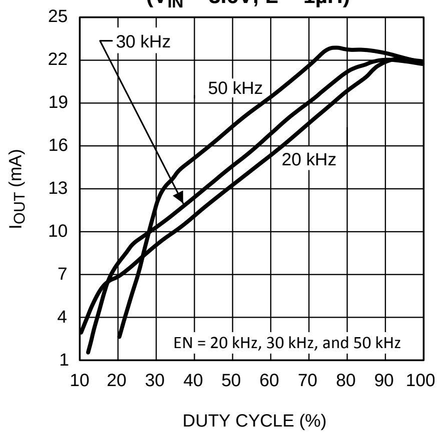
I_{OUT} vs PWM Duty Cycle (V_{IN} = 3.6V, L = 1µH)



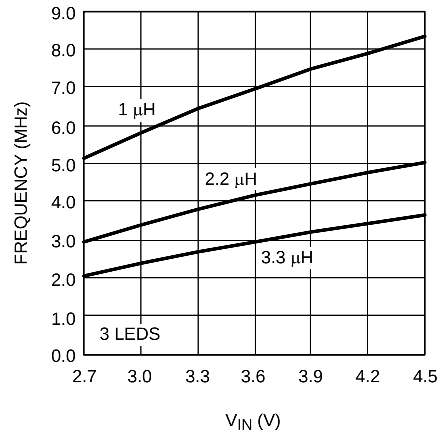
Typical Performance Characteristics (continued)

(See Figure 1: V_{IN} = 3.6V, C_{IN} = 4.7µF and C_{OUT} = 1µF, L = 2.2µH and 4 LEDs. T_A = +25°C, unless otherwise stated.)

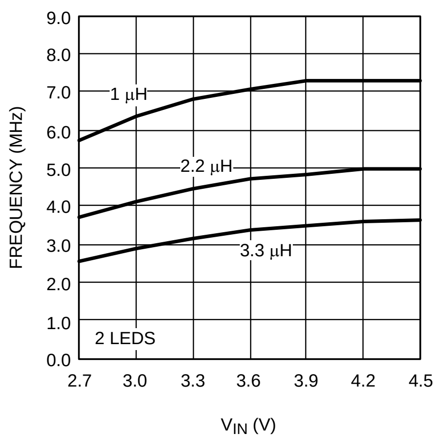
I_{OUT} vs PWM Duty Cycle (V_{IN} = 3.6V, L = 1µH)



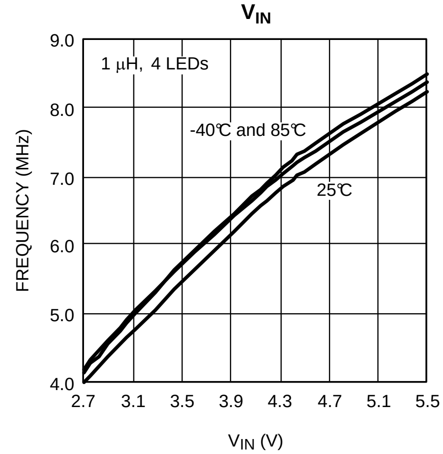
Switching Frequency vs V_{IN}



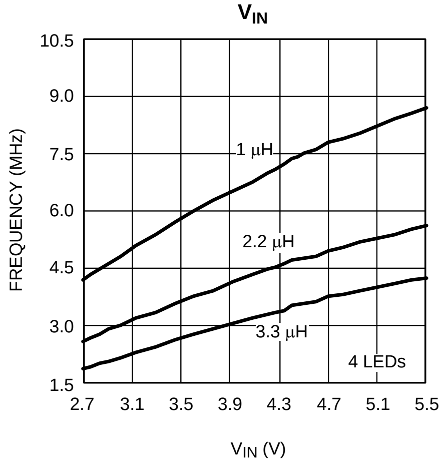
Switching Frequency vs V_{IN}



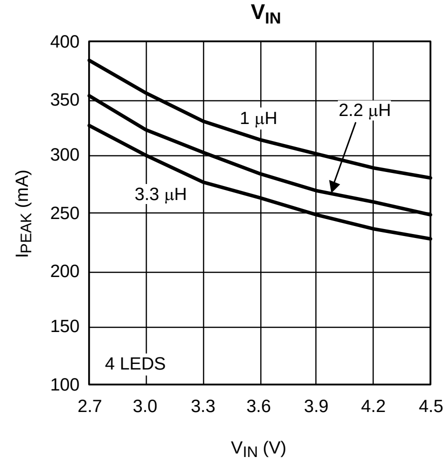
Switching Frequency vs V_{IN}



Switching Frequency vs V_{IN}



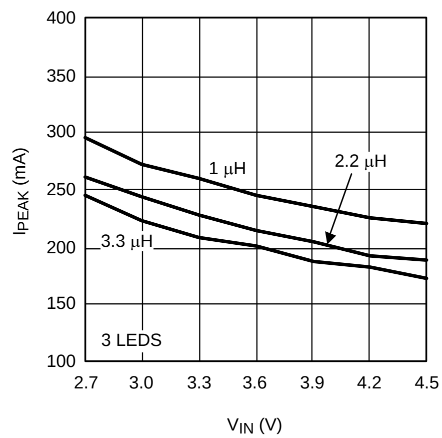
Peak Inductor Current vs V_{IN}



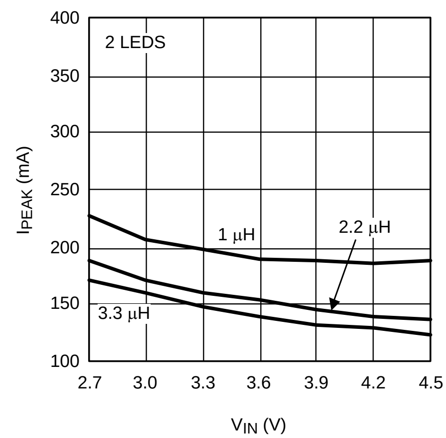
Typical Performance Characteristics (continued)

(See Figure 1: V_{IN} = 3.6V, C_{IN} = 4.7µF and C_{OUT} = 1µF, L = 2.2µH and 4 LEDs. T_A = +25°C, unless otherwise stated.)

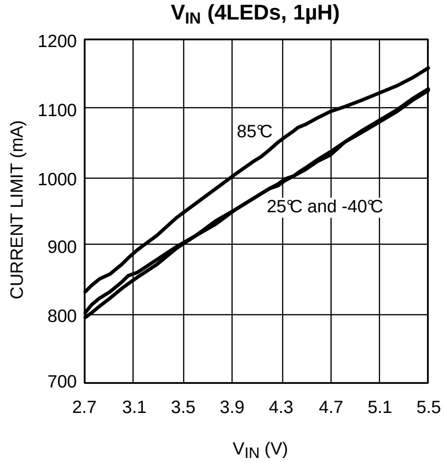
Peak Inductor Current vs V_{IN}



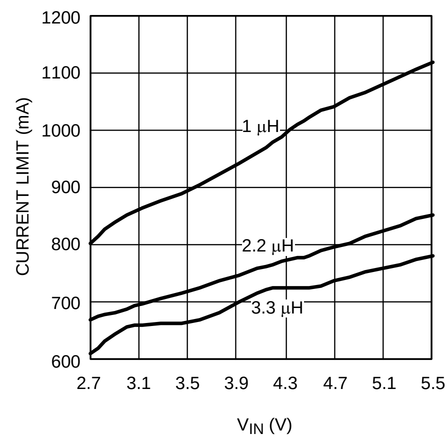
Peak Inductor Current vs V_{IN}



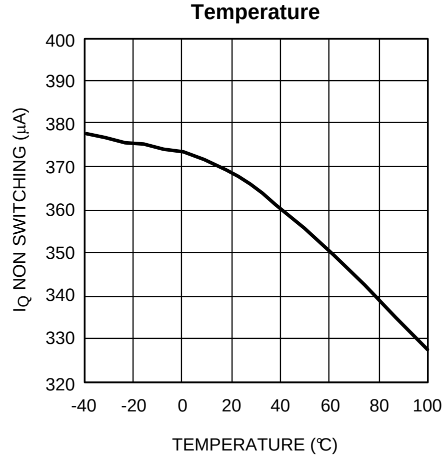
Current Limit vs V_{IN} (4LEDs, 1µH)



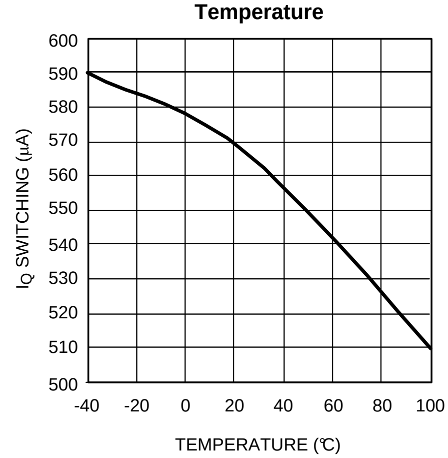
Current Limit vs V_{IN}



Iq (non switching) vs Temperature



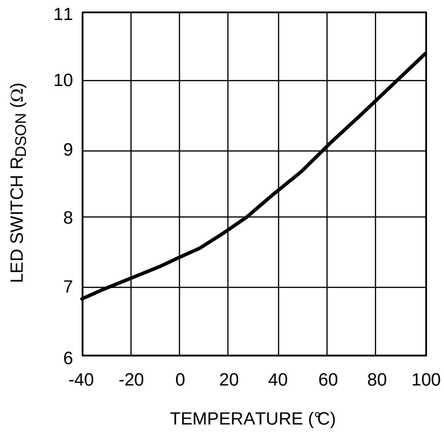
Iq (switching) vs Temperature



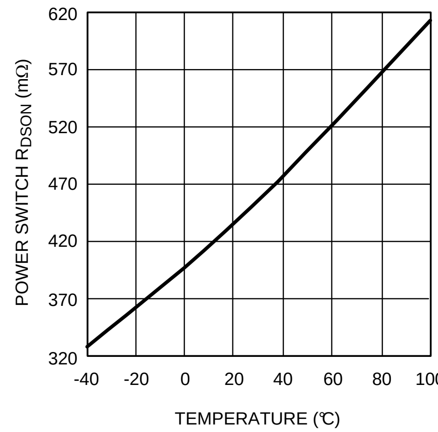
Typical Performance Characteristics (continued)

(See Figure 1: V_{IN} = 3.6V, C_{IN} = 4.7µF and C_{OUT} = 1µF, L = 2.2µH and 4 LEDs. T_A = +25°C, unless otherwise stated.)

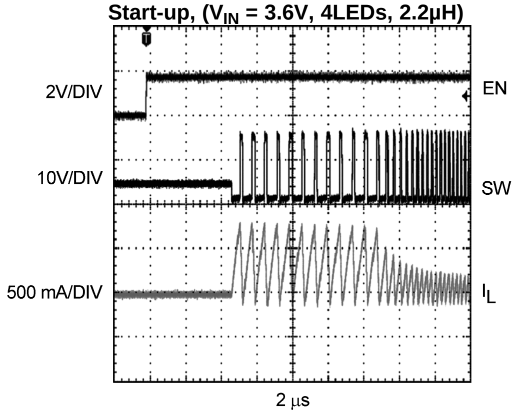
LED Switch R_{DS_ON} vs Temperature



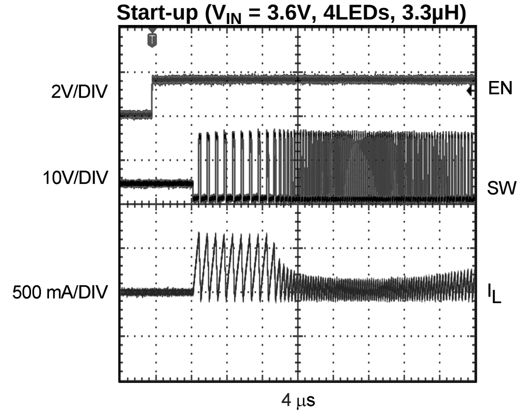
Power Switch R_{DS_ON} vs Temperature



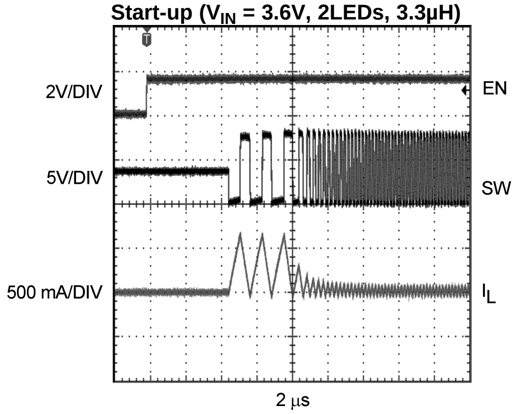
Start-up, (V_{IN} = 3.6V, 4LEDs, 2.2µH)



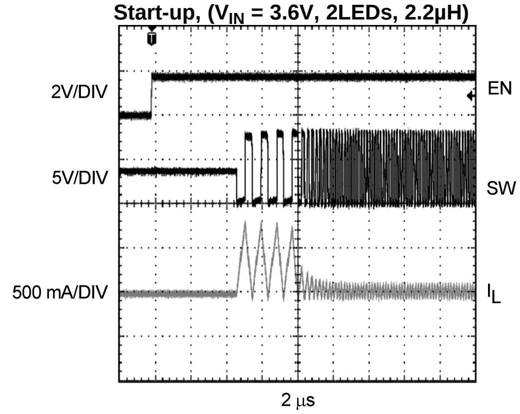
Start-up, (V_{IN} = 3.6V, 4LEDs, 3.3µH)



Start-up, (V_{IN} = 3.6V, 2LEDs, 3.3µH)



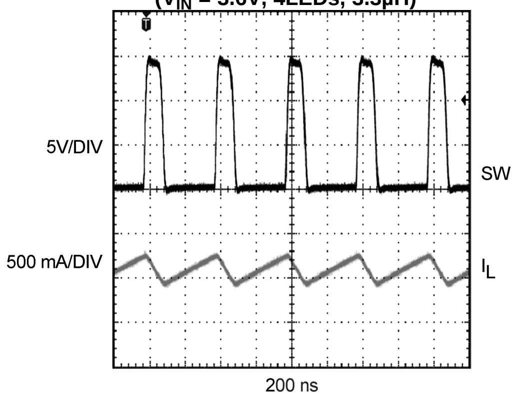
Start-up, (V_{IN} = 3.6V, 2LEDs, 2.2µH)



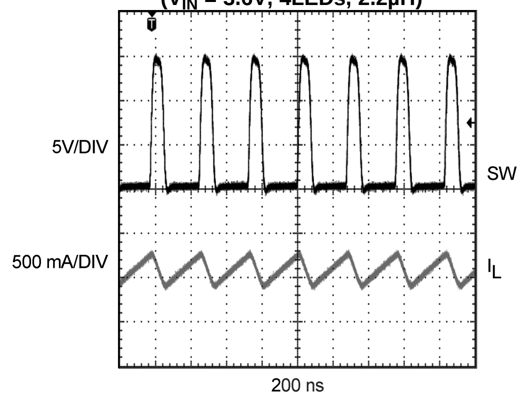
Typical Performance Characteristics (continued)

(See Figure 1: V_{IN} = 3.6V, C_{IN} = 4.7µF and C_{OUT} = 1µF, L = 2.2µH and 4 LEDs. T_A = +25°C, unless otherwise stated.)

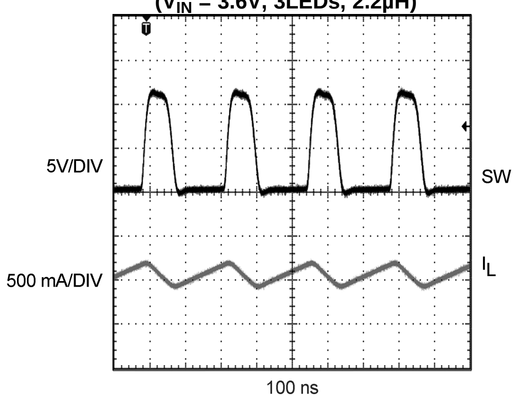
Typical Switching Waveform (V_{IN} = 3.6V, 4LEDs, 3.3µH)



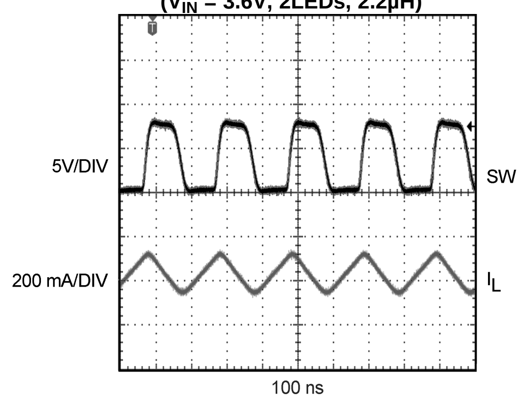
Typical Switching Waveform (V_{IN} = 3.6V, 4LEDs, 2.2µH)



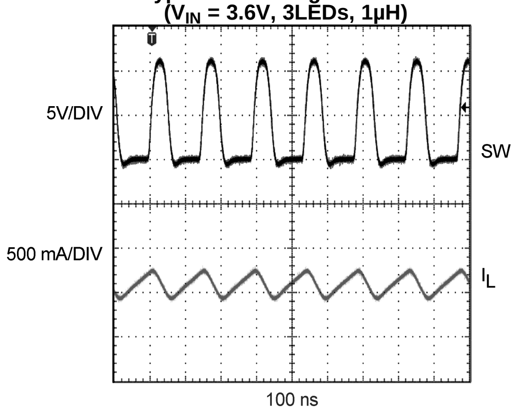
Typical Switching Waveform (V_{IN} = 3.6V, 3LEDs, 2.2µH)



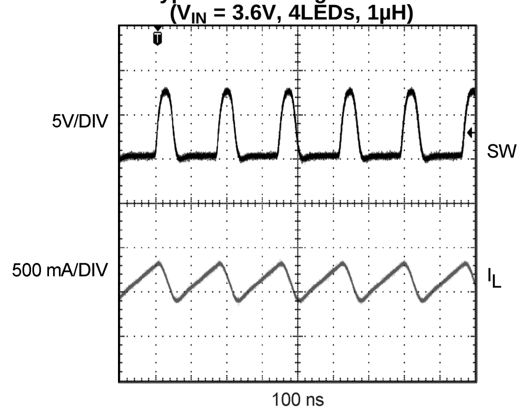
Typical Switching Waveform (V_{IN} = 3.6V, 2LEDs, 2.2µH)



Typical Switching Waveform (V_{IN} = 3.6V, 3LEDs, 1µH)



Typical Switching Waveform (V_{IN} = 3.6V, 4LEDs, 1µH)



APPLICATION INFORMATION

Capacitor Selection

To minimize output and input voltage ripple, low equivalent series resistance (ESR) ceramic capacitors are the best choice to use for the input and output filters. For most display applications, a 4.7µF capacitor is recommended for C_{IN} and 1µF for C_{OUT} .

Larger output capacitors can be used to reduce ripple voltage. To ensure good performance, a minimum of 0.47µF C_{OUT} is required to trade off for large ripple voltage. Care must be taken to account for the true capacitance of a multilayer ceramic capacitor. Smaller case size capacitors typically have less capacitance for a given bias voltage as compared to a larger case size capacitor with the same bias voltage. Please confirm with capacitor manufacturer data before selecting the capacitor.

Recommended capacitor manufacturers include but are not limited to:

Table 1.

Manufacturer	Description	Case Size
AVX	06033D105MAT-25V	0603
	06036D475MAT-6.3V	0603
TDK	C2012X5R1A475M-10V	0805
Taiyo Yuden	TMK212BJ105KG-J	0805
	EM212BJ475MG-16V	0805
muRata	GRM40-034B105K25	0805
	GRM39X5R475K6.3	0603

Inductor Selection

In order to maintain sufficient inductance, the saturation current rating of the inductor used with the LM3519 should be higher than the peak inductor current in the target application. Inductors with low DCR values have less power loss and higher efficiency. Larger inductor values such as 2.2µH and 3.3µH can be used to optimize efficiency, frequency and peak current. If 1µH is used, the peak inductor current, frequency will be higher and the efficiency will be lower. Note that the switching frequency ranges will be higher at lower inductance. Typical frequency range is between 4 to 8MHz for 1µH, 2 to 5MHz for 2.2µH and 2 to 4MHz for 3.3µH over the input range. Below is a sample list of low profile inductors.

Some recommended inductor manufacturers include but are not limited to:

Manufacturer	L	Case Size	I_{SAT}
CoilCraft: DO3314-102	1µH	3.3x3.3x1.4mm	2.1A
DO3314-222	2.2µH		1.6A
DO3314-332	3.3µH		1.4A
Coilcraft: LPO3310-102ML	1µH	3.3x3.3x1.0 mm	1.6A
LPO3310-222ML	2.2µH		1.1A
LPO3310-332ML	3.3µH		0.95A
Cooper: SD31121R0	1µH	3.1x3.1x1.4 mm	2.07A
SD3114-2R2	2.2µH		1.48A
SD3114-3R3	3.3uH		1.15A
Taiyo Yuden: NR3015T1R0N	1µH	3.0x3.0x1.5 mm	2.1A
NR3015T2R2M	2.2µH		1.48A
NR3015T3R3M	3.3µH		1.21A

Diode Selection

Diodes with low forward voltage ratings (V_F) and low junction capacitance magnitudes (C_J or C_T or C_D) are conducive to high efficiency. The chosen diode must have a reverse breakdown voltage rating (V_R and/or V_{RRM}) that is larger than the output voltage. The following criteria should be followed when choosing a diode:

1. V_R (Diode Blocking Voltage Range) and V_{RRM} (Diode Peak Repetitive Reverse Voltage Rating) > V_{OUT} (Output Voltage)
2. I_F or I_O (Diode Average Forward Current Rating) ≥ I_{LOAD} (Load Current)
3. I_{FRM} (Diode Peak Repetitive Forward Current Rating) ≥ I_{Lpeak} (Peak Inductor Current)

Some recommended diode manufacturers include but are not limited to:

Manufacturer	Description
Vishay	SS12(1A/20V)
	SS14(1A/40V)
	SS16(1A/60V)
Central Semiconductor	CMSH1- 40M(1A/40V)
ONSemi	MBRS1540T3(1.5A/40V)

PWM DIMMING

The LED current is set internally by the LM3519 to 20mA (typical); dimming control may be realized by applying a pulse width modulated(PWM) signal to the En pin. For example, a 50% duty cycle waveform will produce an average current of 10mA. A control signal frequency between 17kHz and 30kHz is suitable for dimming.

Although the LM3519 is capable of operation outside this frequency range, it is not recommended to operate below 17kHz for the following reasons: 1) frequency below 100Hz is likely to cause visible flicker in the light emitted by the LED string. 2) frequency below 17kHz may induce audible noise due to combinations of some capacitance/PCB. A PWM frequency above 30kHz is possible but the current linearity vs duty cycle will be affected.

If it is not possible to operate the dimming control above 17kHz, audible noise emission may be minimized by using capacitors with low susceptibility to piezoelectric induced stresses, such as poly film designs. Minimum audible noise is most likely to occur when the PWM frequency is less than 2kHz. It is recommended that any application using a PWM control signal below 17kHz be thoroughly evaluated for undesirable audible or visible noise.

DRIVING 2 LEDs

The LM3519 is optimized to drive up to 4LEDs. When driving 2LEDs, a minimum inductance of 2.2µH is required to maintain good loop regulation and current accuracy. If a smaller inductor is used, the LED current will have more variation with input voltage than a typical application. The following curve illustrates the behavior.

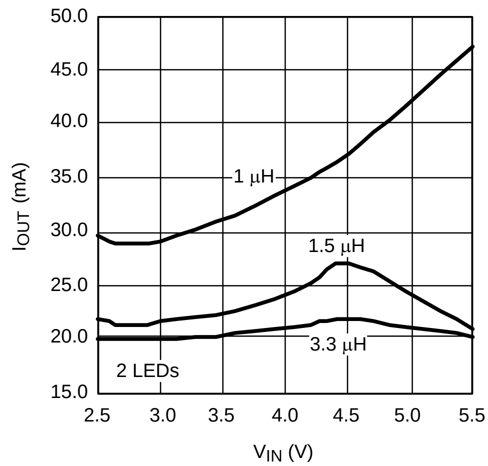


Figure 3. I_{OUT} vs V_{IN}

LAYOUT GUIDELINES

The input capacitor, C_{IN}, must be placed close to the LM3519. Placing C_{IN} close to the device will reduce the metal trace resistance effect on input voltage ripple. Metal trace connections for the C_{OUT} capacitor can increase the effective series resistance, which affects output voltage ripple and efficiency. Trace connections to the inductor should be short and wide to reduce power dissipation, increase overall efficiency and reduce EMI radiation. The diode, like the inductor, should have trace connections that are short and wide to reduce power dissipation and increase overall efficiency. For more details regarding layout guidelines for switching regulators, refer to Application Note AN1149 SNVA021.

REVISION HISTORY

Changes from Revision A (May 2013) to Revision B **Page**

- Changed layout of National Data Sheet to TI format 13

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
LM3519MK-20/NOPB	ACTIVE	SOT-23-THIN	DDC	6	1000	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 85	D52B	Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

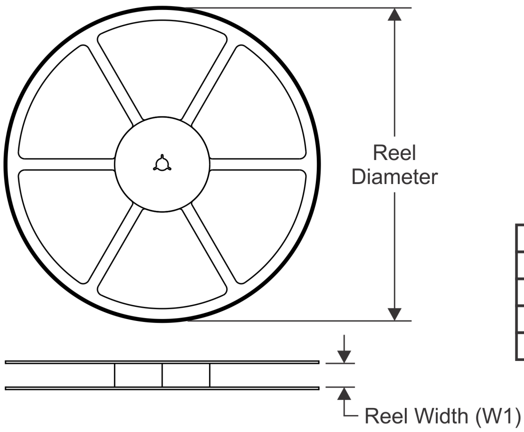
(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

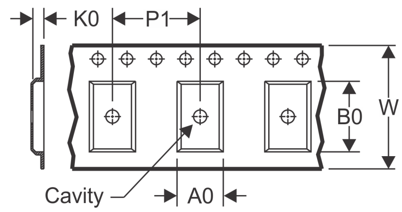
(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

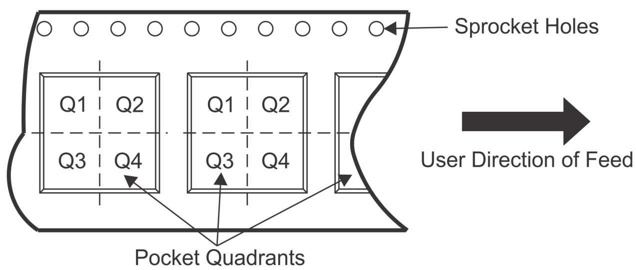


TAPE DIMENSIONS



A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

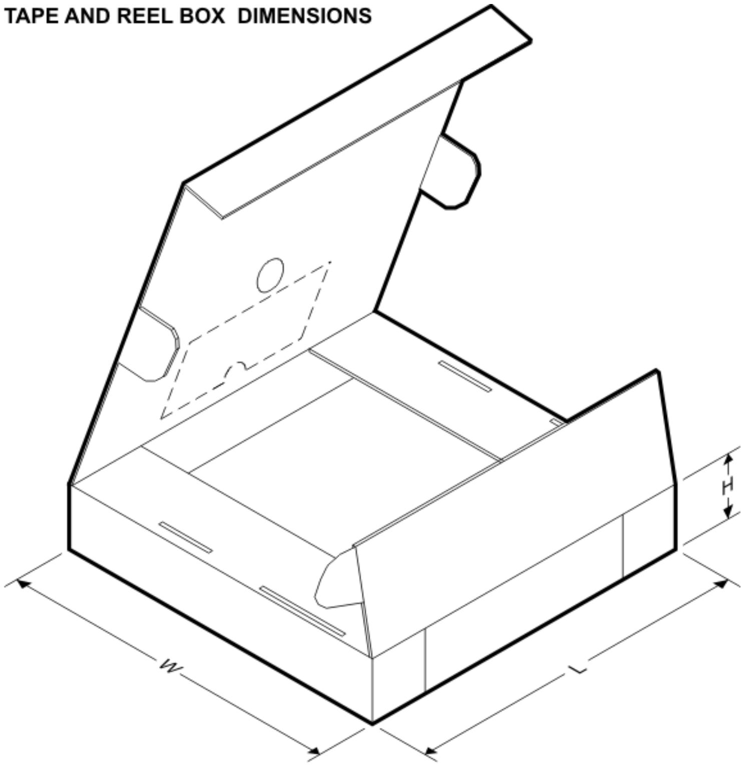
QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

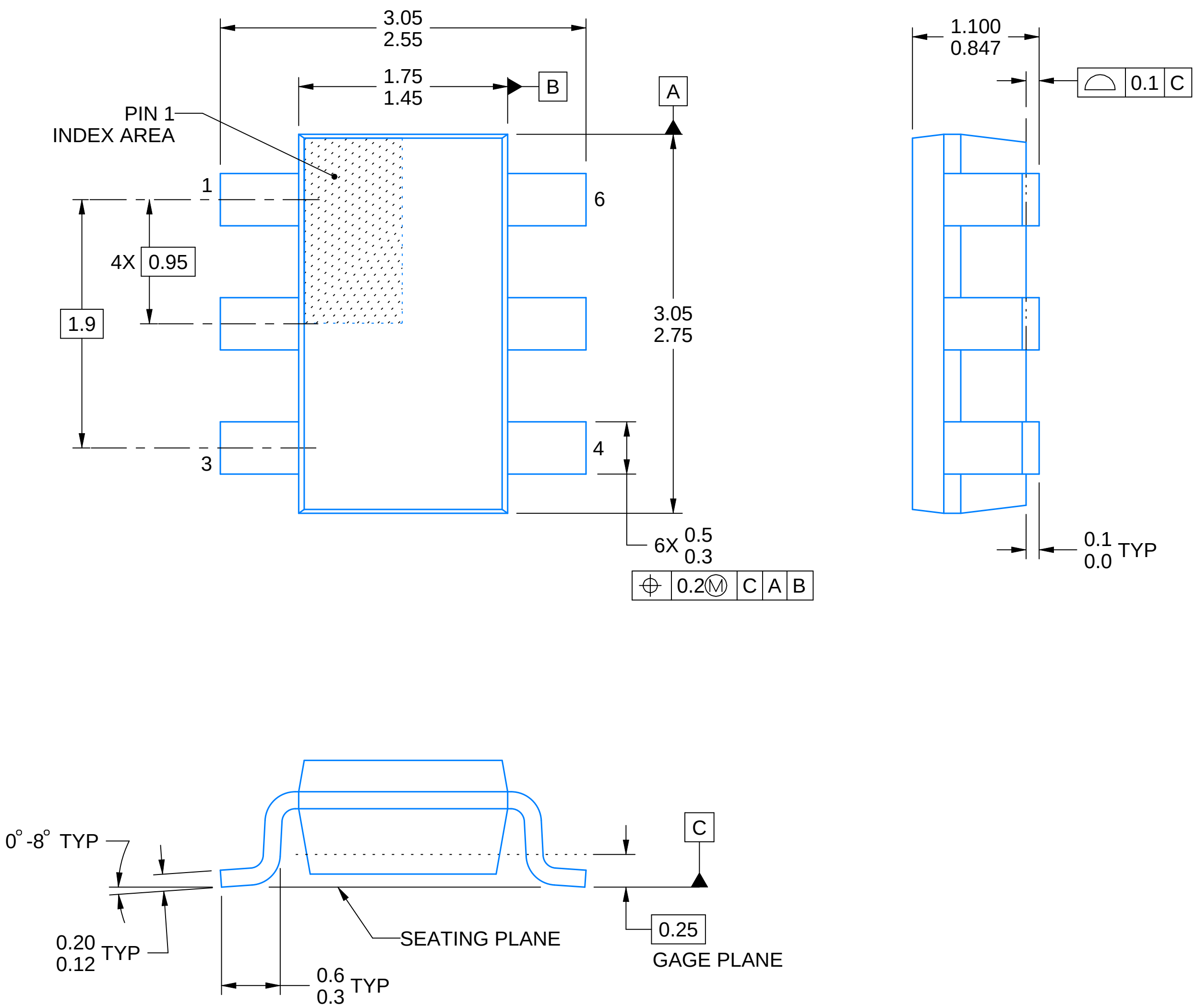
Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM3519MK-20/NOPB	SOT-23-THIN	DDC	6	1000	178.0	8.4	3.2	3.2	1.4	4.0	8.0	Q3

TAPE AND REEL BOX DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM3519MK-20/NOPB	SOT-23-THIN	DDC	6	1000	210.0	185.0	35.0



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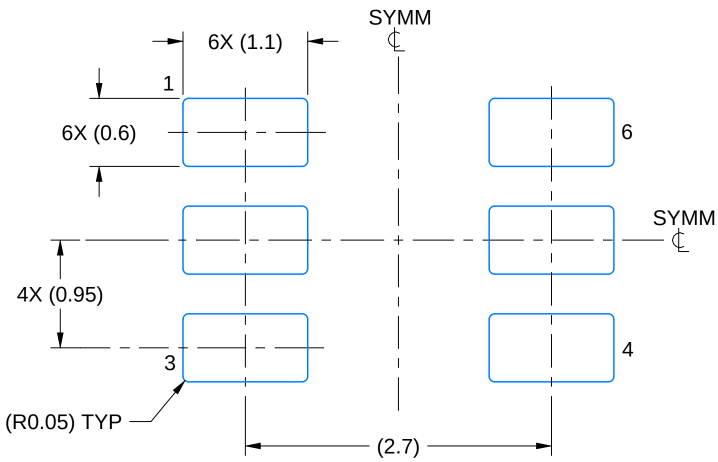
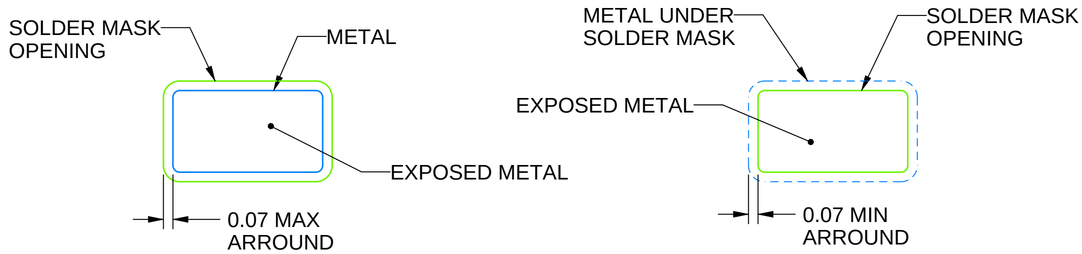
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC MO-193.



LAND PATTERN EXAMPLE
EXPLOSED METAL SHOWN
SCALE:15X



NON SOLDER MASK DEFINED

SOLDER MASK DEFINED

SOLDERMASK DETAILS

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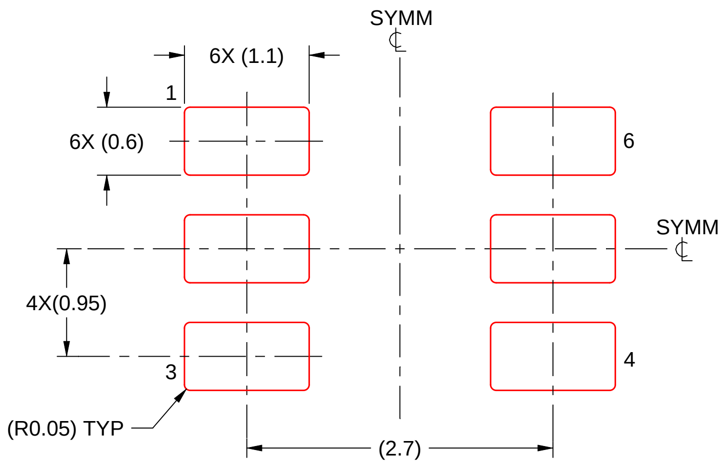
NOTES: (continued)

4. Publication IPC-7351 may have alternate designs.
5. Solder mask tolerances between and around signal pads can vary based on board fabrication site.



SOLDER PASTE EXAMPLE
BASED ON 0.125 THICK STENCIL
SCALE:15X

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NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
7. Board assembly site may have different recommendations for stencil design.