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6-Pin DIP Random-Phase Triac Driver Optocoupler (800 Volt Peak)

The MOC3071M and MOC3072M consist of a GaAs infrared emitting diode optically coupled to a non-zero- crossing silicon bilateral AC switch (triac). These devices isolate low voltage logic from 240 V_{AC} lines to provide random phase control of high current triacs or thyristors. These devices feature greatly enhanced static dv/dt capability to ensure stable switching performance of inductive loads.

Features

- Excellent I_{FT} Stability—IR Emitting Diode Has Low Degradation
- 800 V Peak Blocking Voltage
- Safety and Regulatory Approvals
 - UL1577, 4,170 VAC_{RMS} for 1 Minute
 - DIN EN/IEC60747-5-5 (pending approval)

Typical Applications

- Solenoid/ValveControls
- Lamp Ballasts
- Static AC Power Switch
- Interfacing Microprocessors to 240 V_{AC} Peripherals
- Solid State Relay
- Incandescent Lamp Dimmers
- Temperature Controls
- Motor Controls



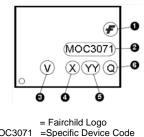
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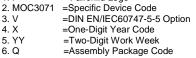


MDIP 6L WHITE

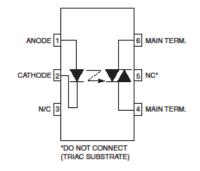
MARKING DIAGRAM



1. F



PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information page 9 of this data sheet.

SAFETY AND INSULATIONS RATINGS

As per DIN EN/IEC 60747-5-5 (pending approval), this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

	Parameter		Charac	teristics	
Installation Classifications per DIN VDE 0110/1.89 Table 1,		< 150 VRMS	I–	I–IV	
For Rated M	lains Voltage	< 300 V _{RMS}	I–	IV	
Climatic Cla	ssification	·	40/8	5/21	
Pollution De	gree (DIN VDE 0110/1.89)		:	2	
Comparativ	e Tracking Index		1	75	
Symbol	Parameter		Value	Unit	
Vaa	Input-to-Output Test Voltage, Method A, $V_{IORM} \times S_{Sample}$ Test with $t_m = 10 \text{ s}$, Partial Discharge < 5 p		1360	V _{peak}	
VPR		1 075 1/ 1000/			

Voo	VPR VPR		V _{peak}
۲PK	Input-to-Output Test Voltage, Method B, VIORM x 1.875 = VPR, 100% Production Test with $t_m = 1$ s, Partial Discharge < 5 pC	1594	V _{peak}
VIORM	Maximum Working Insulation Voltage	850	V _{peak}
VIOTM	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥7	mm
	External Clearance	≥7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥ 10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.5	mm
R _{IO}	Insulation Resistance at T _S , V_{IO} = 500 V	> 10 ⁹	Ω

MAXIMUM RATINGS (Note 1)

 $T_A = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameters	Value	Unit
Total Devic	Ce	· ·	
TSTG	Storage Temperature	-40 to +150	°C
TOPR	Operating Temperature	-40 to +85	°C
ТJ	Junction Temperature Range	-40 to +100	°C
TSOL	Lead Solder Temperature	260 for 10 seconds	°C
	Total Device Power Dissipation at 25°C Ambient	330	mW
PD	Derate Above 25°C	4.4	mW/°C
Emitter	•	· ·	
١F	Continuous Forward Current	60	mA
VR	Reverse Voltage	3	V
	Total Power Dissipation at 25°C Ambient	100	mW
PD	Derate Above 25°C	1.33	mW/°C
Detector			
VDRM	Off-State Output Terminal Voltage	800	V
ITSM	Peak Non-Repetitive Surge Current (Single Cycle 60 Hz Sine Wave)	1	А
	Total Power Dissipation at 25°C Ambient	300	mW
PD	Derate Above 25°C	4	mW/°C

1. Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ELECTRICAL CHARACTERISTICS

 $T_A = 25^{\circ}C$ unless otherwise specified.

INDIVIDUAL COMPONENT CHARACTERISTICS

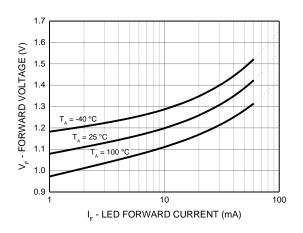
Symbol	Parameters	Test Conditions		Min.	Тур.	Max.	Unit	
EMITTER								
VF	Input Forward Voltage	IF = 10 mA			1.18	1.50	V	
IR	Reverse Leakage Current	V _R = 3 V			0.05	100	μA	
DETECTOR	ł							
IDRM	Peak Blocking Current, Either Direction	V _{DRM} = 800 V, I _F = 0 (Note 2)			10	200	nA	
VTM	Peak On-State Voltage, Either Direction	$I_{TM} = 100 \text{ mA peak}, I_F = 0$			2.2	2.5	V	
dv/dt	Critical Rate of Rise of Off-State Voltage	e I _F = 0, V _{DRM} = 800 V)		1000			V/µs	
TRANSFER CHARACTERISTICS								
Symbol	DC Characteristics	Test	Conditions	Device	Min.	Тур.	Max.	Unit
		Main Terminal MOC3071M				15		
IFT	LED Trigger Current, Either Direction	Voltage	= 3 V (Note 3)	MOC3072M			10	mA
Ι _Η	Holding Current, Either Direction	All			540		μA	
ISOLATION	CHARACTERISTICS						•	
Symbol	Characteristic	Test Conditions		Min.	Тур.	Max.	Unit	
VISO	Input-Output Isolation Voltage (Note 4)	f = 60 Hz, t = 1 Minute		4170			VAC _{RMS}	
RISO	Isolation Resistance	$V_{I-O} = 500 V_{DC}$			10 ¹¹		Ω	
CISO	Isolation Capacitance	V = 0 V, f = 1 MHz			0.2		pF	

2. Test voltage must be applied within dv/dt rating.

3. All devices are guaranteed to trigger at an IF value less than or equal to max IFT. Therefore, the recommended operating IF lies between maximum IFT (15 mA for MOC3071M, 10 mA for MOC3072M) and absolute maximum IF (60 mA).

4. Isolation voltage, VISO, is an internal device dielectric breakdown rating. For this test, pins 1 and 2 are common, and pins 4, 5 and 6 are common.

TYPICAL CHARACTERISTICS





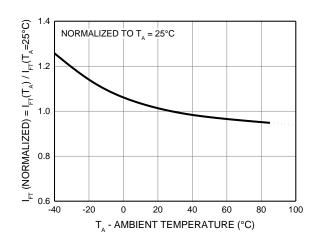


Figure 3. LED Trigger Current vs. Ambient Temperature

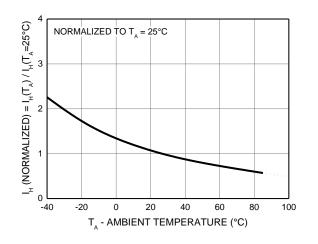


Figure 5. Holding Current vs. Ambient Temperature

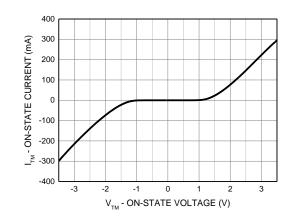
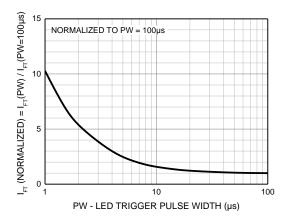
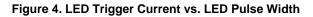


Figure 2. On-State Characteristics





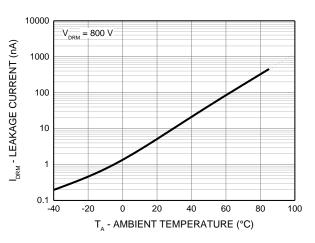


Figure 6. Leakage Current vs. Ambient Temperature

APPLICATIONS INFORMATION

Basic Triac Driver Circuit

The random phase triac drivers MOC3071M and MOC3072M can allow snubberless operations in applications where load is resistive and the external generated noise in the AC line is below its guaranteed dv/dt withstand capability. For these applications, a snubber circuit is not necessary when a noise insensitive power triac is used. Figure 7 shows the circuit diagram. The triac driver is directly connected to the triac main terminal 2 and a series resistor R which limits the current to the triac driver. Current limiting resistor R must have a minimum value which restricts the current into the driver to maximum 1 A.

The power dissipation of this current limiting resistor and the triac driver is very small because the power triac carries the load current as soon as the current through driver and current limiting resistor reaches the trigger current of the power triac. The switching transition times for the driver is only one micro second and for power triacs typical four micro seconds.

Triac Driver Circuit for Noisy Environments

When the transient rate of rise and amplitude are expected to exceed the power triacs and triac drivers maximum ratings a snubber circuit as shown in Figure 8 is recommended. Fast transients are slowed by the R-C snubber and excessive amplitudes are clipped by the Metal Oxide Varistor MOV.

Triac Driver Circuit for Extremely Noisy Environments As specified in the noise standards IEEE472 and IEC255-4.

Industrial control applications do specify a maximum transient noise dv/dt and peak voltage which is superimposed onto the AC line voltage. In order to pass this environment noise test a modified snubber network as shown in Figure 9 is recommended.

LED Trigger Current versus Temperature

Recommended operating LED control current I_F lies between the guaranteed I_{FT} and absolute maximum I_F . Figure 3 shows the increase of the trigger current when the device is expected to operate at an ambient temperature below 25°C. Multiply the datasheet guaranteed I_{FT} with the normalized I_{FT} shown on this graph and an allowance for LED degradation over time.

Example:

$$\begin{split} I_{FT} &= 10 \text{ mA, LED degradation factor} = 20\% \\ I_F \text{ at } -40^\circ\text{C} &= 10 \text{ mA x } 1.25 \text{ x } 120\% = 15 \text{ mA} \end{split}$$

LED Trigger Current vs. Pulse Width

Random phase triac drivers are designed to be phase controllable. They may be triggered at any phase angle within the AC sine wave. Phase control may be accomplished by an AC line zero cross detector and a variable pulse delay generator which is synchronized to the zero cross detector. The same task can be accomplished by a microprocessor which is synchronized to the AC zero crossing. The phase controlled trigger current may be a very short pulse which saves energy delivered to the input LED. LED trigger pulse currents shorter than 100 µs must have increased amplitude as shown on Figure 4. This graph shows the dependency of the trigger current IFT versus the pulse width. IFT in this graph is normalized in respect to the minimum specified I_{FT} for static condition, which is specified in the device characteristic. The normalized IFT has to be multiplied with the devices guaranteed static trigger current. Example:

> $I_{FT} = 10 \text{ mA}$, Trigger PW = 4 µs I_F (pulsed) = 10 mA x 3 = 30 mA

Minimum LED Off Time in Phase Control Applications In phase control applications, one intends to be able to control each AC sine half wave from 0° to 180° . Turn on at 0° means full power and turn on at 180° means zero power. This is not quite possible in reality because triac driver and triac have a fixed turn on time when activated at zero degrees. At a phase control angle close to 180° the driver's turn on pulse at the trailing edge of the AC sine wave must be limited to end 200 µs before AC zero cross as shown in Figure 10. This assures that the triac driver has time to switch off. Shorter times may cause loss of control at the following half cycle.

Static dv/dt

Critical rate of rise of off-state voltage or static dv/dt is a triac characteristic that rates its ability to prevent false triggering in the event of fast rising line voltage transients when it is in the off-state. When driving a discrete power triac, the triac driver optocoupler switches back to off-state once the power triac is triggered. However, during the commutation of the power triac in application where the load is inductive, both triacs are subjected to fast rising voltages. The static dv/dt rating of the triac driver optocoupler and the commutating dv/dt rating of the power triac must be taken into consideration in snubber circuit design to prevent false triggering and commutation failure.

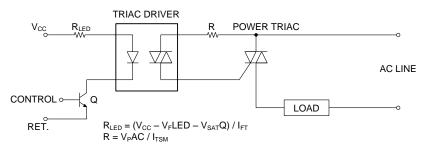
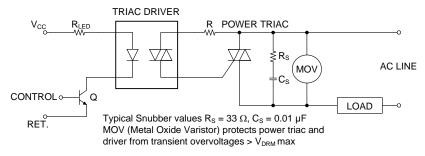
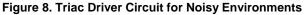


Figure 7. Basic Driver Circuit





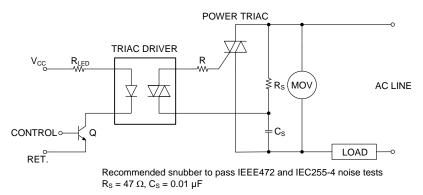
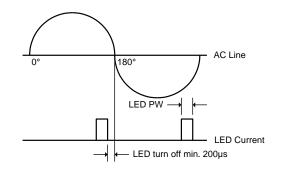
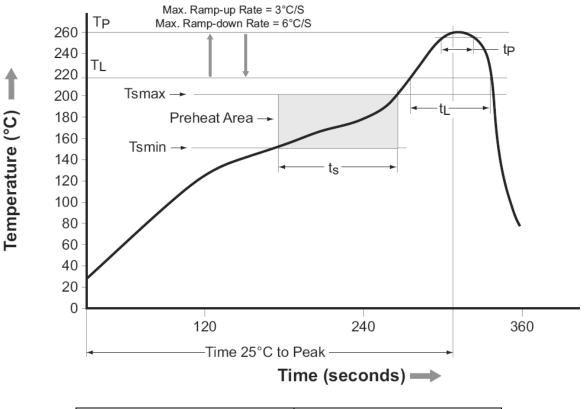


Figure 9. Triac Driver Circuit for Extremely Noisy Environments





REFLOW PROFILE



Profile Feature	Pb-Free Assembly Profile
Temperature Minimum (Tsmin)	150°C
Temperature Maximum (Tsmax)	200°C
Time (t _S) from (Tsmin to Tsmax)	60 seconds to 120 seconds
Ramp-up Rate (TL to TP)	3°C/second maximum
Liquidous Temperature (TL)	217°C
Time (tL) Maintained Above (TL)	60 seconds to 150 seconds
Peak Body Package Temperature	260°C +0°C / –5°C
Time (tp) within 5°C of 260°C	30 seconds
Ramp-down Rate (Tp to TL)	6°C/second maximum
Time 25°C to Peak Temperature	8 minutes maximum

Figure 11. Reflow Profile

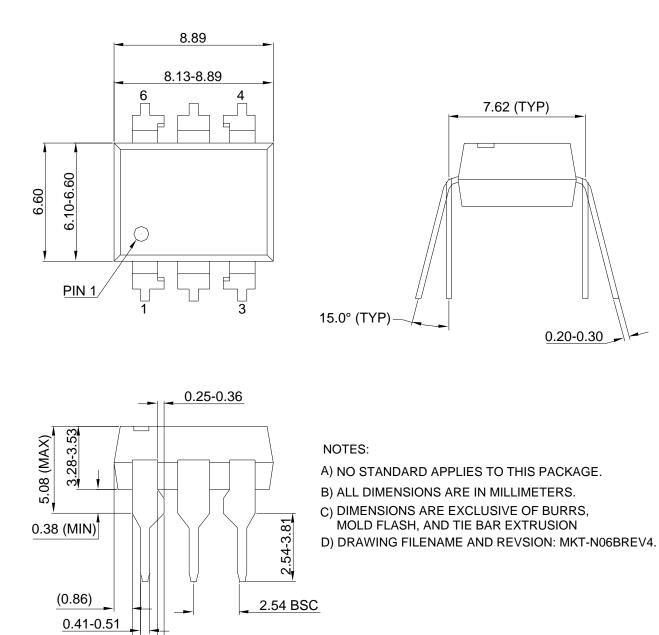
ORDERING INFORMATION (Note 5)

Device	Package	Shipping
MOC3071M	DIP 6-Pin	Tube (50 Units)
MOC3071SM	SMT 6-Pin (Lead Bend)	Tube (50 Units)
MOC3071SR2M	SMT 6-Pin (Lead Bend)	Tape and Reel (1000 Units)
MOC3071VM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option (pending approval)	Tube (50 Units)
MOC3071SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option (pending approval)	Tube (50 Units)
MOC3071SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option (pending approval)	Tape and Reel (1000 Units)
MOC3071TVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option (pending approval)	Tube (50 Units)

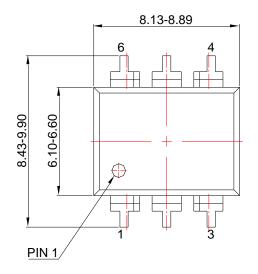
5. The product orderable part number system listed in this table also applies to the MOC3072M product families.

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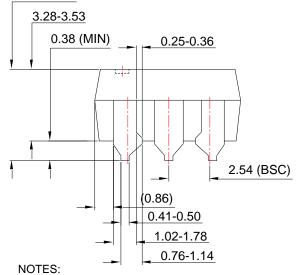
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6 LEAD MDIP OPTO WHITE 0.3" WIDE



5.08 (MAX)



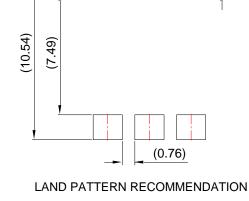
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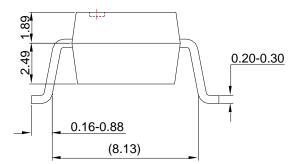
6-LEAD MDIP OPTO WHITE SURFACE MOUNT FORM

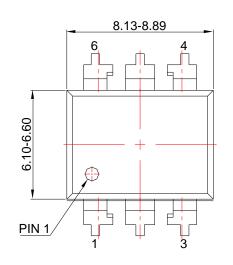


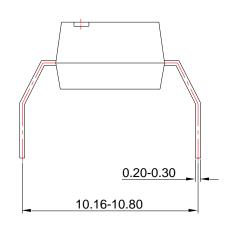
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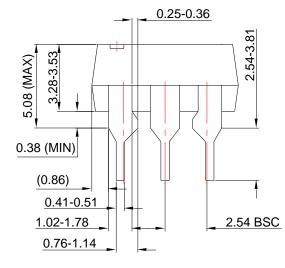
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6 LEAD MDIP OPTO WHITE 0.4" LEAD SPACING

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