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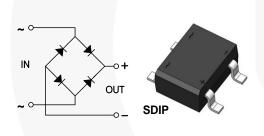
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## DF005S - DF10S Bridge Rectifiers

### Features

- Maximum Surge Rating:  $I_{FSM} = 50 \text{ A}$  $I^2t = 10 \text{ A}^2\text{Sec}$
- Optimized V<sub>F</sub>: Typical 0.94 V at 1.5 A, 25°C
- Glass Passivated Junctions
- Lead Free Compliant to EU RoHS 2002/95/EU Directives The design offers an surge rating of 50 A. This is impor-
- Green Molding Compound: IEC61249
- Qualified with IR Reflow and Wave Soldering
- UL Certified, UL #E258596



### Description

With the ever-pressing need to improve power supply efficiency, improve surge rating, improve reliability, and reduce size, the DFxS family sets a standard in performance.

The design offers an surge rating of 50 A. This is important when improving reliability and increasing efficiency. High efficiency designs strive to reduce circuit resistance, which, unfortunately can result in increased inrush surge. As such high surge current ratings can be required to maintain or improve reliability.

The design also offers better efficiency by achieving a 1.5 A V<sub>F</sub> of 1.1 V maximum at 25°C. This lower V<sub>F</sub> also supports cooler and more efficient operation.

Finally, the DFxS achieves all this in a SDIP surface mount form factor, reducing board space and volumetric requirements vs. competitive devices.

### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
DF005S	DF005S	SDIP 4L	Tape and Reel
DF01S	DF01S	SDIP 4L	Tape and Reel
DF02S	DF02S	SDIP 4L	Tape and Reel
DF04S	DF04S	SDIP 4L	Tape and Reel
DF06S	DF06S	SDIP 4L	Tape and Reel
DF08S	DF08S	SDIP 4L	Tape and Reel
DF10S	DF10S	SDIP 4L	Tape and Reel

May 2015

### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value						Unit	
Oymbol	T arameter	DF005S	DF01S	DF02S	DF04S	DF06S	DF08S	DF10S	
V <sub>RRM</sub>	Maximum Repetitive Reverse Voltage	50	100	200	400	600	800	1000	V
V <sub>RMS</sub>	Maximum RMS Bridge Input Voltage	35	70	140	280	420	560	700	V
V <sub>DC</sub>	DC Reverse Voltage at Rated I <sub>R</sub>	50	100	200	400	600	800	1000	V
I <sub>F(AV)</sub>	Average Rectified Forward Current at $T_A = 40^{\circ}C$				1.5				А
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine Wave				50				А
T <sub>STG</sub>	Storage Temperature Range			-5	5 to +15	0			°C
ТJ	Operating Junction Temperature			-5	5 to +15	0			°C

### **Thermal Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	F	Value	Unit	
PD	Power Dissipation		3.1	W
		Single-Die Measurement <sup>(1)</sup> (Maximum Land Pattern: 13 x 13 mm)	62	
$R_{ extsf{ heta}JA}$	R <sub>0JA</sub> Thermal Resistance, Junction-to-Ambient	Multi-Die Measurement <sup>(2)</sup> (Maximum Land Pattern: 13 x 13 mm)	50	°C/W
		Multi-Die Measurement <sup>(2)</sup> (Minimum Land Pattern: 1.3 x 1.5 mm)	105	
Ψjl	Thermal Characterization Parameter, Junction to Lead	Single-Die Measurement <sup>(2)</sup> (Maximum and Minimum Land Pattern)	27	°C/W

### Notes:

1. Device mounted on PCB with 0.5 inch  $\times$  0.5 inch (13 mm  $\times$  13 mm).

2. The thermal resistances ( $R_{\theta JA} \& \psi_{JL}$ ) are characterized with the device mounted on the following FR4 printed circuit boards, as shown in Figure 1 and Figure 2. PCB size: 76.2 x 114.3 mm.

Heating effect from adjacent dice is considered and only two dices are powered at the same time.

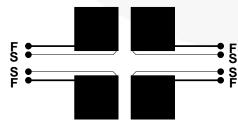


Figure 1. Maximum Pads of 2 oz Copper

Figure 2. Minimum Pads of 2 oz Copper

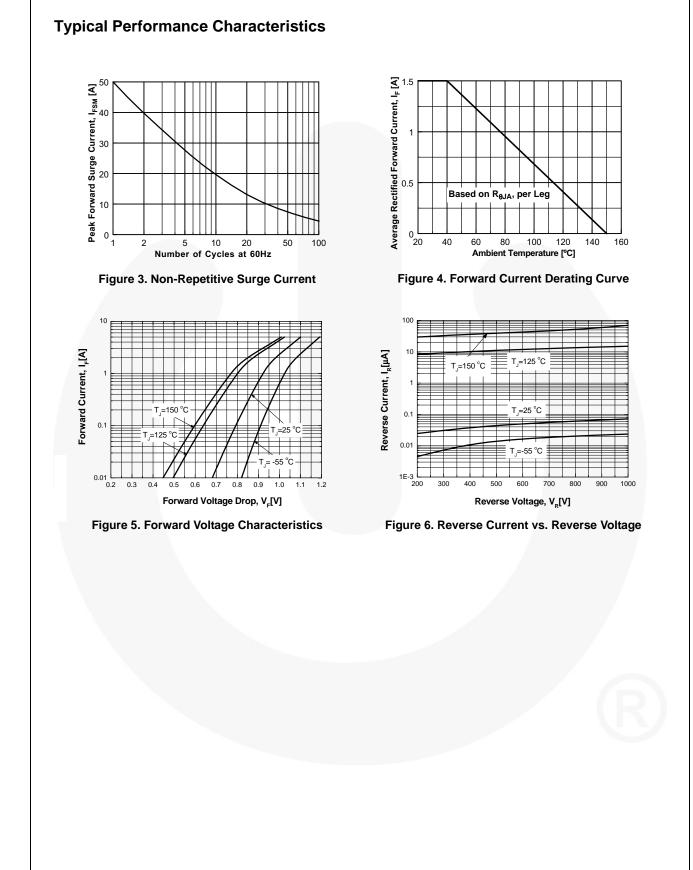
# DF005S - DF10S — Bridge Rectifiers

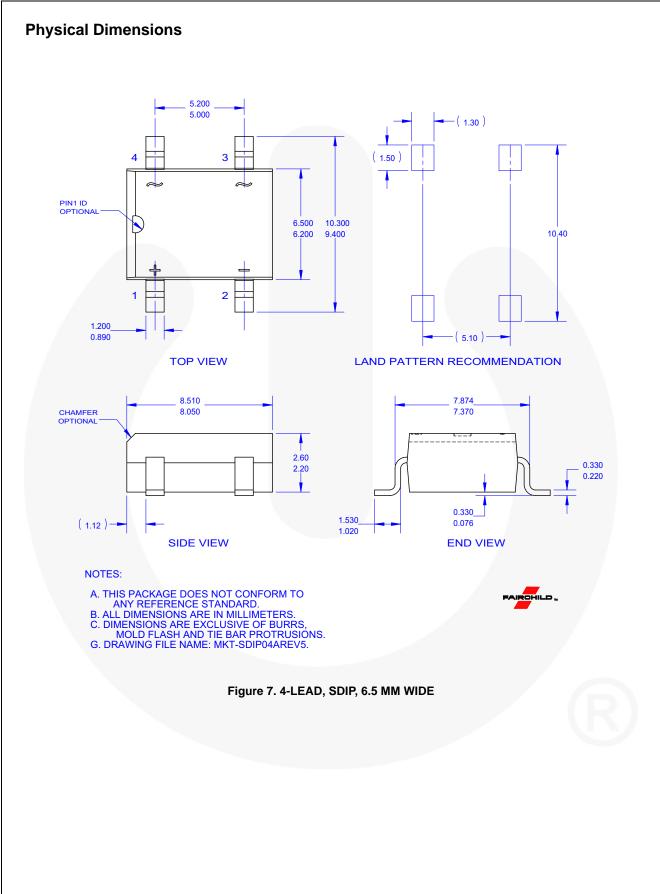
### **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
V <sub>F</sub>	Forward Voltage, per Element	I <sub>F</sub> = 1.5 A			1.1	V	
1	Reverse Current, per Element at Rated V <sub>R</sub>	$T_A = 25^{\circ}C$			5.0		
۱ <sub>R</sub>	Reverse Current, per Element at Rated VR	$T_A = 125^{\circ}C$			500	μA	
l <sup>2</sup> t	Rating for Fusing (t < 8.35 ms)				10	A <sup>2</sup> s	
CJ	Typical Capacitance, per Leg	V <sub>R</sub> = 4.0 V, f = 1.0 MHz		25		pF	







**DF005S - DF10S** 

Bridge Rectifiers

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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