## VS-UFL230FA60

**Vishay Semiconductors** 

## Insulated Ultrafast Rectifier Module, 230 A



PRIMARY CHARACTERISTICS						
V <sub>R</sub> 600 V						
$I_{F(AV)}$ per module at $T_C = 102 \ ^{\circ}C$	230 A					
t <sub>rr</sub>	44 ns					
Туре	Modules - diode FRED Pt®					
Package	SOT-227					

#### FEATURES

- Two fully independent diodes
- Fully insulated package
- Ultrafast, soft reverse recovery, with high operation junction temperature (T<sub>J</sub> max. = 175 °C)
   RoHS
   RoHS
   COMPLIANT
- Low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- Easy to use and parallel
- Industry standard outline
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **DESCRIPTION / APPLICATIONS**

The VS-UFL230FA60 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V <sub>R</sub>		600	V		
Continuous forward current per diode	١ <sub>F</sub>	T <sub>C</sub> = 85 °C	160	А		
Single pulse forward current per diode	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	1500			
Maximum power dissipation per module	PD	T <sub>C</sub> = 85 °C	416	W		
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C		







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<b>ELECTRICAL SPECIFICATIONS PER DIODE</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS N		TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 100 μA	600	-	-	
Forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 100 A	-	1.28	1.44	V
		$I_F = 100 \text{ A}, T_J = 125 ^\circ\text{C}$	-	1.13	1.24	
		I <sub>F</sub> = 200 A	-	1.48	1.66	
		I <sub>F</sub> = 200 A, T <sub>J</sub> = 125 °C	-	1.37	1.55	
Reverse leakage current	I <sub>RM</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	0.1	50	μA
		$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	-	0.25	2	mA
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	72	-	pF

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	I <sub>F</sub> = 1.0 A, dI <sub>F</sub> /dt = 200 A/µs, V <sub>R</sub> = 30 V		-	44	-		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 50 A dI <sub>F</sub> /dt = 200 A/µs V <sub>R</sub> = 200 V	-	104	-	ns
		T <sub>J</sub> = 125 °C		-	210	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	10	-	А
		T <sub>J</sub> = 125 °C		-	22	-	~
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	520	-	nC
		T <sub>J</sub> = 125 °C		-	2200	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	Р		-	-	0.43	
Junction to case, both leg conducting	R <sub>thJC</sub>		-	-	0.215	°C/W
Case to heatsink	R <sub>thCS</sub>	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				S	OT-227	



VS-UFL230FA60

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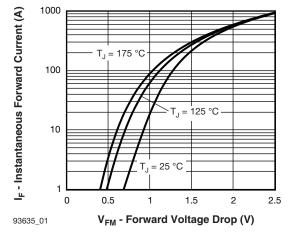
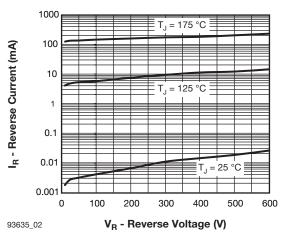
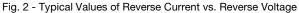


Fig. 1 - Typical Forward Voltage Drop Characteristics





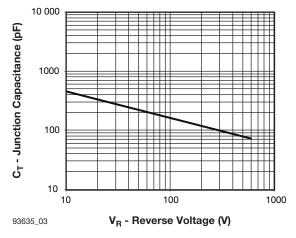
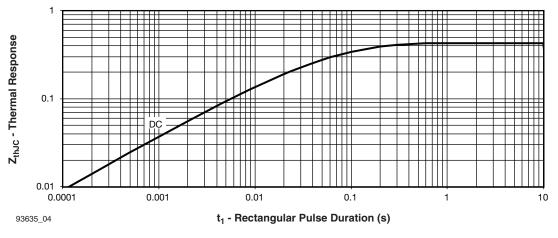


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

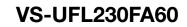




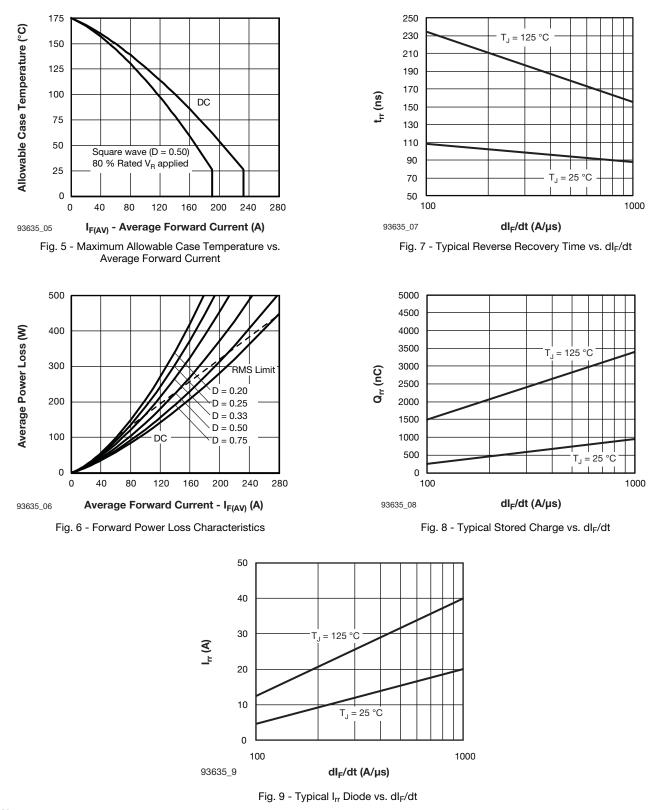
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<sup>&</sup>lt;sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $<sup>\</sup>begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 



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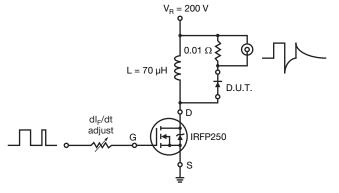


Fig. 10 - Reverse Recovery Parameter Test Circuit

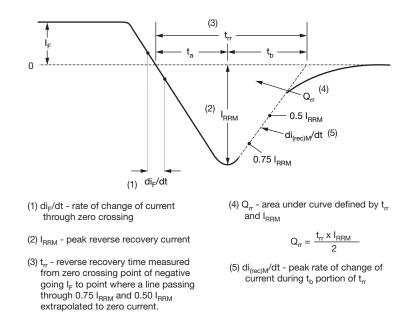
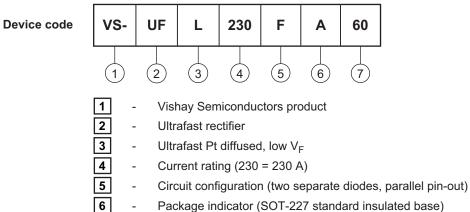


Fig. 11 - Reverse Recovery Waveform and Definitions





#### **ORDERING INFORMATION TABLE**



- Package indicator (SOT-227 standard insulated base) -
- Voltage rating (60 = 600 V)

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CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two separate diodes, parallel pin-out	F	Lead Assignment			

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95423					
Packaging information	www.vishay.com/doc?95425				



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