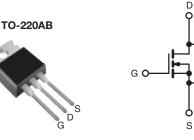


Vishay Siliconix



Power MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	600				
R _{DS(on)} (Ω)	V _{GS} = 10 V 1.2				
Q _g (Max.) (nC)	60				
Q _{gs} (nC)	8.3				
Q _{gd} (nC)	30				
Configuration	Single				



N-Channel MOSFET

FEATURES

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Fast Switching
- Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC

DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION				
Package	TO-220AB			
Lead (Pb)-free	IRFBC40PbF			
Leau (FD)-hee	SiHFBC40-E3			
SnPb	IRFBC40			
	SiHFBC40			

ABSOLUTE MAXIMUM RATINGS ($T_{\rm C}$	= 25 °C, unl	ess otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V _{DS}	600	M	
Gate-Source Voltage			V _{GS}	± 20	- V	
Continuous Drain Current	V at 10.V	T _C = 25 °C	I	6.2		
Continuous Drain Current	V _{GS} at 10 V	$T_C = 100 \ ^\circ C$	ID	3.9	А	
Pulsed Drain Current ^a			I _{DM}	25		
Linear Derating Factor				1.0	W/°C	
Single Pulse Avalanche Energy ^b			E _{AS}	570	mJ	
Repetitive Avalanche Current ^a			I _{AR}	6.2	A	
Repetitive Avalanche Energy ^a			E _{AR}	13	mJ	
Maximum Power Dissipation $T_{\rm C} = 25 ^{\circ}{\rm C}$			PD	125	W	
Peak Diode Recovery dV/dt ^c			dV/dt	3.0	V/ns	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	- 55 to + 150		
Soldering Recommendations (Peak Temperature)	Temperature) for 10 s			300 ^d	- °C	
Mounting Torque	6-32 or M3 screw			10	lbf ∙ in	
Mounting Torque				1.1	N · m	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. V_{DD} = 50 V, starting T_J = 25 °C, L = 27 mH, R_g = 25 Ω , I_{AS} = 6.2 A (see fig. 12).

c. $I_{SD} \leq 6.2$ A, dl/dt ≤ 80 A/µs, $V_{DD} \leq V_{DS}$, $T_J \leq 150$ °C.

d. 1.6 mm from case.

* Pb containing terminations are not RoHS compliant, exemptions may apply

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COMPLIANT

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THERMAL RESISTANCE RATINGS									
PARAMETER	SYMBOL	TYP.		MAX.		UNIT			
Maximum Junction-to-Ambient	R _{thJA}	-		62		1			
Case-to-Sink, Flat, Greased Surface	R _{thCS}	0.50				°C/W			
Maximum Junction-to-Case (Drain)	R _{thJC}	-	- 1.0						
SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$, u	nless otherwi	ise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT		
Static									
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} =	0 V, I _D = 2	250 µA	600	-	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	to 25 °C,	$I_D = 1 \text{ mA}$	-	0.7	-	V/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V	$V_{GS}, I_D = 2$	250 µA	2.0	-	4.0	V	
Gate-Source Leakage	I _{GSS}	Vo	_{GS} = ± 20	V	-	-	± 100	nA	
Zara Cata Valtaga Drain Currant		$V_{DS} = 6$	600 V, V _G	_S = 0 V	-	-	100		
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 480 V,	$V_{DS} = 480 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$		-	-	500	μΑ	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V I _D = 3.7A ^b		-	-	1.2	Ω		
Forward Transconductance	9 _{fs}	$V_{DS} = 100 \text{ V}, \text{ I}_{D} = 3.7 \text{ A}^{b}$		4.7	-	-	S		
Dynamic									
Input Capacitance	C _{iss}	$V_{GS} = 0 V,$ $V_{DS} = 25 V,$		-	1300	-	pF		
Output Capacitance	C _{oss}			-	160	-			
Reverse Transfer Capacitance	C _{rss}	f = 1.0 MHz, see fig. 5		-	30	-			
Total Gate Charge	Qg				-	-	60		
Gate-Source Charge	Q _{gs}	$V_{GS} = 10 V$		2 A, V _{DS} = 360 V, fig. 6 and 13 ^b	-	-	8.3	nC	
Gate-Drain Charge	Q _{gd}	-	See	lig. 6 and 15°	-	-	30		
Turn-On Delay Time	t _{d(on)}				-	13	-	-	
Rise Time	t _r	V _{DD} = 3	800 V, I _D =	= 6.2 A,	-	18	-		
Turn-Off Delay Time	t _{d(off)}	$R_g = 9.1 \ \Omega$, $R_D = 47 \ \Omega$, see fig. 10^b		-	55	-	ns		
Fall Time	t _f			-	20	-			
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-			
Internal Source Inductance	Ls			-	7.5	-	- nH		
Drain-Source Body Diode Characteristic	s								
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	6.2			
Pulsed Diode Forward Current ^a	I _{SM}			-	-	25	A		
Body Diode Voltage	V _{SD}	$T_J = 25 \text{ °C}, I_S = 6.2 \text{ A}, V_{GS} = 0 \text{ V}^{\text{b}}$		-	-	1.5	V		
Body Diode Reverse Recovery Time	t _{rr}	$T_J = 25 \text{ °C}, I_F = 6.2 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}^b$		-	450	940	ns		
Body Diode Reverse Recovery Charge	Q _{rr}			-	3.8	7.9	μC		
Douy Didde Heverse Hecovery Charge	Grr					0.0	1.5		

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

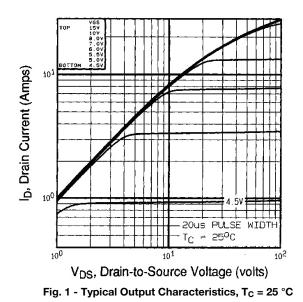
b. Pulse width \leq 300 µs; duty cycle \leq 2 %.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

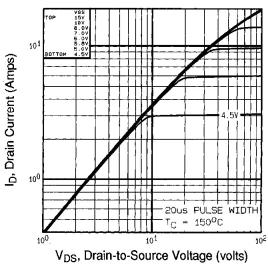


Fig. 2 - Typical Output Characteristics, T_C = 150 °C

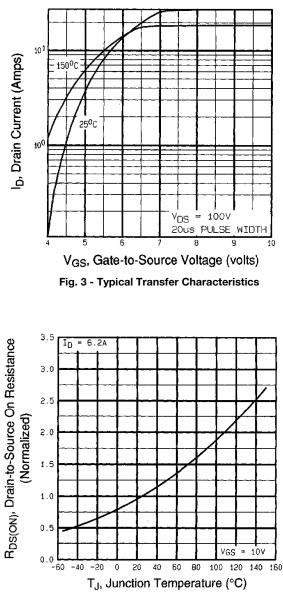


Fig. 4 - Normalized On-Resistance vs. Temperature

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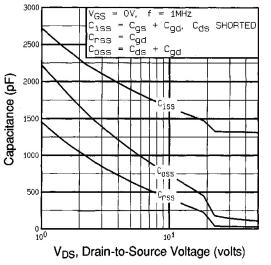
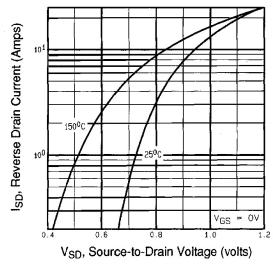
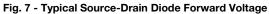


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage





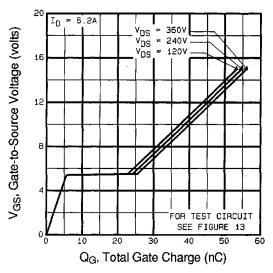
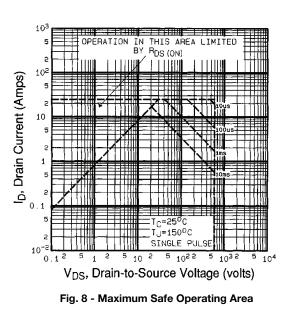


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage



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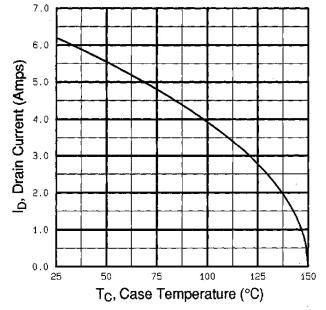


Fig. 9 - Maximum Drain Current vs. Case Temperature

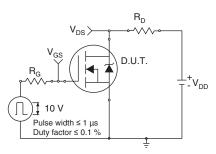


Fig. 10a - Switching Time Test Circuit

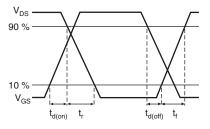


Fig. 10b - Switching Time Waveforms

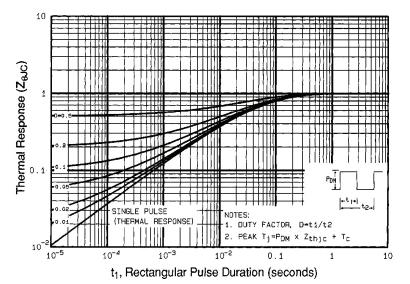


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

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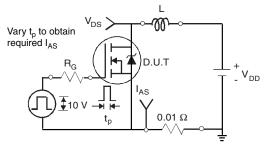


Fig. 12a - Unclamped Inductive Test Circuit

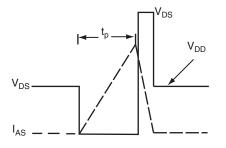


Fig. 12b - Unclamped Inductive Waveforms

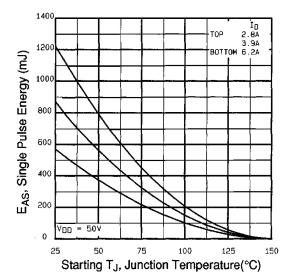


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

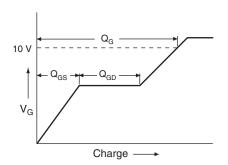


Fig. 13a - Basic Gate Charge Waveform

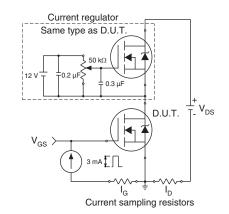
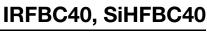


Fig. 13b - Gate Charge Test Circuit

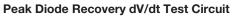
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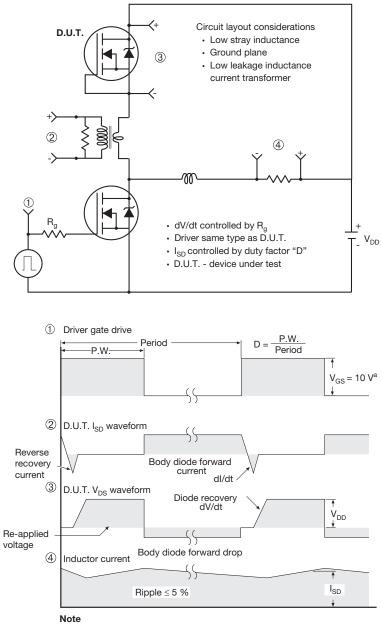
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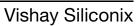


a. $V_{GS} = 5 V$ for logic level devices

Fig. 14 - For N-Channel

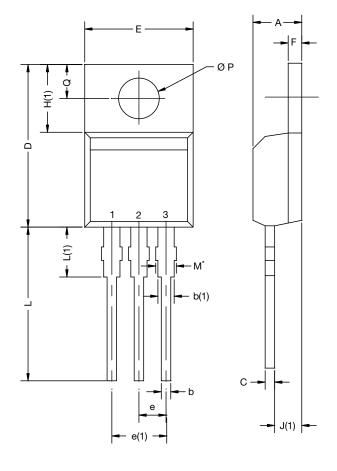
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TO-220-1



DIM.	MILLIN	IETERS	INCHES		
Dilvi.	MIN.		MIN.	MAX.	
А	4.24	4.65	0.167	0.183	
b	0.69	1.02	0.027	0.040	
b(1)	1.14	1.78	0.045	0.070	
С	0.36	0.61	0.014	0.024	
D	14.33	15.85	0.564	0.624	
E	9.96	10.52	0.392	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.10	6.71	0.240	0.264	
J(1)	2.41	2.92	0.095	0.115	
L	13.36	14.40	0.526	0.567	
L(1)	3.33	4.04	0.131	0.159	
ØР	3.53	3.94	0.139	0.155	
Q	2.54	3.00	0.100	0.118	
ECN: X15-0364-Rev. C, 14-Dec-15 DWG: 6031					

Note

- M^{\star} = 0.052 inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM

Package Picture						
ASE		Xi'an				
EGNEOA 7KAB 193 Co A		IRF 9510 744K AB 25 (C) (A)				

Revison: 14-Dec-15

Document Number: 66542

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