

TIP120, TIP121, TIP122 TIP125, TIP126, TIP127

Complementary power Darlington transistors

Features

- Low collector-emitter saturation voltage
- Complementary NPN PNP transistors

Applications

■ General purpose linear and switching

Description

The devices are manufactured in planar technology with "base island" layout and monolithic Darlington configuration. The resulting transistors show exceptional high gain performance coupled with very low saturation voltage.

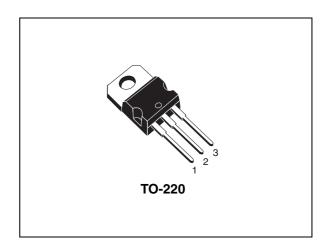


Figure 1. Internal schematic diagrams

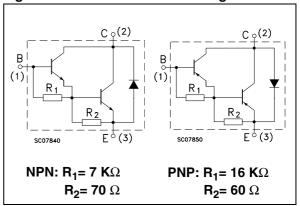


Table 1. Device summary

Order codes	Marking	Package	Packaging
TIP120	TIP120	TIP120	
TIP121	TIP121		
TIP122	TIP122	TIP122 TO-220 Tube	
TIP125	TIP125		
TIP126	TIP126		
TIP127	TIP127		

November 2008 Rev 4 1/13

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1 Electrical ratings

Table 2. Absolute maximum rating⁽¹⁾

Symbol	Parameter		Value			Unit
		NPN	TIP120	TIP121	TIP122	
		PNP	TIP125	TIP126	TIP127	
V _{CBO}	Collector-base voltage (I _E = 0)		60	80	100	V
V _{CEO}	Collector-emitter voltage (I _B = 0)		60	80	100	٧
V _{EBO}	Emitter-base voltage (I _C = 0)		5			٧
I _C	Collector current		5			Α
I _{CM}	Collector peak current		8			Α
I _B	Base current		0.12			Α
P _{TOT}	Total dissipation at T _c ≤25 °C T _{amb} ≤25 °C		65 2			W
T _{stg}	Storage temperature		-65 to 150			°C
T _J	Max. operating junction temperature		150			

^{1.} For PNP types voltage and current values are negative.

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max.	1.92	°C/W
R _{thj-amb}	Thermal resistance junction-ambient max.	62.5	C/VV

2 Electrical characteristics

(T_{case} = 25 °C; unless otherwise specified)

Table 4. Electrical characteristics⁽¹⁾

Symbol	Parameter	Test con	Min.	Тур.	Max.	Unit	
I _{CEO}	Collector cut-off current	for TIP120/125 for TIP121/126	$V_{CE} = 30 \text{ V}$ $V_{CF} = 40 \text{ V}$			0.5 0.5	mA mA
·CEO	(I _B = 0)	for TIP122/127	$V_{CE} = 50 \text{ V}$			0.5	mA
	Collector cut-off current	for TIP120/125	V _{CE} = 60 V			0.2	mA
I _{CBO}	$(I_{\rm B}=0)$	for TIP121/126	$V_{CE} = 80 \text{ V}$			0.2	mA
	(IB – 0)	for TIP122/127	$V_{CE} = 100 \text{ V}$			0.2	mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 5 V				2	mA
V _{CEO(sus)} ⁽²⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C = 30 mA for TIP120/125 for TIP121/126 for TIP122/127		60 80 100			V V V
V _{CE(sat)} ⁽²⁾	Collector-emitter saturation voltage	I _C = 3 A I _C = 5 A	$I_B = 12 \text{ mA}$ $I_B = 20 \text{ mA}$			2 4	V V
V _{BE(on)} ⁽²⁾	Base-emitter on voltage	I _C = 3 A	V _{CE} = 3 V			2.5	V
h _{FE} ⁽²⁾	DC current gain	$I_C = 0.5 A$ $I_C = 3 A$	$V_{CE} = 3 V$ $V_{CE} = 3 V$	1000 1000			

^{1.} For PNP types voltage and current values are negative.

^{2.} Pulsed duration = 300 μ s, duty cycle \leq 2%

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

Figure 3. Derating curve

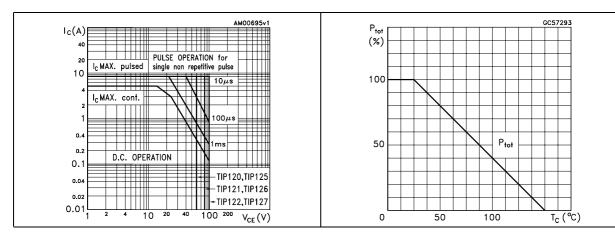


Figure 4. DC current gain for NPN type

Figure 5. DC current gain for PNP type

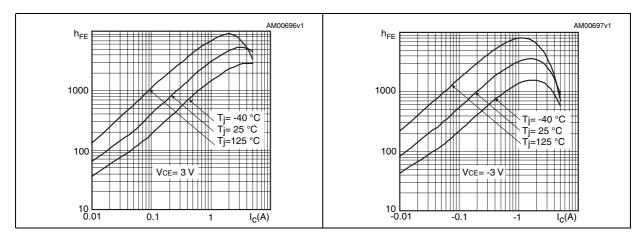


Figure 6. Collector-emitter saturation voltage Figure 7. Collector-emitter saturation voltage for NPN type

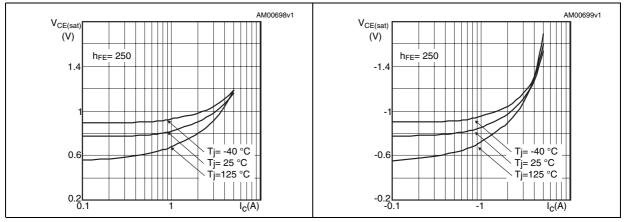


Figure 8. Base-emitter saturation voltage for Figure 9. Base-emitter saturation voltage for NPN type PNP type

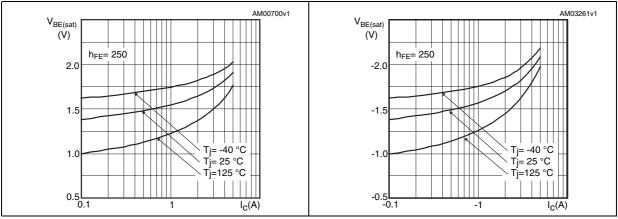


Figure 10. Base-emitter on voltage for NPN type

Figure 11. Base-emitter on voltage for PNP type

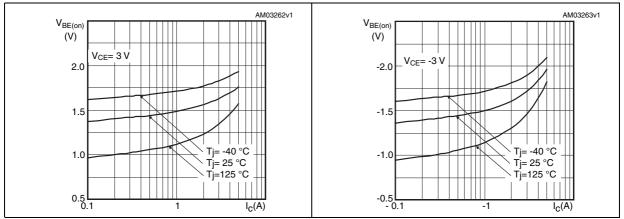


Figure 12. Switching time on resistive load for Figure 13. Switching time on resistive load for NPN type (on)

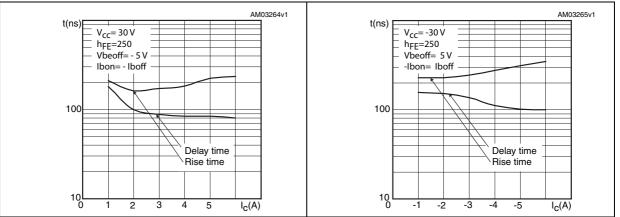


Figure 14. Switching time on resistive load for Figure 15. Switching time on resistive load for NPN type (off)

NPN type (off)

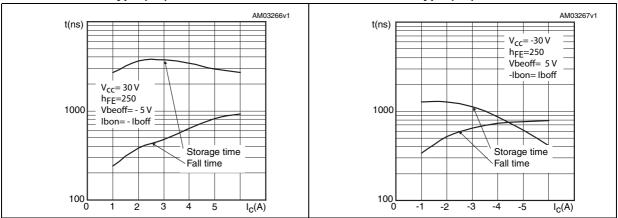
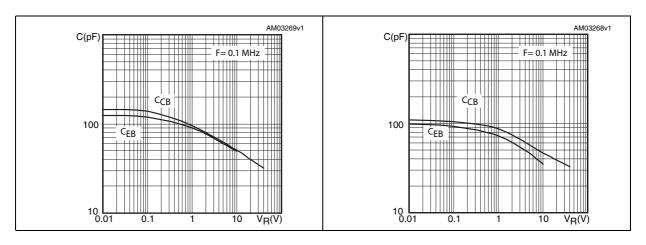


Figure 16. Capacitances for NPN type

Figure 17. Capacitances for PNP type



3 Test circuits

Figure 18. Resistive load switching for NPN type

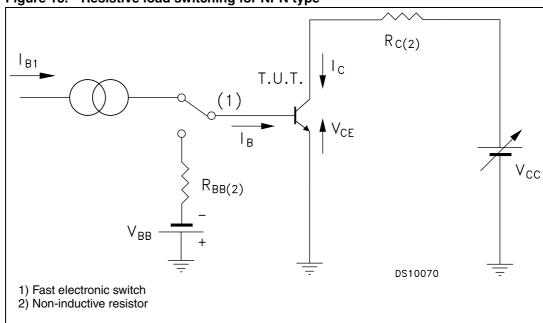
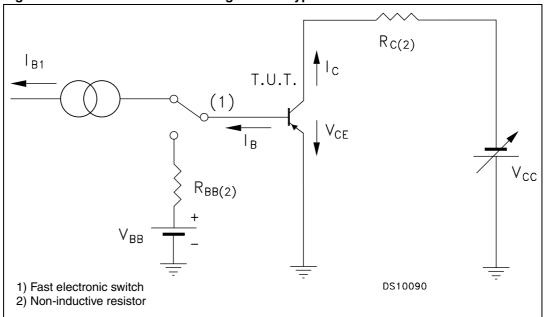


Figure 19. Resistive load switching for PNP type

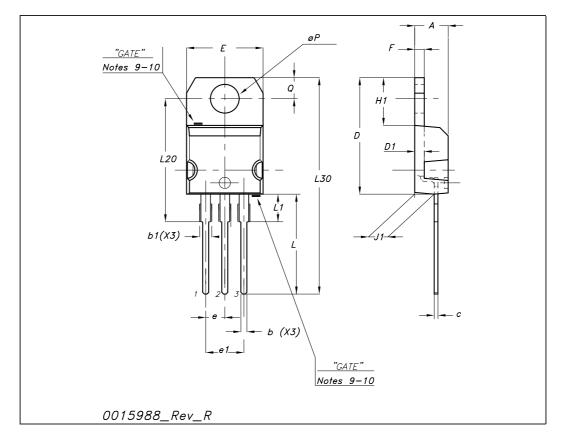


4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

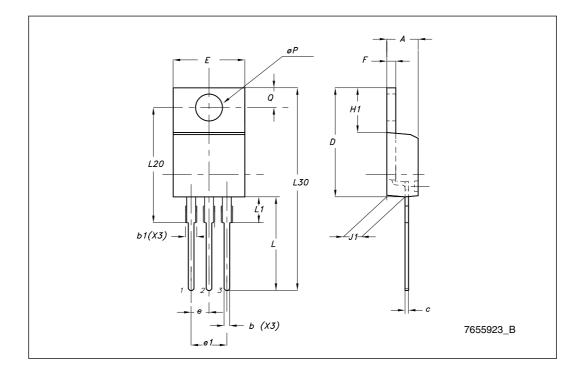
TO-220 mechanical data

Dim		mm			inch			
Dim	Min	Тур	Max	Min	Тур	Max		
Α	4.40		4.60	0.173		0.181		
b	0.61		0.88	0.024		0.034		
b1	1.14		1.70	0.044		0.066		
С	0.48		0.70	0.019		0.027		
D	15.25		15.75	0.6		0.62		
D1		1.27			0.050			
E	10		10.40	0.393		0.409		
е	2.40		2.70	0.094		0.106		
e1	4.95		5.15	0.194		0.202		
F	1.23		1.32	0.048		0.051		
H1	6.20		6.60	0.244		0.256		
J1	2.40		2.72	0.094		0.107		
L	13		14	0.511		0.551		
L1	3.50		3.93	0.137		0.154		
L20		16.40			0.645	İ		
L30		28.90			1.137			
ØP	3.75		3.85	0.147		0.151		
Q	2.65		2.95	0.104		0.116		



TO-220 type E mechanical data

DIM.	mm.					
DIWI.	MIN.	TYP	MAX.			
Α	4.47		4.67			
b	0.70		0.91			
b1	1.17		1.37			
С	0.31		0.53			
D	14.60		15.70			
E	9.96		10.36			
е		2.54				
e1	4.98	5.08	5.18			
F	1.17		1.37			
H1	6.10		6.80			
J1	2.52		2.82			
L	12.70		13.80			
L1	3.20		3.96			
L20	15.21		16.77			
øΡ	3.73		3.94			
Q	2.59		2.89			



5 Revision history

Table 5. Document revision history

Date	Revision	Changes
21-Jun-2004	3	
25-Nov-2008	4	Inserted new Section 2.1: Electrical characteristics (curves)

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