Innovative Service Around the Globe YAGEO

# DATA SHEET

## THICK FILM WIDE TERMINAL CHIP RESISTORS AUTOMOTIVE GRADE

AC series

±5%, ±1%, ±0.5%

**Sizes 0612/1020/1225**

**RoHS compliant & Halogen free**


YAGEO Phycomp

Product specification – December 11, 2015 V.1

## SCOPE

This specification describes AC0612 to AC1225 chip resistors with lead-free terminations made by thick film process.

## APPLICATIONS

- All general purpose applications
- Car electronics, industrial application

## FEATURES

- AEC-Q200 qualified
- Moisture sensitivity level: MSL 1
- AC series soldering is compliant with J-STD-020D
- Halogen free epoxy
- RoHS compliant
  - Products with lead-free terminations meet RoHS requirements
  - Pb-glass contained in electrodes, resistor element and glass are exempted by RoHS
- Reduce environmentally hazardous waste
- High component and equipment reliability
- The resistors are 100% performed by automatic optical inspection prior to taping.

## ORDERING INFORMATION - GLOBAL PART NUMBER

Part number is identified by the series name, size, tolerance, packaging type, temperature coefficient, taping reel and resistance value.

### GLOBAL PART NUMBER

| AC | XXXX | X | X | X | XX | XXXX | L |
|---|---|---|---|---|---|---|---|
| | (1) | (2) | (3) | (4) | (5) | (6) | (7) |

### (1) SIZE

0612/1020/1225

### (2) TOLERANCE

D = ±0.5%

F = ±1%

J = ±5% (for Jumper ordering, use code of J)

### (3) PACKAGING TYPE

R = Paper taping reel

K = Embossed taping reel

### (4) TEMPERATURE COEFFICIENT OF RESISTANCE

– = Base on spec

### (5) TAPING REEL

07 = 7 inch dia. Reel

13 = 13 inch dia. Reel

### (6) RESISTANCE VALUE

1Ω to 1 MΩ

There are 2~4 digits indicated the resistance value. Letter R/K/M is decimal point, no need to mention the last zero after R/K/M, e.g.1K2, not 1K20.

Detailed coding rules of resistance are shown in the table of "Resistance rule of global part number".

### (7) DEFAULT CODE

Letter L is the system default code for ordering only. (Note)

Resistance rule of global part number

| Resistance coding rule | Example |
|---|---|
| XRXX (1 to 9.76 Ω) | 1R = 1 Ω<br>1R5 = 1.5 Ω<br>9R76 = 9.76 Ω |
| XXRX (10 to 97.6 Ω) | 10R = 10 Ω<br>97R6 = 97.6 Ω |
| XXXR (100 to 976 Ω) | 100R = 100 Ω<br>976R = 976 Ω |
| XKXX (1 to 9.76 KΩ) | 1K = 1,000 Ω<br>9K76 = 9760 Ω |
| XMXX (1 to 9.76 MΩ) | 1M = 1,000,000 Ω<br>9M76= 9,760,000 Ω |
| XXMX (10 MΩ) | 10M = 10,000,000 Ω |

### ORDERING EXAMPLE

The ordering code for an AC0612 chip resistor, value 100 KΩ with ±1% tolerance, supplied in 7-inch tape reel is: AC0612FR-07100KL.

### NOTE

1. All our R-Chip products are RoHS compliant and Halogen free. "LFP" of the internal 2D reel label states "Lead-Free Process".
2. On customized label, "LFP" or specific symbol can be printed.
3. AC series with ±0.5% tolerance is also available. For further information, please contact sales.

## MARKING

AC0612 / AC1020 / AC1225

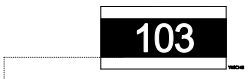



Fig. 1 Value=10 KΩ

E-24 series: 3 digits, ±5%
First two digits for significant figure and 3rd digit for number of zeros


Fig. 2 Value=10 KΩ

E-24/ E-96 series: 4 digits, ±1% & ±0.5%
First three digits for significant figure and 4th digit for number of zeros

## CONSTRUCTION

The resistors are constructed on top of a high-grade ceramic body. Internal metal electrodes are added at each end and connected by the resistive glaze. The resistive glaze is covered by a leadfree glass. The composition of the glaze is adjusted to give the approximate required resistance value and laser trimming of this resistive glaze achieves the value inside tolerance. The whole element is covered by a protective overcoat. Size 0508 and bigger is marked with the resistance value on top. Finally, the two external terminations (Ni / matte tin) are added. See fig.3.

## OUTLINES

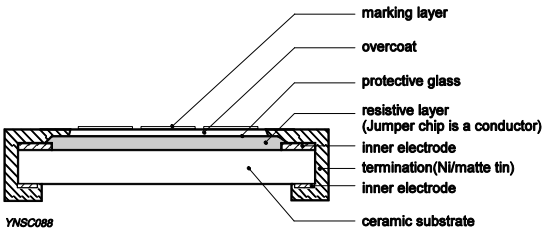


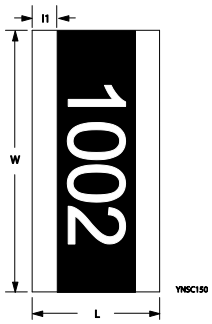

Fig. 3 Chip resistor outlines

## DIMENSIONS

Table 1 For outlines, please refer to Fig. 4

| TYPE | L (mm) | W (mm) | H (mm) | $I_1$ (mm) | $I_2$ (mm) |
|---|---|---|---|---|---|
| AC0612 | 1.60±0.20 | 3.20 ±0.20 | 0.55±0.10 | 0.18±0.15 | 0.40±0.15 |
| AC1020 | 2.50 ±0.20 | 5.00 ±0.20 | 0.55±0.10 | 0.25 ±0.20 | 0.90 ±0.20 |
| AC1225 | 3.20 ±0.20 | 6.40 ±0.20 | 0.55±0.10 | 0.45 ±0.20 | 0.75 ±0.20 |

For dimension, please refer to Table 1




Side view for all type

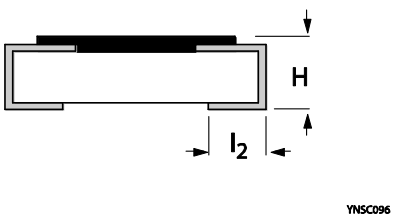



Fig. 4 Chip resistor dimensions

## ELECTRICAL CHARACTERISTICS

Table 2

| TYPE | RESISTANCE RANGE | CHARACTERISTICS | | | | | |
|---|---|---|---|---|---|---|---|
| | | Operating Temperature Range | Max. Working Voltage | Max. Overload Voltage | Dielectric Withstanding Voltage | Temperature Coefficient of Resistance | Jumper Criteria |
| AC0612 | 5% (E24) 1Ω to 1MΩ<br>0.5%, 1% (E24/E96) 1Ω to 1MΩ<br>Jumper < 50mΩ | –55 °C to +155 °C | 200V | 400V | 500V | 1Ω≤R≤10Ω, ±200ppm/°C<br>10Ω<R≤1MΩ, ±100ppm/°C | Rated Current 2A<br>Max. Current 10A |
| AC1020 | | | 200V | 400V | 500V | | |
| AC1225 | | | 200V | 400V | 500V | | |

## FOOTPRINT AND SOLDERING PROFILES

Recommended footprint and soldering profiles of AC-series is the same as RC-series. Please refer to data sheet "Chip resistors mounting".

## PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

| PACKING STYLE | REEL DIMENSION | AC0612 | AC1020 | AC1225 |
|---|---|---|---|---|
| Paper taping reel (R) | 7" (178 mm) | 5,000 | --- | --- |
| | 13" (330 mm) | 20,000 | --- | --- |
| Embossed taping reel (K) | 7" (178 mm) | --- | 4,000 | 4,000 |

**NOTE**

1. For paper/embossed tape and reel specifications/dimensions, please refer to data sheet "Chip resistors packing".

## FUNCTIONAL DESCRIPTION

### OPERATING TEMPERATURE RANGE

Range: –55 °C to +155 °C

### POWER RATING

Each type rated power at 70 °C:
AC0612 =3/4W (0.75W)
AC1020 =1W
AC1225 =2W

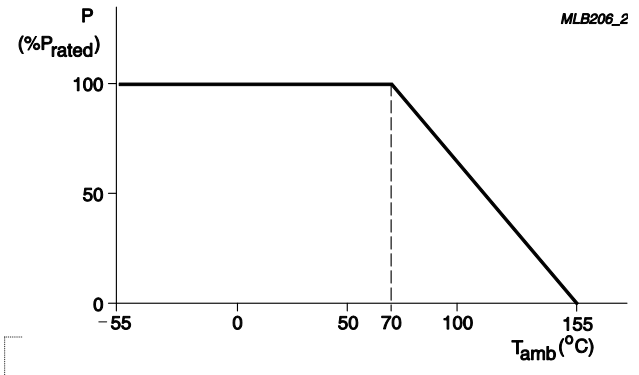



Fig. 10 Maximum dissipation ($P_{max}$) in percentage of rated power as a function of the operating ambient temperature ($T_{amb}$)

### RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

$$V = \sqrt{(P \times R)}$$

Or Maximum working voltage whichever is less

Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

R = Resistance value (Ω)

## TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

| TEST | TEST METHOD | PROCEDURE | REQUIREMENTS |
|---|---|---|---|
| High Temperature Exposure | AEC-Q200 Test 3<br>MIL-STD-202 Method 108 | 1,000 hours at $T_A$ = 155 °C, unpowered | ±(1.0%+0.05Ω) for D/F tol<br>±(2.0%+0.05Ω) for J tol<br><50 mΩ for Jumper |
| Moisture Resistance | AEC-Q200 Test 6<br>MIL-STD-202 Method 106 | Each temperature / humidity cycle is defined at 8 hours (method 106F), 3 cycles / 24 hours for 10d. with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered<br>Parts mounted on test-boards, without condensation on parts | ±(0.5%+0.05Ω) for D/F tol<br>±(2.0%+0.05Ω) for J tol<br><100 mΩ for Jumper |
| Biased Humidity | AEC-Q200 Test 7<br>MIL-STD-202 Method 103 | 1,000 hours; 85 °C / 85% RH<br>10% of operating power<br>Measurement at 24±4 hours after test conclusion. | ±(1.0%+0.05Ω) for D/F tol<br>±(3.0%+0.05Ω) for J tol<br><100 mΩ for Jumper |
| Operational Life | AEC-Q200 Test 8<br>MIL-STD-202 Method 108 | 1,000 hours at 125 °C, derated voltage applied for 1.5 hours on, 0.5 hour off, still-air required | ±(1.0%+0.05Ω) for D/F tol<br>±(3.0%+0.05Ω) for J tol<br><100 mΩ for Jumper |
| Resistance to Soldering Heat | AEC-Q200 Test 15<br>MIL-STD-202 Method 210 | Condition B, no pre-heat of samples<br>Lead-free solder, 260±5 °C, 10±1 seconds immersion time<br>Procedure 2 for SMD: devices fluxed and cleaned with isopropanol | ±(0.5%+0.05Ω) for D/F tol<br>±(1.0%+0.05Ω) for J tol<br><50 mΩ for Jumper<br>No visible damage |
| Thermal Shock | AEC-Q200 Test 16<br>MIL-STD-202 Method 107 | -55/+125 °C<br>Number of cycles is 300. Devices mounted<br>Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air | ±(0.5%+0.05Ω) for D/F tol<br>±(1.0%+0.05Ω) for J tol<br><50 mΩ for Jumper |
| ESD | AEC-Q200 Test 17<br>AEC-Q200-002 | Human Body Model,<br>1 $_{pos.}$ + 1 $_{neg.}$ discharges<br>0612 and above: 2KV | ±(3.0%+0.05 Ω)<br><50 mΩ for Jumper |

| TEST | TEST METHOD | PROCEDURE | REQUIREMENTS |
|---|---|---|---|
| Solderability<br>- Wetting | AEC-Q200 Test 18<br>J-STD-002 | Electrical Test not required Magnification 50X<br>SMD conditions:<br>(a) Method B, aging 4 hours at 155 °C dry heat, dipping at 235±3 °C for 5±0.5 seconds.<br>(b) Method B, steam aging 8 hours, dipping at 215±3 °C for 5±0.5 seconds.<br>(c) Method D, steam aging 8 hours, dipping at 260±3 °C for 7±0.5 seconds. | Well tinned (≥95% covered)<br>No visible damage |
| Board Flex | AEC-Q200 Test 21<br>AEC-Q200-005 | Chips mounted on a 90mm glass epoxy resin PCB (FR4)<br>Bending for 0612 and above: 2 mm<br>Holding time: minimum 60 seconds | ±(1.0%+0.05Ω)<br><50 mΩ for Jumper |
| Temperature Coefficient of Resistance (T.C.R.) | MIL-STD-202 Method 304 | At +25/–55 °C and +25/+125 °C<br>**Formula:**<br>$T.C.R=\frac{R_2-R_1}{R_1(t_2-t_1)}\times 10^6$ (ppm/°C)<br>Where<br>$t_1$=+25 °C or specified room temperature<br>$t_2$=–55 °C or +125 °C test temperature<br>$R_1$=resistance at reference temperature in ohms<br>$R_2$=resistance at test temperature in ohms | Refer to table 2 |
| Short Time Overload | IEC60115-1 4.13 | 2.5 times of rated voltage or maximum overload voltage whichever is less for<br>1225 : 2s<br>0612/2010: 5s<br>at room temperature | ±(1.0%+0.05Ω) for D/F tol<br>±(2.0%+0.05Ω) for J tol<br><50 mΩ for Jumper |
| FOS | ASTM-B-809-95 | Sulfur (saturated vapor) 500 hours, 60±2℃, unpowered | ±( 1.0%+0.05Ω) |

## REVISION HISTORY

| REVISION | DATE | CHANGE NOTIFICATION | DESCRIPTION |
|---|---|---|---|
| Version 1 | Dec. 11, 2015 | - | - Tests and requirements update |
| Version 0 | Aug. 21, 2015 | - | - First issue of this specification |

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