



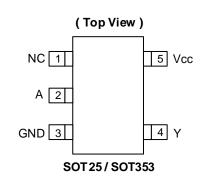
SINGLE SCHMITT-TRIGGER INVERTER

Description

The 74AHCT1G14Q is an automotive compliant Schmitt-trigger inverter gate with a standard push-pull output. The device is designed for operation with a power supply range of 4.5V to 5.5V. The gate performs the positive Boolean function:

$$Y = \overline{A}$$

Pin Assignments



Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 4.5V to 5.5V
- ±8mA Output Drive at 5.0V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time.
- Inputs not Limited by Vcc
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74AHCT1G14Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Applications

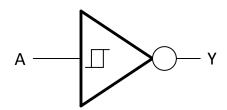
- General Purpose Logic
- Wide Array of Products, such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High Reliability Networking/Communications
 - Industrial/Agricultural Equipment



Pin Descriptions

Pin Name	Description
NC	No Connection
A	Data Input
GND	Ground
Y	Data Output
Vcc	Supply Voltage

Logic Diagram



Function Table

Inputs	Output
А	Y
Н	L
L	Н

Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to Vcc +0.5	V
Ік	Input Clamp Current VI < 0	-20	mA
Іок	Output Clamp Current ($V_0 < 0$ or $V_0 > V_{CC}$)	±20	mA
lo	Continuous Output Current (Vo = 0 to Vcc)	±25	mA
lcc	Continuous Current Through Vcc	75	mA
Ignd	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
Tstg	Storage Temperature	-65 to +150	°C
PtotTOT	Total Power Dissipation (Note 6)	250	mW

Notes:

4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

6. This will need to be derated at higher operating temperatures to prevent exceeding maximum $T_{J_{c}}$ refer to package thermal characteristics section.



Recommended Operating Conditions (Note 7)

Symbol		Parameter	Min	Max	Unit
Vcc	Operating Voltage	—	4.5	5.5	V
VIH	High-Level Input Voltage	$V_{CC} = 5V \pm 0.5V$	2.0	—	V
VIL	Low-Level Input Voltage	$V_{CC} = 5V \pm 0.5V$	_	0.8	V
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	Vcc	V
I _{OH}	High-Level Output Current	$V_{CC} = 5V \pm 0.5V$	_	-8	mA
lol	Low-Level Output Current	$V_{CC} = 5V \pm 0.5V$	_	8	mA
T _A	Ambient Temperature	_	-40	+125	°C

Note: 7. Unused inputs should be held at VCC or Ground.

Electrical Characteristics (All typical values are at $V_{CC} = 5V$, $T_A = +25^{\circ}C$)

			+25°C			-40°C te	o +85⁰C	-40°C to +125°C			
Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit
	Positive-Going Input		4.5V	_	_	2.0	_	2.0	_	2.0	v
V _{T+}	Threshold Voltage	_	5.5V	_	_	2.0	_	2.0	_	2.0	
	Negative-Going		4.5V	0.5	_	_	0.5	_	0.5	_	V
VT-	Threshold Voltage	_	5.5V	0.6	_	_	0.6	_	0.6	_	
A) /	Hysteresis		4.5V	0.4	_	1.4	0.4	1.4	0.35	1.4	v
ΔVτ	(VT+ - VT-)	—	5.5V	0.4	_	1.6	0.6	1.6	0.35	1.6	V
	Low Level	VI = VT+ IOL = 50µA	4.5V	_	_	0.1	_	0.1	_	0.1	
Vol	Output Voltage	$V_I = V_{T+}$ $I_{OL} = 8mA$	4.5V	_	_	0.36	_	0.44	_	0.55	V
	High Level	Vı = Vт- Іон = -50µА	4.5V	4.4	4.5	_	4.4	_	4.4	_	
Vон	Output Voltage	VI = Vт- Іон = -8mA	4.5V	3.94	_	_	3.8	_	3.70	_	V
h	Input Current	$V_1 = 5.5V$ or GND	0V to 5.5V	_	_	± 0.1	_	± 1	_	± 2	μA
Δlcc	Additional Supply Current	$V_1 = 5.5V \text{ or GND}$ lo = 0	5.5V	_	_	2	_	20	_	40	μA
lcc	Supply Current	VI = 3.4V, Io = 0	5.5V	_	_	1.35	—	1.5	_	1.5	mA
Cı	Input Capacitance	VI = Vcc – or GND	5.5V	_	1.5	10	_	10	_	10	pF



Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance SOT25		Nata 0		184	_	00M/
ΑΙθ	Junction-to-Ambient	SOT353	Note 8	_	385	_	°C/W
0	Thermal Resistance	SOT25	Nista 0	-	62	—	0000
θις	Junction-to-Case	SOT353	Note 8		164	_	°C/W

Note: 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

Vcc = 5V \pm 0.5V (See Figure 1, typical values at Vcc = 5V)						

Parameter	From	То	Test			-40°C to +85°C		-40°C to +125°C		Unit	
(Input) (Output)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Мах		
t==	٨	V	$C_L = 15 pF$	1.0	4.1	7.0	1.0	8.0	1.0	9.0	ns
tpD	A	ř	$C_L = 50 pF$	1.0	5.9	8.5	1.0	10.0	1.0	11.0	ns

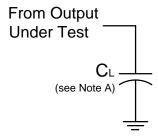
Operating Characteristics

 $T_A = +25^{\circ}C$

Parameter		Test Conditions	Тур	Unit
Cpd	Power Dissipation Capacitance	$V_{CC} = 5.0V, f = 1MHz$ $C_L = 50pF$ $V_I = GND to V_{CC}$	12	pF



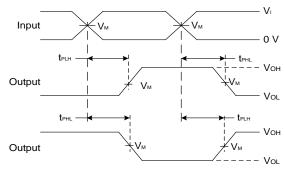
Measurement Information



Vcc		Inputs		Output	CL
•00	Vi	tr/tr	VM	Vм	UL
5V±0.5V	GND to 3.0V	≤3ns	1.5V	V _{CC} /2	15pF
5V±0.5V	GND to 3.0V	≤3ns	1.5V	V _{CC} /2	50pF



Voltage Waveform Pulse Duration



Voltage Waveform Propagation Delay Times Inverting and Non-Inverting Outputs

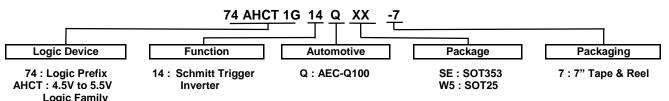
Figure 1. Load Circuit and Voltage Waveforms

Notes:

A. Includes test lead and test apparatus capacitance.
B. All pulses are supplied at pulse repetition rate ≤ 1MHz.
C. Inputs are measured separately one transition per measurement.



Ordering Information (Notes 9 to 11)



Logic Family 1G : One Gate

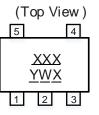
Part Number	Package Code	Package (Notes 9 & 10)	Package Size	Packaging
74AHCT1G14QSE-7	SE	SOT353	2.15mm × 2.1mm × 1.1mm 0.65mm lead pitch	3,000/7" Tape & Reel
74AHCT1G14QW5-7	W5	SOT25	3.0mm × 2.8mm × 1.2mm 0.95mm lead pitch	3,000/7" Tape & Reel

9. For packaging details, go to our website at http://www.diodes.com/products/packages.html. Notes:

10. Pad layout as shown in Diodes Incorporated suggested pad layouts, which can be found on our website at http://www.diodes.com/package-outlines.html. 11. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Marking Information

SOT25, SOT353



SOT 25 / SOT 353

XXX: Identification Code

 $\underline{\underline{Y}}$: Year 0 to 9 $\underline{\underline{W}}$: Week: A~Z: 1 to 26 Week;

- a~z: 27 to 52 Week;
- z Represents 52 to 53 Week

X : A~Z: Internal Code

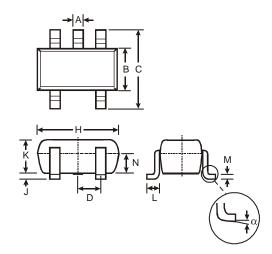
Part Number	Package	Identification Code
74AHCT1G14QW5-7	SOT25	ZVQ
74AHCT1G14QSE-7	SOT353	ZVQ



Package Outline Dimensions

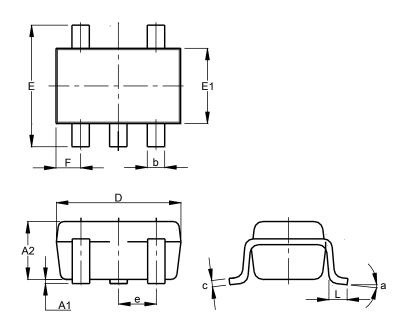
Please see https://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



	SOT25					
Dim	Min	Max	Тур			
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
С	2.70	3.00	2.80			
D	-	-	0.95			
н	2.90	3.10	3.00			
J	0.013	0.10	0.05			
κ	1.00	1.30	1.10			
L	0.35	0.55	0.40			
М	0.10	0.20	0.15			
Ν	0.70	0.80	0.75			
α	0°	8°	-			
All Dimensions in mm						

(2) Package Type: SOT353



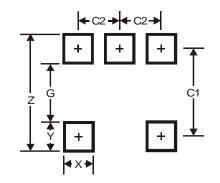
SOT353				
Dim	Min	Max	Тур	
A1	0.00	0.10	0.05	
A2	0.90	1.00	0.95	
b	0.10	0.30	0.25	
С	0.10	0.22	0.11	
D	1.80	2.20	2.15	
Е	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	0.650 BSC			
F	0.40	0.45	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All Dimensions in mm				



Suggested Pad Layout

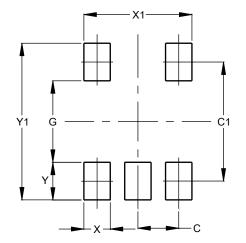
Please see https://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value	
Z	3.20	
G	1.60	
Х	0.55	
Y	0.80	
C1	2.40	
C2	0.95	

(2) Package Type: SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Y	0.600
Y1	2.500

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 🕄
- Weight: 15.8mg (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 6.4mg (Approximate)



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