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GRF2140

Low Current LNA w/Bypass Tuning Range: 0.1 to 3.8 GHz



Features

Reference: 3.3V/18mA/1.95 GHz

EVB NF: 1.1 dB

Gain: 17.8 dB

Bypass Gain: -2.5 dB

• OP1dB (Gain Mode): 9.3 dBm

OP1dB (Bypass Mode): 21.8 dBm

Flexible voltage: 2.7 to 5.0 volts

Optimal for low Iddq to 8 mA

Simple matching to 50 ohms

Process: GaAs pHEMT

Applications

WiFi Access Points

Mobile WiFi Devices

Cellular Boosters

Drones

UHF/900/2400 ISM

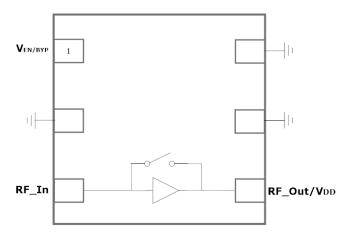
Revision Date: 10/24/19

Product Description

GRF2140 is a low noise amplifier (LNA) with bypass designed for high-performance WLAN/ISM and other applications up to 3.8 GHz.

The LNA is operated from a single positive supply of 2.7 to 5.0 V with typical bias condition of 3.3 volts and 18 mA.

Consult with the GRF applications engineering team for custom tuning/evaluation board data and device sparameters.



1.5 x 1.5 mm DFN-6



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Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V _{DD}	0	6.0	V
RF Input Power: (Load VSWR < 2:1; V _D : 5.0 volts)	P _{IN MAX}		15	dBm
Operating Temperature (Package Heat Sink)	Т _{АМВ}	-40	105	°C
Maximum Channel Temperature (MTTF > 10^6 Hours)	Тмах		170	°C
Maximum Dissipated Power	P _{DISS MAX}		200	mW
Electrostatic Discharge:				
Charged Device Model:	CDM	1500		V
Human Body Model:	НВМ	250		V
Storage:				
Storage Temperature	T _{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	



Caution! ESD Sensitive Device



Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

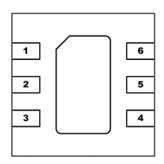
Note: For manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF2140 landing page: Manufacturing Note-MN-001 Product Tape and Reel, Solderability and Package Outline Specification.

Link to manufacturing note



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Pin Out (Top View)



Pin Assignments:

Pin	Name	Description	Note
1	VENABLE/BYPASS	LNA enable/Bypass Con- trol	Venable and series resistor set IDDQ. Venable < 0.3 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.
2	NC	No Connect or Ground	No internal connections to die
3	RF_In	LNA RF input	Requires external DC block.
4	RF_Out/VDD	LNA RF output	Requires external bias inductor followed by DC block.
5	NC	No Connect or Ground	No internal connections to die
6	NC	No Connect or Ground	No internal connections to die
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

VENABLE Truth Table:

Mode	Description	Venable/bypass
High Gain	High LNA Gain	1
Bypass	High Linearity Bypass	0
Logic Level "0"	Logic Low	0.0V to 0.3V
Logic Level "1"	Logic High	1.5V to Vdd



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Nominal Operating Parameters:

Parameter	Symbol	Specification			Unit	Condition	
Farameter	Symbol	Min.	lin. Typ.		Ullit	Condition	
High Gain Mode						V _{DD} = 3.3 V; V _{ENABLE} : High	
Test Frequency	F _{TEST}		1.95		GHz		
Gain	S21	16.3	17.8	19.3	dB		
Noise Figure (Evaluation Board)	NF		1.1	1.3	dB		
Output 1dB Compression Point	OP1dB	7.3	9.3		dBm		
Output Third Order Intercept Point	OIP3		20.0		dBm		
Switching Time (Bypass to Gain)	T_Gain		200		ns		
Switching Time (Gain to Bypass)	T_Bypass		100		ns		
Supply Current	I _{DD}	13	18	25	mA	Vdd=Venable=3.3 volts; Rbias: 500 Ω	
Enable Current	I _{ENABLE}		1.0	3.0	mA		
Bypass Mode						V _{DD} : 3.3 V; Ven: 0.0 V	
Gain	S(2,1)	-3.5	-2.5		dB		
Output 1dB Compression Point	OP1dB		21.8		dBm		
Output Intercept Point	OIP3		39.0		dBm		
Thermal Data							
Thermal Resistance (Infra-Red Scan)	Θјс		235		°C/W		
Channel Temperature @ +85 C reference (Package heat sink)	TCHANNEL		99 (See note)		°C	V _{DD} : 3.3 V; I _{DDQ} : 18 mA; No RF; Dissi- pated Power: 59 mW	

Note: MTTF >10^6 hours for TCHANNEL < =170 degrees C.

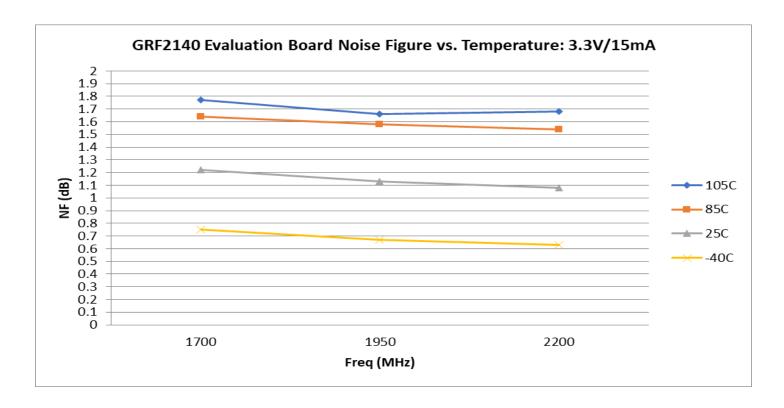


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GRF2140 Evaluation Board Measured Data:



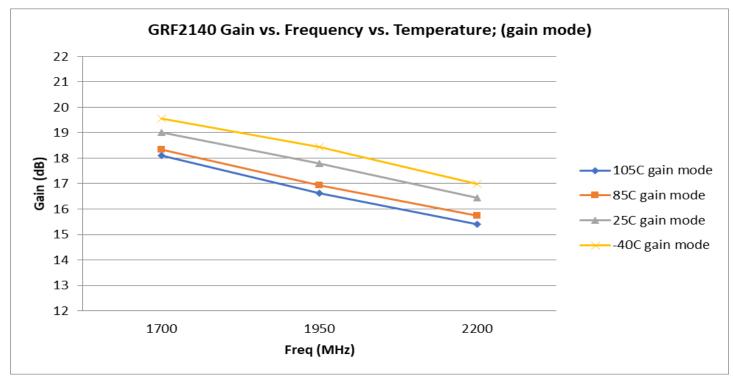


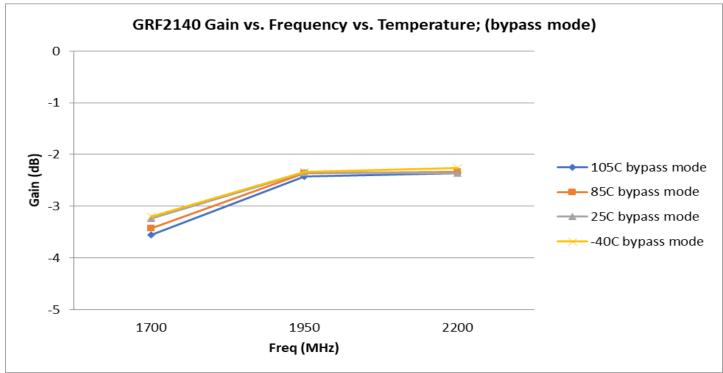
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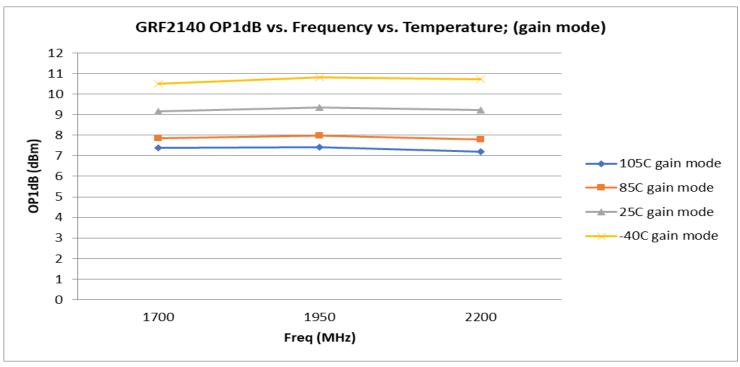


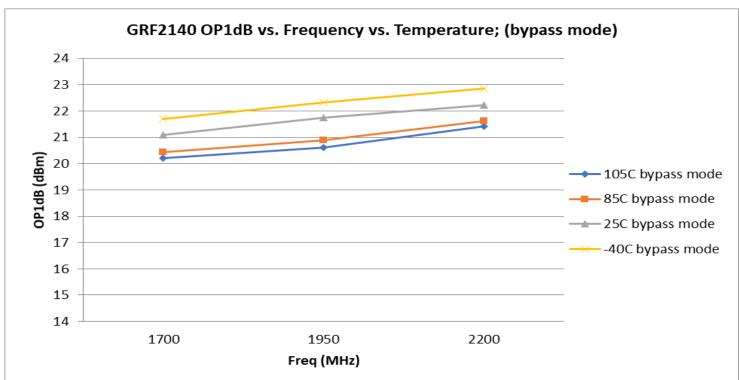
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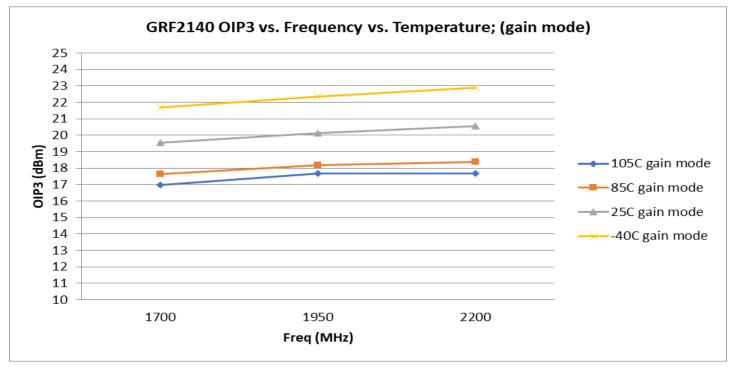


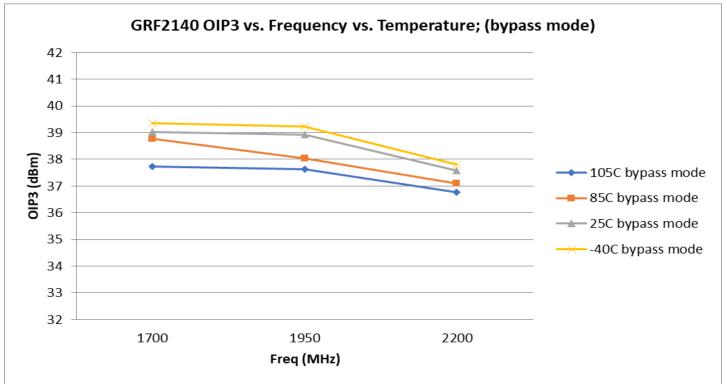
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GRF2140 Evaluation Board Measured Data in Gain and Bypass Modes:





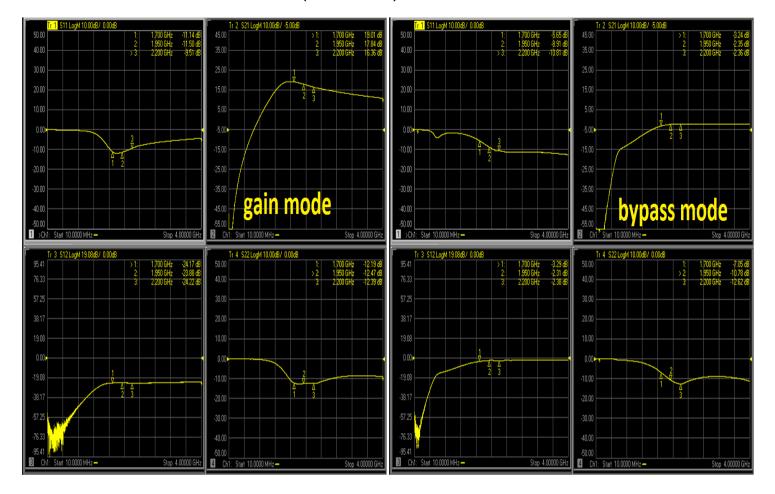


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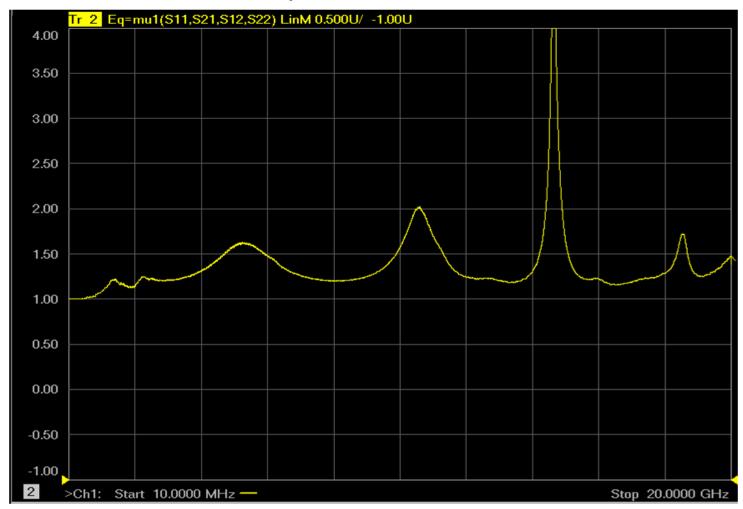
GRF2140 Evaluation Board S-Pars (Gain Mode) with 1.7 to 2.2 GHz Tune:





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GRF2140 Evaluation Board Stability Mu Factor:



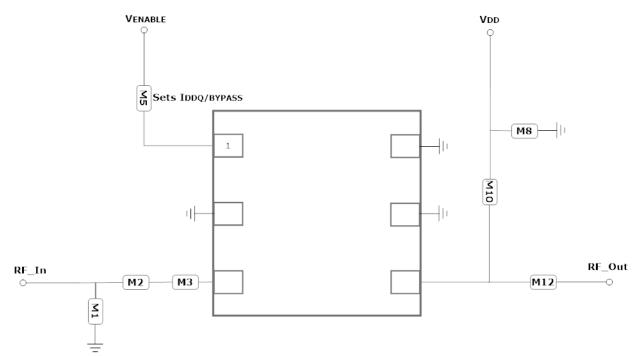
Note: Mu factor >= 1.0 implies unconditional stability.



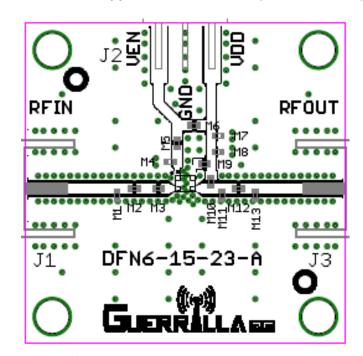
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GRF2140 Application Schematic (1.7 to 2.2 GHz)



GRF2140 Evaluation Board Assembly Diagram



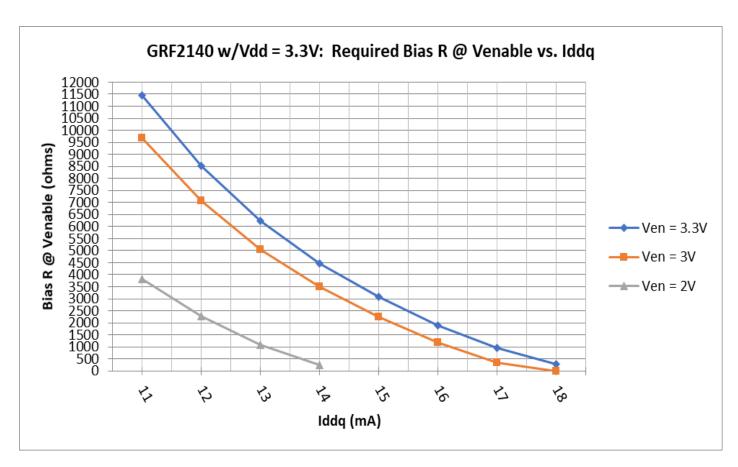
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GRF2140 Standard Evaluation Board BOM: (1.7 to 2.2 GHz Tune)

Component	Туре	Manufacturer	Family	Value	Package Size	Substitution
M1	Inductor	Murata	LQP	3.9 nH	0402	Yes
M2	Capacitor	Murata	GJM	30 pF	0402	Yes
M3	Inductor	Murata	LQP	1.5 nH	0402	Yes
M5 (see curves)	Resistor	Various	_	TBD	0402	Yes
M8	Capacitor	Murata	GRM	0.1 uF	0402	Yes
M10	Inductor	Murata	LQP	3.3 nH	0402	Yes
M12	Capacitor	Murata	GJM	2.2 pF	0402	Yes
Evaluation Board:	DFN6-15-23-A					

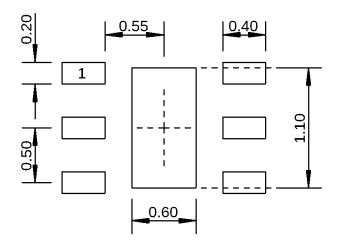




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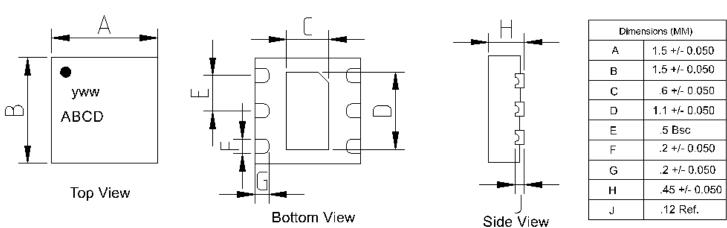
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Dimensions in millimeters

1.5 mm DFN-6 Suggested PCB Footprint (Top View)



Dimensions (MM)				
A	1.5 +/- 0.050			
В	1.5 +/- 0.050			
Ç	.6 +/- 0.050			
D	1.1 +/- 0.050			
E	.5 Bsc			
F	.2 +/- 0.050			
G	.2 +/- 0.050			
Н	.45 +/- 0.050			
J	.12 Ref.			

1.5 mm DFN-6 Package Dimensions



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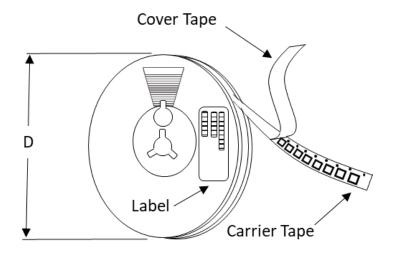
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Tape and Reel Information:

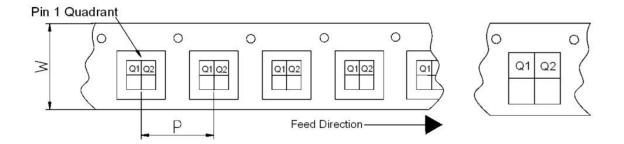
Revision Date: 10/24/19

Guerrilla RF's Tape and Reel specification complies with the Electronics Industries Association (EIA) standards for 'Embossed Carrier Tape of Surface Mount Components for Automatic Handling". Reference EIA-481. See the table on the following page for Tape and Reel specifications along with units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape, wound into a plastic reel. Each reel will be packaged in a cardboard box. There will be product labels on the reel, the protective ESD bag and the outside surface of the box.



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information



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Tape and Reel Specification and Device Package Information Table

Package			Carrier Tape			Reel		
Туре	Dimensions (mm)	Leads	Weight (mg)	Width (W) (mm)	Pocket Pitch (P) (mm)	Pin 1 Quad- rant	Diameter (D) (inches)	Units per Reel
QFN	2.0 x 2.0 x 0.50	12	7	8	4	Q1	7	2500
QFN	3.0 x 3.0 x 0.85	16	24	12	8	Q1	7	1500
DFN	1.5 x 1.5 x 0.45	6	4	8	4	Q1	7	2500
DFN	2.0 x 2.0 x 0.75	8	12	8	4	Q1	7	2500
LFM	3.5 x 3.5 x 0.75	See note	TBD	12	8	Q2	7	1500
LFM	4.0 x 4.0 x 0.75	See note	TBD	12	8	Q2	7	1500

Note: Lead count may vary. Reference applicable product data sheet



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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

Revision Date: 10/24/19

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