

- C.
- TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX
 TO THE PART NUMBER(i.e HX1224CNL BECOMES HX1224CNLT).



FINAL OUTLINE

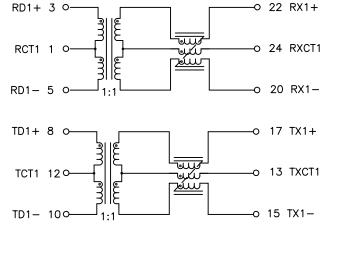
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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100D,1:1,4KV,SM,TU	HX1224CNL-10	PS-2611.001-D	1 OF 3	HX1224CNL	В

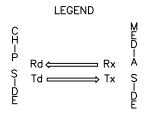
E-MAIL: PRODINFONETWORK@PULSEELECTRONICS.COM(US), ASIA@PULSEELECTRONICS.COM(Asia) PHONE: USA:858 674 8100, GERMANY: 49 7032 78060, SINGAPORE: 65 6287 8998, SHANGHAI: 86 21 62787060, TAIWAN: 886 3 4356768







SCHEMATIC



ALL CHANNELS ARE IN-PHASE BETWEEN INPUT AND OUTPUT

ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED

MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS						
OPERATION TEMP	-40°C TO +85°C						
TURNS RATIO	1 : 1 ±2%						
POLARITY	PER SCHEMATIC						
OCL, TRANSMIT CHANNEL -40 TO +85 °C	350 uH MIN. AT 100KHz, 0.1VRMS WITH 8mA DC BIAS						
INSERTION LOSS	100 KHz 1-80 MHz 100 MHz 125 MHz						
INSERTION LOSS	-1.2 dB MAX -1.0 dB MAX -1.4 dB MAX -3.0 dB MAX						
RETURN LOSS	.5-15 MHz 30-45 MHz45-60 MHz 60-80 MHz						
$(Z \text{ OUT} = 100 \text{ OHM } \pm 15\%)$	-18 dB MIN -14 dB MIN -10 dB MIN -6 dB MIN						
CROSSTALK (TX-RX)	1 MHz 30-100 MHz						
	-50 dB MIN -55+24*LOG ₁₀ (f/10 MHz) dB MIN						
DIFFERENTIAL TO	2 MHz 30-100 MHz						
COMMON MODE REJECTION	-50 dB MIN -43+22*LOG ₁₀ (f/30 MHz) dB MIN						
DCR	PIN 1 AND PINS 3, $5 = 0.6$ OHM MAX PIN 12 AND PINS 8, $10 = 0.6$ OHM MAX PIN 24 AND PINS 22, $20 = 1.2$ OHMS MAX PIN 13 AND PINS 15, $17 = 1.2$ OHMS MAX						
LEAKAGE INDUCTANCE @100KHz, 20mVRMS	PINS $(3-5)$ WITH PINS $(22-20)$ SHORTED = 0.35 uH MAX PINS $(8-10)$ WITH PINS $(17-15)$ SHORTED = 0.35 uH MAX						
INPUT – OUTPUT ISOLATION							

NOTE: f IS FREQUENCY IN MHz.

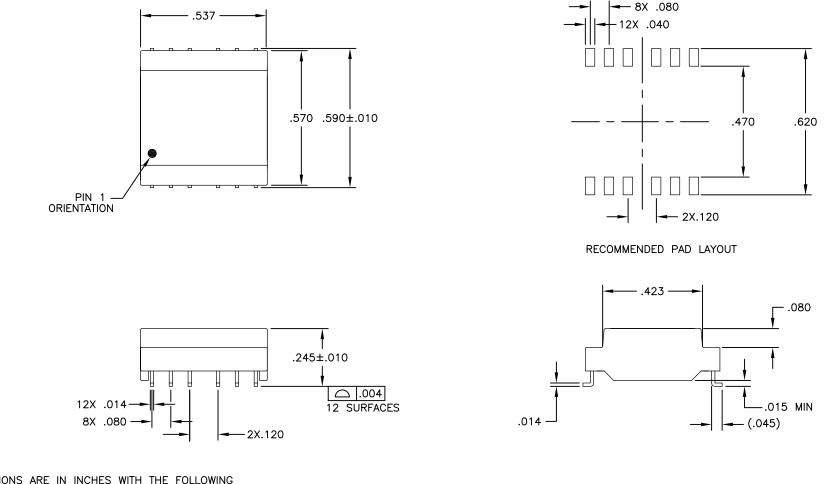
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MDL,SIN,100D,1:1,4KV,SM,TU	HX1224CNL-10	PS-2611.001-D	2 OF 3	HX1224CNL	В

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DIMENSIONS ARE IN INCHES WITH THE FOLLOWING TOLERANCES: .XX= \pm .01 .XXX= \pm .005

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MDL,SIN,100D,1:1,4KV,SM,TU	HX1224CNL-10	PS-2611.001-D	3 OF 3	HX1224CNL	В

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Mouser Electronics

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Pulse: HX1224CNL HX1224CNLT