AX7 Series

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7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant** MSL Level = 1





Features

- 0.125ps typ jitter (150fs MAX $f > 200MHz, 25^{\circ}C$)
- Highest in-class frequency range from 50 to 2100MHz
- Excellent spurious suppresion
- 70mA MAX IDD (LVDS, any VDD)
- Lowest in-class power consumption
- Supports LVPECL, HCSL, LVDS, CML
- Supports ± 50 ppm or ± 100 ppm all inclusive stability
- -40°C to 85°C or -20°C to 70°C operation
- Industry standard 5x7mm footprint

Applications

- Networking and communications
- RF systems, base stations (BTS)
- Test and measurement
- Cloud, server and storage, Fibre Channel
- 100/400GbEthernet
- **PCI Express**

Key Electrical Specifications

Paramet	ters	Min.	Тур.	Max.	Units	Notes	
	LVPECL	50		2100		Option "P"	
E D	LVDS	50		2100		Option "D"	
Frequency Range	HCSL	50		700	MHz	Option "H"	
	CML	50		2100		Option "M"	
		2.97	3.3	3.63		Option "A"	
Power Supply Voltage (Vdd) [No	2.25	2.5	2.75	V	Option "B"		
		1.71	1.8	1.89		Option "C"	
	LVPECL		87	94			
Current Consumption (Idd)	LVDS		64	70	mA	@ Vdd=3.3V	
Consumption (Idd)	HCSL		75	80	ША		
	CML		63	68		@ Vdd=1.8V	
Set Tolerance (as received) @ 2	5°C ±3°C	-5.00	<±3.00	+5.00	ppm	Relative to carrier	
Operating Temperature Range (OTR)	-40		+85	°C	See Options	
Storage Temperature		-55		+150	°C		
Frequency Stability over OTR		-25		+25		Options "D" or "F"	
		-50		+50	ppm	Options "E" or "G"	
Aging over 10-Year Product Life [Note 2]		-15		+15	ppm		
All Inclusive Frequency Accuracy over [Note 2]		-50		+50	ppm	Specific to freq. stability options "D" or "F" (±25ppm)	
10-Year Product Life	-100		+100	11	Specific to freq. stability options "E"or "G"(±50ppm)		



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7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant**

ESD Sensitive

(Py)

MSL Level = 1

Parameters			Min.	Typ.	Max.	Units	Notes	
Rise (Tr) / Fall (Tf) Time	LVPECL/LVI	OS/CML			0.35	nS	20% ↔ 80% waveform	
	HCSL	HCSL			0.40			
Duty Cycle			45		55	%	@ 50% Vdd	
Start-up Time [Note 2]				< 5.0	10	ms		
	LVPECL	V_{OH}	V _{dd} -1.165		V_{dd} -0.8		50Ω into Vdd–2.0V or	
		V_{OL}	V _{dd} -2.0		V _{dd} -1.55		Thevenin equivalent	
	LVDS	V_{OH}		1.4	1.6		100Ω between	
Output High Voltage (V _{OH}) Output Low Voltage (V _{OL})		V_{OL}	0.9	1.1		V	OUT and OUTN	
Output Low Voltage (VOL)	HCSL	V _{OH}	0.66		1.15	v	500 · 1/11	
		V_{OL}	0.0		0.15		50Ω to Vdd	
	CML	V _{OH}	V _{dd} -0.085		V _{dd} =Max		50Ω into GND	
		V_{OL}	V _{dd} -0.6		V_{dd} -0.32		3022 Into GND	
Output Enable (OE) Control			0.8*(V _{dd})			* 7		
					0.2*(V _{dd})	V		
Output Enable Time					2.5	ms		
Output Disable Time					10	μs		
	LVPEC	L		85	86			
Output Disable Current Consumption	LVDS	LVDS		63	65	mA	@ Vdd=3.3V	
consumption	HCSL			77	78	1111 1	(a) vad 3.3 v	
	CML			62	67		@ Vdd=1.8V	
RMS Phase Jitter (12kHz -2	0MHz BW)			•			•	
201.0000MHz – 2100.0000M	Hz			125	150	0	O 1/11 0 01/	
50.0000MHz - 200.0000MHz				200	300	fsec	@ Vdd=3.3V	
156.2500MHz				< 130	200			

Note 1: Supply Voltage (Vdd) = 1.8V option not available with LVPECL output

Note 2: Relative to initial measured frequency @ 25°C ±3°C



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7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant** MSL Level = 1

Typical Phase Noise and Jitter Characteristics (@ $25^{\circ}C \pm 3^{\circ}C$)

Frequency (M	IHz)	148.35	150	155.52	156.25	156.25	200	212.5	312.5
RF Output		LVDS	LVPECL	LVPECL	LVPECL	HCSL	LVPECL	LVDS	LVDS
RMS Phase Jitto 12kHz-20MHz I	` ,	125	137	124	123	129	122	127	114
	100Hz	-96	-98	-98	-98	-99	-90	-83	-94
	1kHz	-120	-120	-120	-121	-121	-114	-114	-115
	10kHz	-132	-132	-132	-132	-132	-129	-129	-126
Phase Noise (dBc/Hz)	100kHz	-140	-139	-141	-141	-140	-138	-137	-134
	1MHz	-149	-150	-151	-150	-151	-148	-147	-144
	10MHz	-157	-159	-159	-159	-160	-159	-157	-156
	20MHz	-157	-159	-159	-159	-160	-159	-157	-157

Frequency (M	IHz)	322.265	6625	491.52	644.53125	1000	1244.16	1500	2100
RF Output		LVPECL	HCSL	LVPECL	LVPECL	LVDS	LVDS	LVDS	LVPECL
RMS Phase Jitte 12kHz-20MHz I	` /	121	121	123	127	114	127	112	138
	100Hz	-91	-92	-91	-77	-76	-78	-79	-77
	1kHz	-113	-114	-111	-107	-102	-102	-101	-98
	10kHz	-125	-125	-122	-119	-115	-113	-112	-108
Phase Noise (dBc/Hz)	100kHz	-133	-133	-131	-127	-124	-122	-120	-117
(ubc/11z)	1MHz	-144	-144	-138	-138	-134	-131	-130	-124
	10MHz	-157	-157	-154	-154	-150	-149	-145	-145
	20MHz	-159	-159	-154	-155	-152	-150	-145	-148

Note 3: Refer to Section 1.2 for selected Phase Noise Plots



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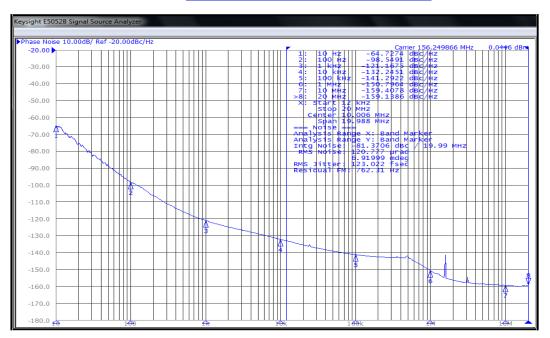


7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant**

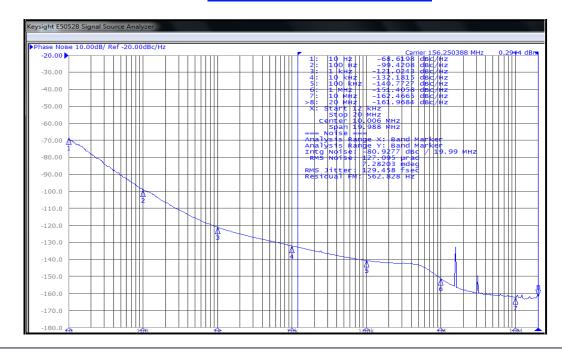
ESD Sensitive (Pb) MSL Level = 1

Typical Phase Noise and Jitter Characteristics (@ $25^{\circ}C \pm 3^{\circ}C$) continued

156.25MHz - LVPECL - Vdd=3.3V



156.25MHz - HCSL - Vdd=1.8V





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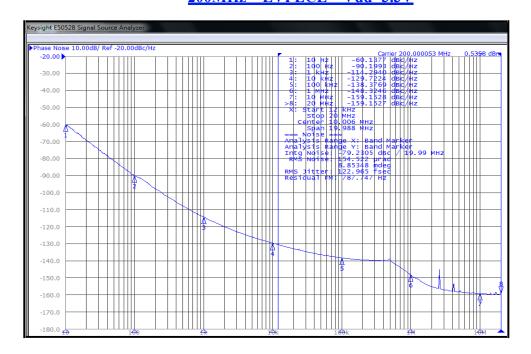
Check Inventory

ESD Sensitive Pb

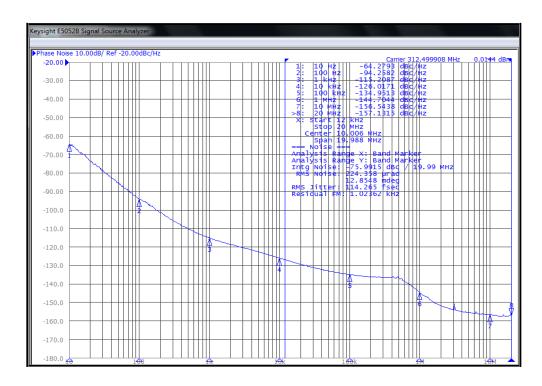


7.0 x 5.0 x 1.8 mm RoHS/RoHS II Compliant MSL Level = 1

200MHz - LVPECL - Vdd=3.3V



312.5MHz - LVDS - Vdd=3.3V





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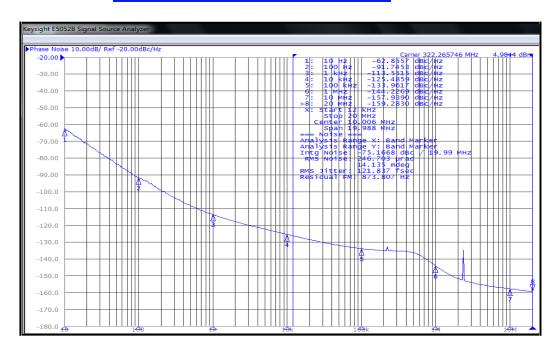


Check Inventory

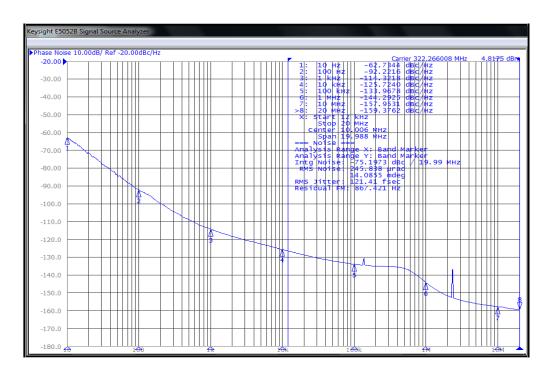


7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant** MSL Level = 1





322.265625MHz – HCSL – Vdd=3.3V





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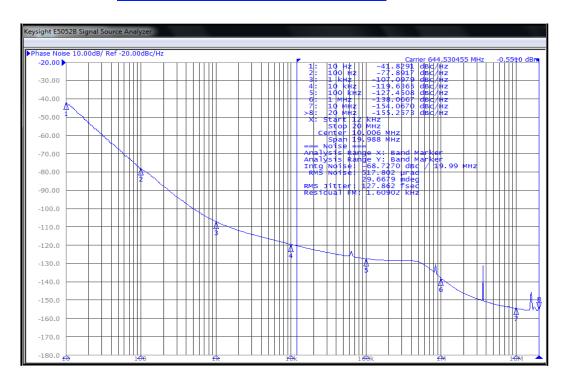
Check Inventory

ESD Sensitive (Pb)

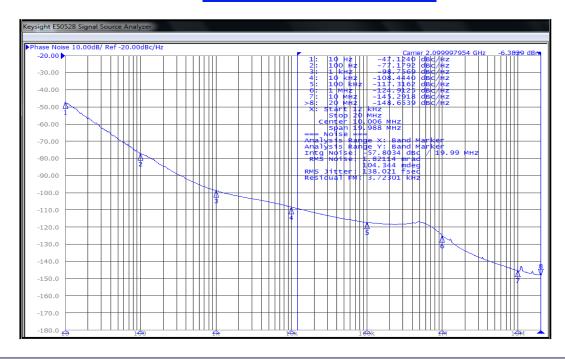


7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant** MSL Level = 1

644.53125MHz - LVPECL - Vdd=3.3V



2100MHz - LVPECL - Vdd=3.3V





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ClearClockTM Oscillator Family | Low Jitter | 7.0 x 5.0mm XO

AX7 Series

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ESD Sensitive (Pb)

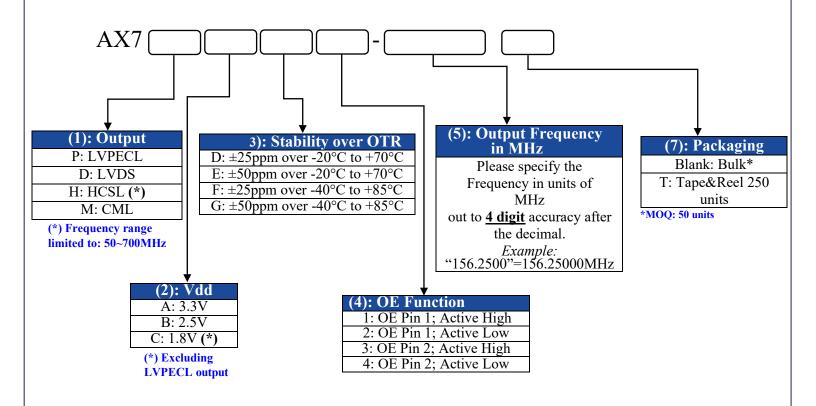


7.0 x 5.0 x 1.8 mm RoHS/RoHS II Compliant

MSL Level = 1

Options and Part Identification [Note 4]

Note 4: Contact Abracon for part number requests with carrier frequency callouts up to 5 & 6 digit accuracy after the decimal.



Part Number Example:

AX7PAF1-644.53125



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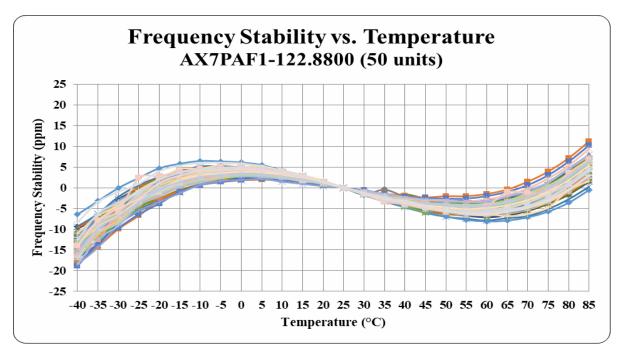
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ESD Sensitive (Pb)

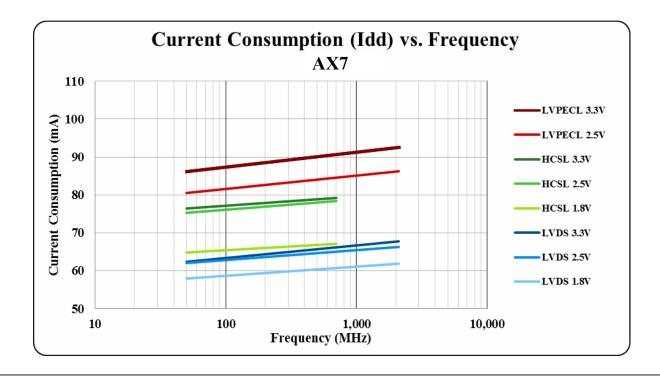


7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant** MSL Level = 1

Typical Frequency vs. Temperature Characteristics



Typical Current Consumption (Idd) vs. Frequency Characteristics (@ 25°C ± 3°C)





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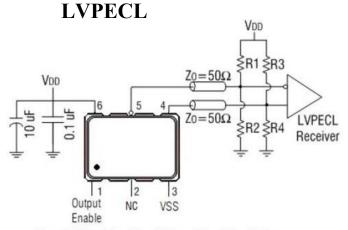
Check Inventory

ESD Sensitive (Pb)



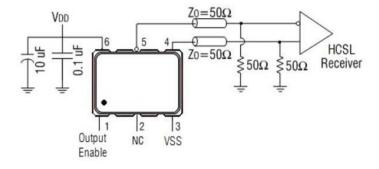
7.0 x 5.0 x 1.8 mm RoHS/RoHS II Compliant MSL Level = 1

Recommended Test Circuit

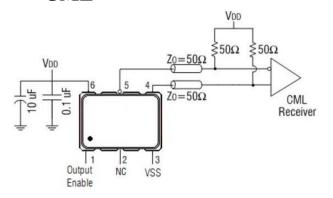


 V_{DD} =3.3V: R1=R3=127Ω; R2=R4=82.5Ω V_{DD} =2.5V: R1=R3=250Ω; R2=R4=62.5Ω

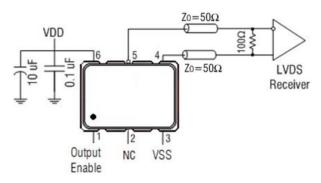
HCSL



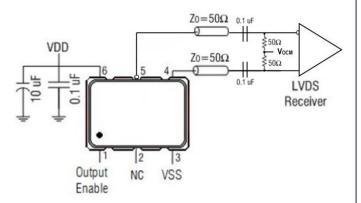
CML



LVDS @ Vdd = 3.3V or 2.5V



LVDS @ Vdd = 1.8V





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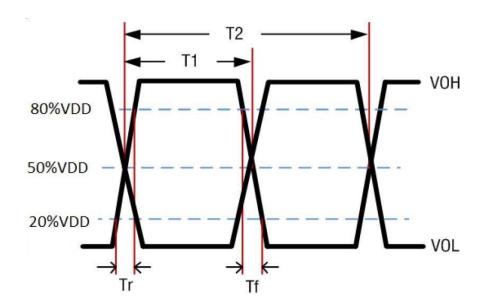


Check Inventory (>)



7.0 x 5.0 x 1.8 mm RoHS/RoHS II Compliant MSL Level = 1

Differential Output Waveform





AX7 Series

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ESD Sensitive (Pb)



7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant**

MSL Level = 1

Reflow Profile [JEDEC J-STD-020]

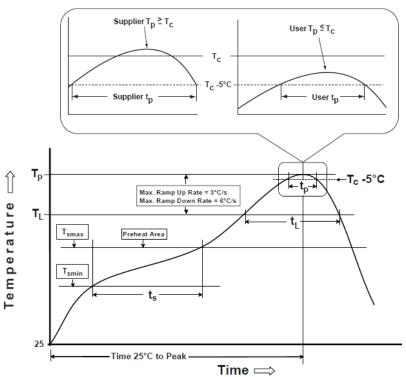


Table 1

Classification Temperatures (T _c)							
Package Thickness	Volume mm³ <350	Volume mm³ ≥350					
<2.5 mm	235 °C	220 °C					
>2 5 mm	220 °C	220 °C					

Table 2

Pb-Free Process Classification Temperatures (T _c)					
Package Thickness	Volume mm³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000		
<1.6 mm	260 °C	260 °C	260 °C		
1.6 mm - 2.5 mm	260 °C	250 °C	245 °C		
>2.5 mm	250 °C	245 °C	245 °C		

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T _{smin})	100°C	150°C
Temperature maximum (T _{smax})	150°C	200°C
Time (T _{smin} to T _{smax}) (t _s)	60 - 120 sec.	60 - 120 sec.
Average ramp-up rate (T _{smax} to T _P)	3°C/sec. max	3°C/sec. max
Liquidous temperature (T _L)	183°C	217°C
Time at liquidous (t _L)	60 - 150 sec.	60 - 150 sec.
Peak package body temperature (T _P)*	see Table 1	see Table 2
Time $(t_p)^{**}$ within 5°C of the specified classification temperature (T_c)	20 sec.	30 sec.
Ramp-down rate (T _p to T _{smax})	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

^{*}Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.



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^{**}Tolerance for time at peak profile temperature (tp) is defined as supplier minimum and a user maximum.

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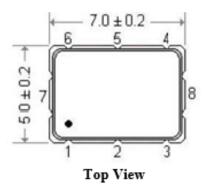
Check Inventory

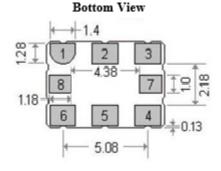
ESD Sensitive (Pb)

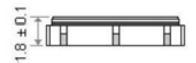


7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant** MSL Level = 1

Mechanical Dimensions

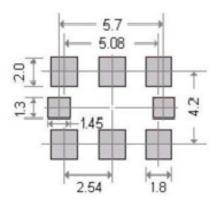








Recommended Land Pattern



Pin#	Function
# 1	Option 1 & 2: Output Enable/Disable Option 3 & 4:
	No Connect
# 2	Option 1 & 2: No Connect Option 3 & 4: Output Enable/Disable
# 3	GND
# 4	Output
# 5	Complementary output
# 6	Supply Voltage (Vdd)
# 7	No connect
# 8	No connect

Dimensions: mm



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ESD Sensitive Pb



7.0 x 5.0 x 1.8 mm **RoHS/RoHS II Compliant** MSL Level = 1

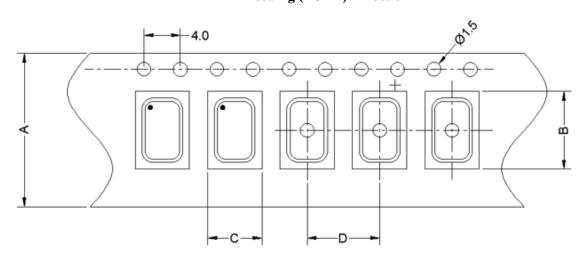
Packaging

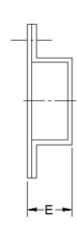
Blank = Bulk*

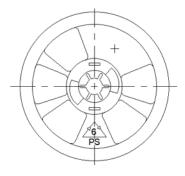
T = Tape & Reel 250 unit/reel

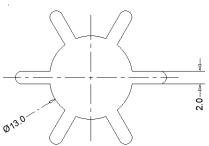
*MOQ=50 units

Feeding (PULL) Direction \rightarrow

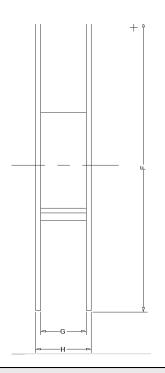








Tap	Tape Dimensions				
A	16.0				
В	7.2				
C	5.4				
D	8.0				
E	1.8				
Re	el Dimensions				
F	180.0				
G	16.5				
Н	19.6				



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