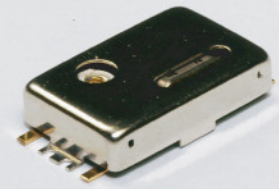


TCXO & VCTCXO

STB, STE

- Temperature Compensated Crystal Oscillator
- HCMOS / TTL output
- Clipped Sinewave Output
- 3.3V / 5.0V supply voltage



STB



STE

SMD TYPE

ELECTRICAL SPECIFICATIONS

	TTL / HCMOS	CLIPPED SINEWAVE
Frequency Range	1.200 to 40.000MHz	9.600 to 40.000MHz
Load Drive Capability	10 TTL Load or 15pF HCMOS Load(STD)	10kOhms//10pF
Output Voltage Logic High(V _{OH})	w/TTL : 2.4V _{DC} min. w/HCMOS : 90% of V _{DD} min.	0.8V _{p-p} min.(V _{DD} : 3.3V _{DC})
Output Voltage Logic Low(V _{OL})	w/TTL : 0.4V _{DC} max. w/HCMOS : 10% of V _{DD} max.	1.0V _{p-p} min.(V _{DD} : 5.0V _{DC})
Frequency / Stability	vs. Operating Temperature Range vs. Input Voltage (±5%) vs. Load (±10%)	See Table 2 ±0.3ppm max. ±0.3ppm max.
Supply Voltage(V _{DD})	3.3V _{DC} ±5%, 5.0V _{DC} ±5%	3.3V _{DC} ±5%, 5.0V _{DC} ±5%
Input Current	1.200 to 27.000MHz 20mA max. 25mA max. 27.001 to 40.000MHz 35mA max. 40mA max.	9.600 to 27.000MHz 2mA max. 3mA max. 27.001 to 40.000MHz 3mA max. 4mA max.
Rise / Fall Time	4ns max. 5ns max.	
Duty Cycle	50 ± 10%	
Internal Trim (Top of Can)		± 3ppm min.
Control Voltage(External)	1.65V _{DC} ± 1.65V _{DC} (V _{DD} : 3.3V _{DC}), 2.5V _{DC} ± 2.0V _{DC} (V _{DD} : 5.0V _{DC}) Positive transfer Characteristic	
Frequency Deviation		± 5ppm or ± 10ppm minimum Over Control Voltage
Aging (at 25 °C)		± 1ppm/year max.

MECHANICAL DIMENSIONS

