Unit: mm

TOSHIBA Transistor Silicon PNP·NPN Epitaxial Type (PCT Process) (Bias Resistor Built-in Transistor)

RN4907FE

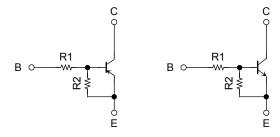
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into an Extreme-Super-Mini (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

Equivalent Circuit and Bias Resistor Values

Q1

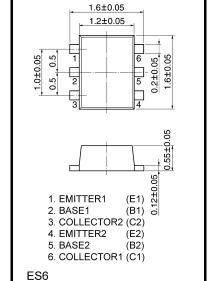
Q2



R1: $10 \text{ k}\Omega$

R2: 47 kΩ

(Q1, Q2 common)

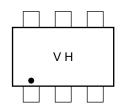


2-2N1G

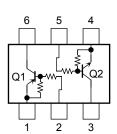
Weight: 0.003g (typ.)

JEDEC JEITA TOSHIBA

Marking



Equivalent Circuit (top view)



RN4907FE

Absolute Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-50	V
Collector-emitter voltage	V _{CEO}	-50	V
Emitter-base voltage	V _{EBO}	-6	V
Collector current	IC	-100	mA

Absolute Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	6	V
Collector current	IC	100	mA

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P _C (Note 1)	100	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

Electrical Characteristics (Ta = 25°C) (Q1)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA
	I _{CEO}	$V_{CE} = -50 \text{ V}, I_B = 0$	_	_	-500	ш
Emitter cut-off current	I _{EBO}	$V_{EB} = -6 \text{ V}, I_{C} = 0$	-0.081	_	-0.15	mA
DC current gain	h _{FE}	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	80	_	_	
Collector-emitter saturation voltage	V _{CE} (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V _{I (ON)}	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-0.7	_	-1.8	V
Input voltage (OFF)	V _{I (OFF)}	$V_{CE} = -5 \text{ V}, I_{C} = -0.1 \text{ mA}$	-0.5	_	-1.0	V
Transition frequency	f _T	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	_	200	_	MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	3	6	pF

Electrical Characteristics (Ta = 25°C) (Q2)

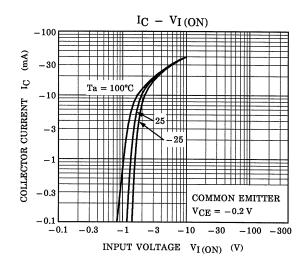
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = 50 \text{ V}, I_{E} = 0$	_	_	100	nA
	I _{CEO}	$V_{CE} = 50 \text{ V}, I_{B} = 0$	_		500	
Emitter cut-off current	I _{EBO}	$V_{EB} = 6 \text{ V}, I_{C} = 0$	0.081	_	0.15	mA
DC current gain	h _{FE}	$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$	80			
Collector-emitter saturation voltage	V _{CE (sat)}	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$		0.1	0.3	>
Input voltage (ON)	V _{I (ON)}	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	0.7		1.8	>
Input voltage (OFF)	V _{I (OFF)}	$V_{CE} = 5 \text{ V}, I_{C} = 0.1 \text{ mA}$	0.5		1.0	>
Transition frequency	f _T	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	_	250		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$	_	3	6	pF

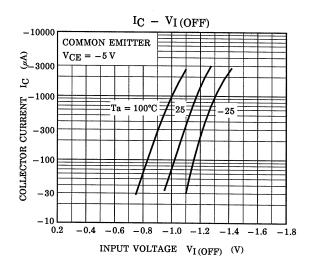
Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

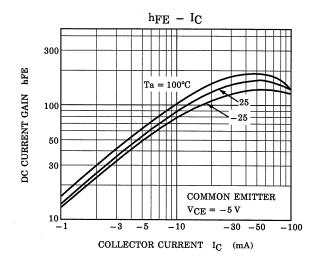
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1	_	7	10	13	kΩ
Resistor ratio	R1/R2		0.191	0.213	0.232	

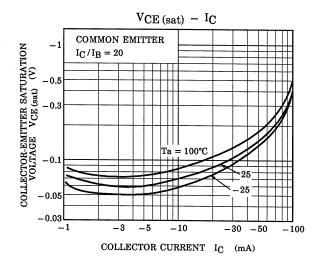
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Q1

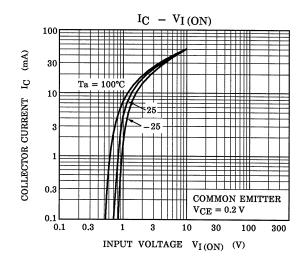


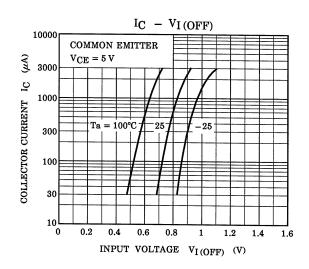


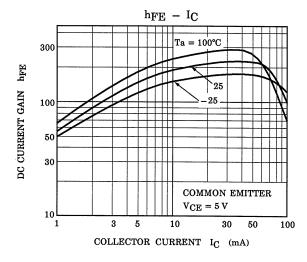


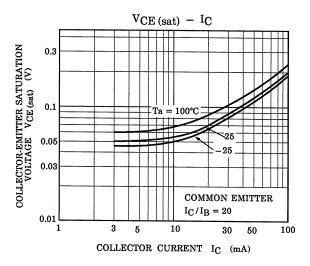


Q2









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