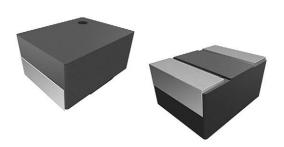


www.vishay.com

Vishay Dale

# **Low Profile, High Current Inductors**



STANDARD ELECTRICAL SPECIFICATIONS						
L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A	DCR 25 °C (mΩ)		HEAT RATING CURRENT DC (A) (3)		SATURATION CURRENT DC (A) (4)	
(μH)	TYP.	MAX.	TYP.	MAX.	TYP.	MAX.
0.22	15	19	5.20	4.68	6.10	5.50
0.24	16	20.5	4.70	4.20	6.00	5.40
0.47	31	39	3.80	3.30	4.40	3.85
1.0	49	65	3.10	2.80	3.50	3.10
1.5	116	140	2.10	1.90	2.50	2.20
2.2	132	158	2.00	1.80	2.10	1.80

#### Notes

- (1) All test data is referenced to 25 °C ambient
- (2) Operating temperature range -55 °C to +125 °C
- $^{(3)}$  DC current (A) that will cause an approximate  $\Delta T$  of 40  $^{\circ}$ C
- $^{(4)}$  DC current (A) that will cause  $L_0$  to drop approximately 30 %
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application

#### **FEATURES**

- Magnetic alloy power choke coil
- Miniature size (2.0 x 1.6) and low profile
- · Magnetic shielded
- · Low acoustic noise and high efficiency
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

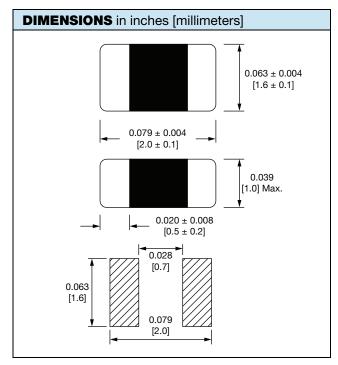
# Pb-free

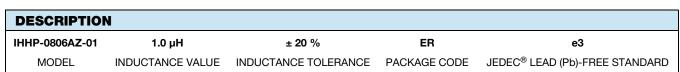
ROHS COMPLIANT HALOGEN

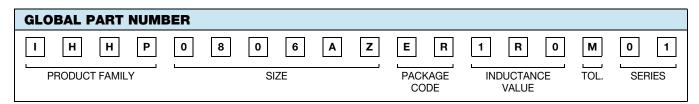
FREE

#### **APPLICATIONS**

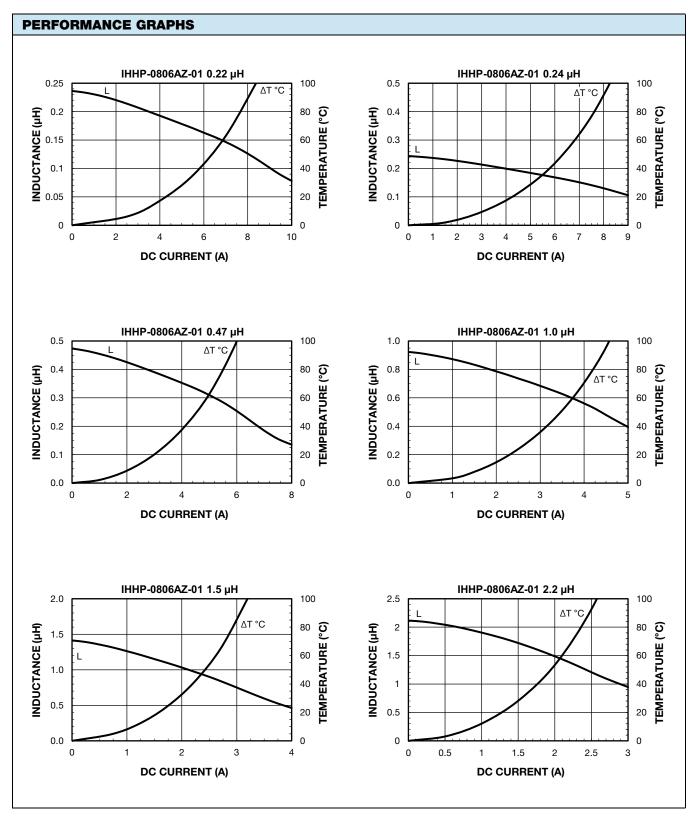
- PDA / notebook / desktop / server applications
- High current POL converters
- · Low profile, high current power supplies
- · Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)



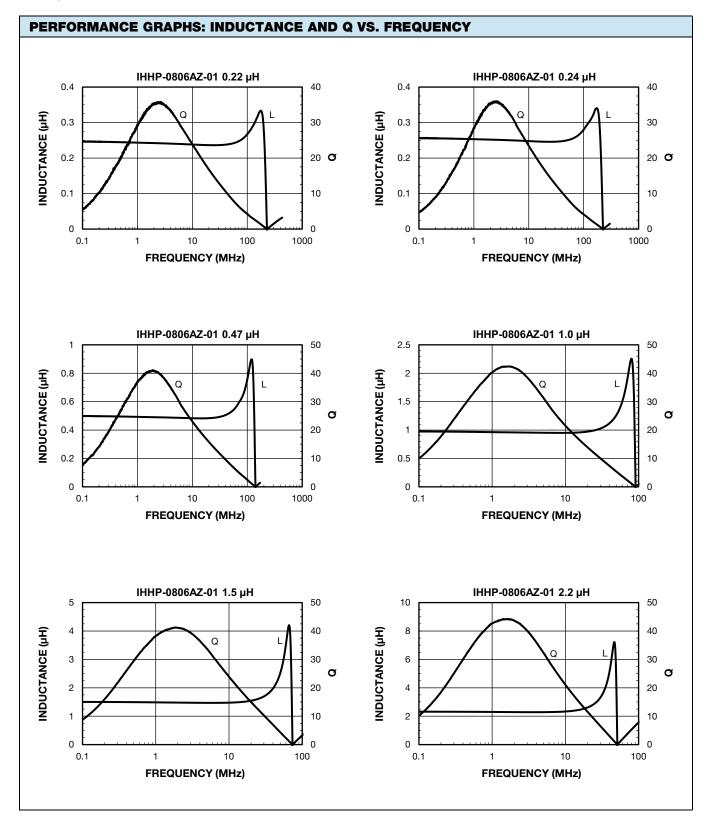














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