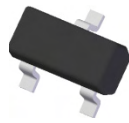


Features

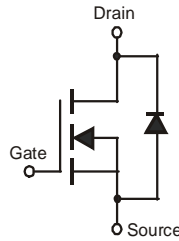
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

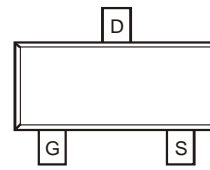
- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)



TOP VIEW



Internal Schematic



TOP VIEW

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	± 12	V
Continuous Drain Current (Note 3)	Steady State	$T_A = 25^\circ\text{C}$	I_D	6.4	A
		$T_A = 70^\circ\text{C}$		4.5	
Pulsed Drain Current (Note 4)			I_{DM}	30	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	P_D	0.78	W
Thermal Resistance, Junction to Ambient @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$	161	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

- Notes:
1. No purposefully added lead.
 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 3. Device mounted on FR-4 PCB with minimum recommended pad layout.
 4. Repetitive rating, pulse width limited by junction temperature.

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	1.0	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	V _{GS(th)}	0.5	-	1.2	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	-	20	28	mΩ	V _{GS} = 4.5V, I _D = 6.0A
			26	41		V _{GS} = 2.5V, I _D = 5.2A
Forward Transfer Admittance	Y _{fs}	-	6	-	S	V _{DS} = 10V, I _D = 6A
Diode Forward Voltage	V _{SD}	-	0.7	1.2	V	V _{GS} = 0V, I _S = 1.7A
DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	C _{iss}	-	550	-	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	88	-		
Reverse Transfer Capacitance	C _{rss}	-	81	-		
Gate Resistance	R _g	-	1.34	-	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (10V)	Q _g	-	15.6	-	nC	V _{GS} = 10V, V _{DS} = 10V, I _D = 6A
Total Gate Charge (4.5V)	Q _g	-	7.2	-		
Gate-Source Charge	Q _{gs}	-	1.0	-		
Gate-Drain Charge	Q _{gd}	-	1.9	-		
Turn-On Delay Time	t _{D(on)}	-	4.69	-	ns	V _{DD} = 10V, V _{GEN} = 4.5V, R _{GEN} = 1Ω, I _D = 6.7A
Turn-On Rise Time	t _r	-	13.19	-		
Turn-Off Delay Time	t _{D(off)}	-	22.10	-		
Turn-Off Fall Time	t _f	-	6.43	-		

Notes: 5. Short duration pulse test used to minimize self-heating effect.
6. Guaranteed by design. Not subject to production testing.

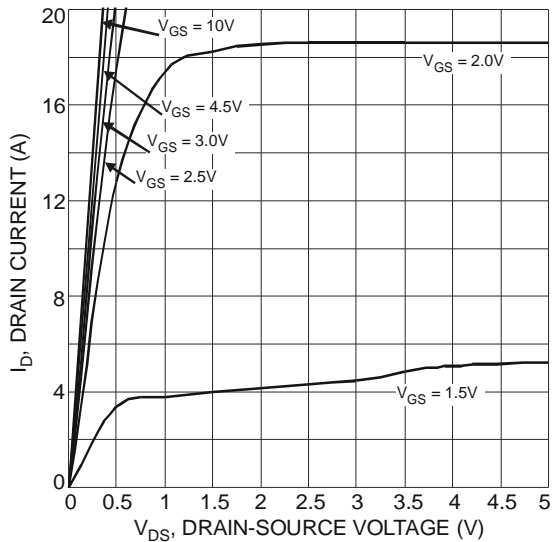


Fig. 1 Typical Output Characteristic

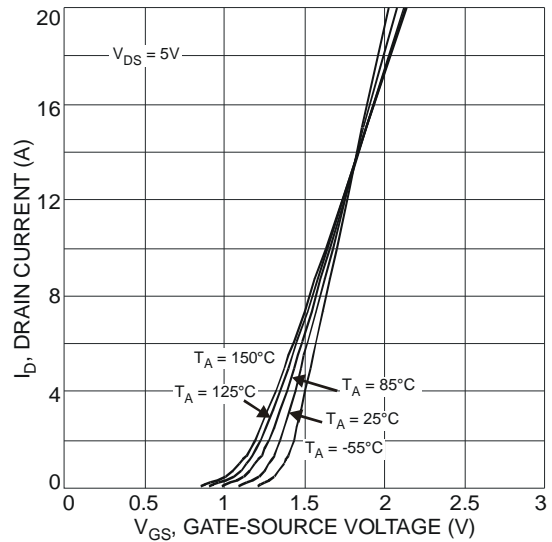


Fig. 2 Typical Transfer Characteristic

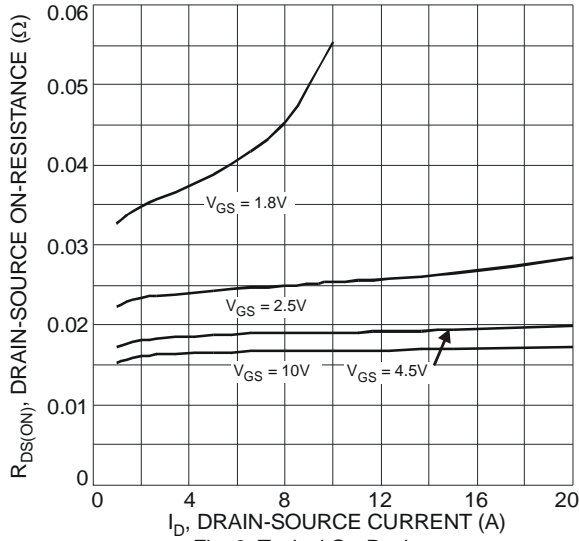


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

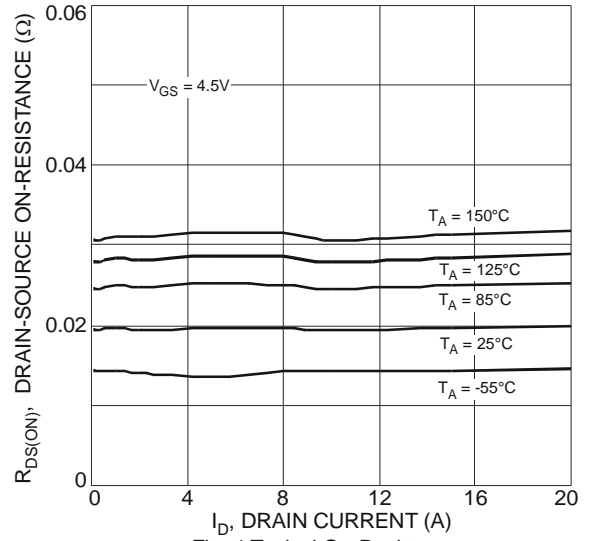


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

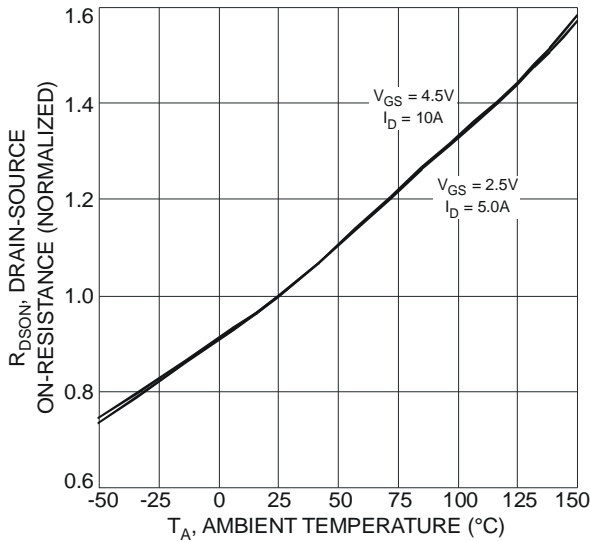


Fig. 5 On-Resistance Variation with Temperature

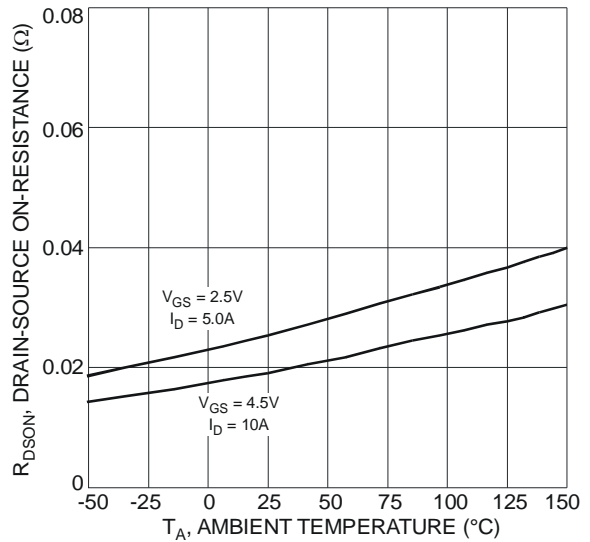


Fig. 6 On-Resistance Variation with Temperature

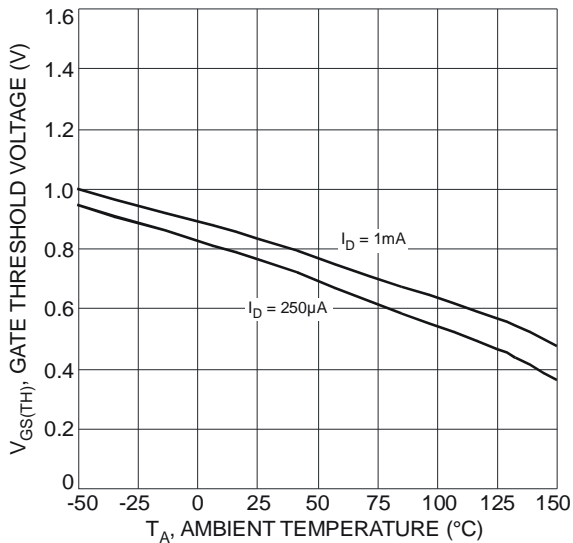


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

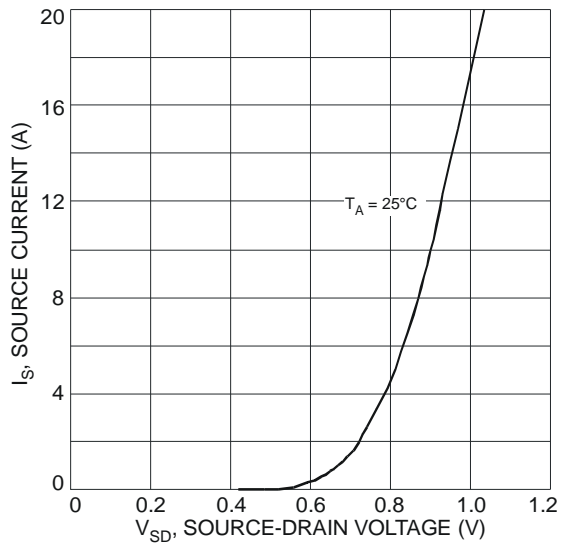


Fig. 8 Diode Forward Voltage vs. Current

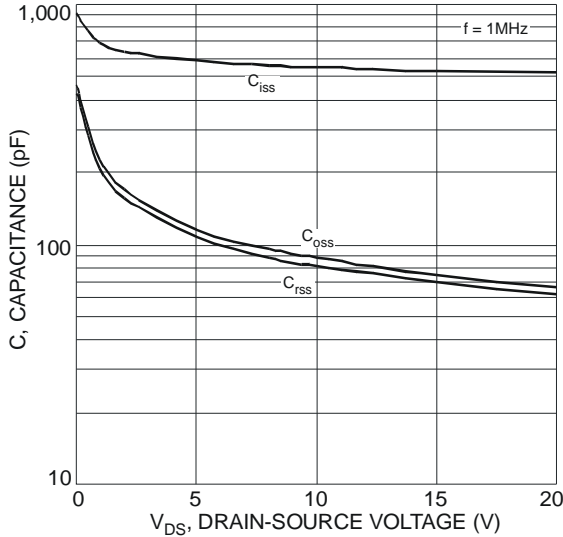


Fig. 9 Typical Total Capacitance

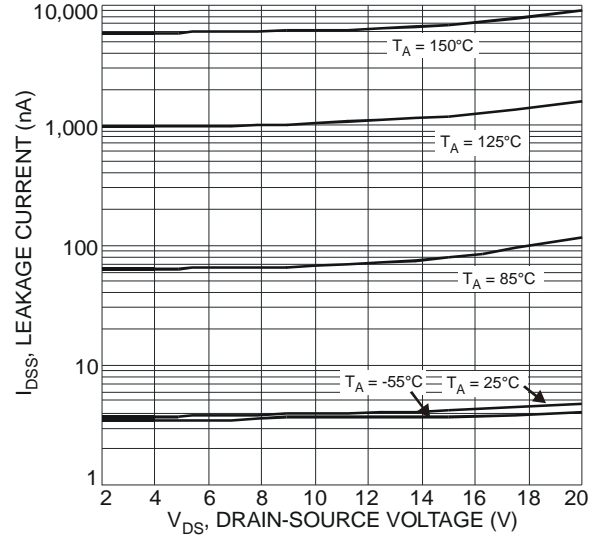


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

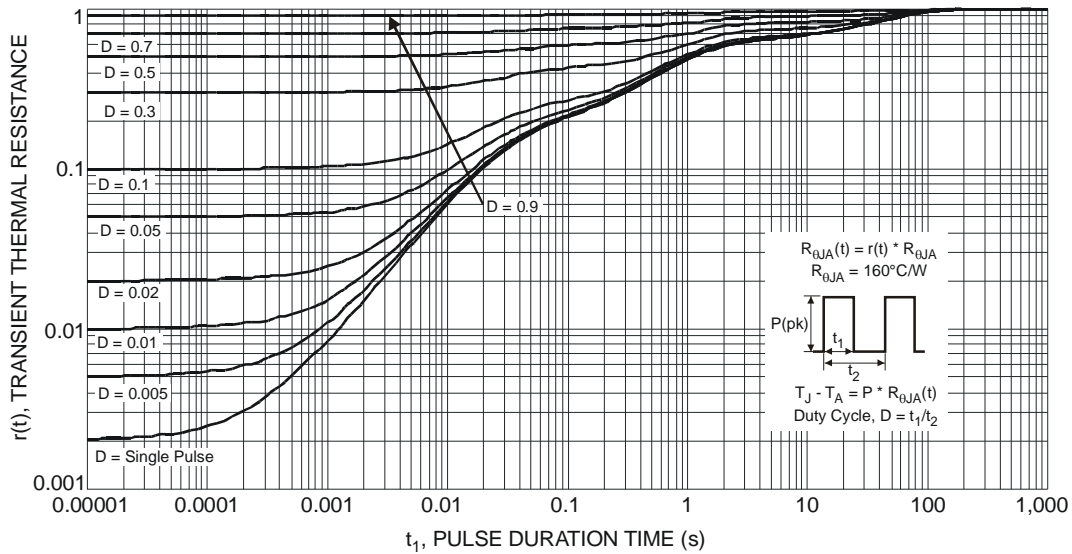


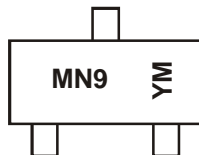
Fig. 11 Transient Thermal Response

Ordering Information (Note 7)

Part Number	Case	Packaging
DMN2041L-7	SOT-23	3000/Tape & Reel

Notes: 7. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information

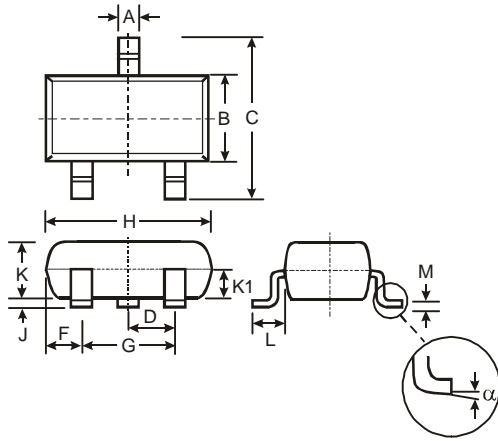


MN9 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: W = 2009)
 M = Month (ex: 9 = September)

Date Code Key

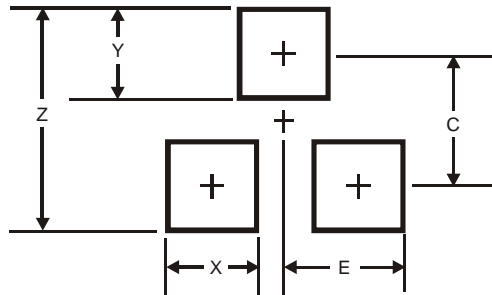
Year	2009	2010	2011	2012	2013	2014	2015					
Code	W	X	Y	Z	A	B	C					
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Package Outline Dimensions



SOT-23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.903	1.10	1.00
K1	-	-	0.400
L	0.45	0.61	0.55
M	0.085	0.18	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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