RN2483 LoRa™ Technology PICtail™/PICtail Plus Daughter Board User's Guide

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ISBN: 978-1-63277-330-2

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This declaration of conformity is issued by the manufacturer.

The development/evaluation tool is designed to be used for research and development in a laboratory environment. This development/evaluation tool is not a Finished Appliance, nor is it intended for incorporation into Finished Appliances that are made commercially available as single functional units to end users under EU EMC Directive 2004/108/EC and as supported by the European Commission's Guide for the EMC Directive 2004/108/EC (8th February 2010).

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Signed for and on behalf of Microchip Technology Inc. at Chandler, Arizona, USA

Derek Carlson
VP Development Tools

12-Sep-14
Date

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Preface

NOTICE TO CUSTOMERS

All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our web site (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a "DS" number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is "DSXXXXXXXXA", where "XXXXXXXX" is the document number and "A" is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the RN2483 LoRa™ Technology PICtail™/PICtail Plus Daughter Board. Items discussed in this chapter include:

- Document Layout
- Conventions Used in this Guide
- Recommended Reading
- The Microchip Web Site
- Development Systems Customer Change Notification Service
- Customer Support
- Document Revision History

DOCUMENT LAYOUT

This document describes how to use the RN2483 LoRa™ Technology PICtail™/PICtail Plus Daughter Board as a development tool to emulate and debug firmware on a target board, as well as how to program devices. The document is organized as follows:

- Chapter 1. "Overview" – This chapter describes the RN2483 LoRa™ Technology PICtail™/PICtail Plus Daughter Board and presents various board configurations.
- Chapter 2. "Getting Started" – This chapter describes the two main communication modes and the hardware requirements for getting started with RN2483 LoRa™ Technology PICtail™/PICtail Plus Daughter Board.
- Appendix A. "Board Schematic and PCB Details" – This appendix provides the RN2483 LoRa™ Technology PICtail™/PICtail Plus Daughter Board 's schematic, PCB layouts and Bill of Materials (BOM).

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	*MPLAB® IDE User's Guide*
	Emphasized text	...is the *only* compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	*File>Save*
Bold characters	A dialog button	Click **OK**
	A tab	Click the **Power** tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	`#define START`
	Filenames	`autoexec.bat`
	File paths	`c:\mcc18\h`
	Keywords	`_asm, _endasm, static`
	Command-line options	`-Opa+, -Opa-`
	Bit values	`0, 1`
	Constants	`0xFF, 'A'`
Italic Courier New	A variable argument	*`file`*`.o`, where *`file`* can be any valid filename
Square brackets [ ]	Optional arguments	`mcc18 [options]` *`file`* `[options]`
Curly brackets and pipe character: { | }	Choice of mutually exclusive arguments; an OR selection	`errorlevel {0|1}`
Ellipses...	Replaces repeated text	`var_name [, var_name...]`
	Represents code supplied by user	`void main (void) { ... }`

RECOMMENDED READING

This user's guide describes how to use LoRa™ Technology PICtail™/PICtail Plus Daughter Board. Other useful documents are listed below. The following Microchip documents are available and recommended as supplemental reference resources:

RN2483 Low-Power Long Range LoRa™ Technology Transceiver Module Data Sheet (DS50002346A)

This data sheet provides detailed specifications for the RN2483 module.

RN2483 LoRa™ Technology Module Command Reference User's Guide (DS40001784A)

This command reference user's guide describes how to configure the RN2483 module.

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- **In-Circuit Debuggers** – The latest information on the Microchip in-circuit debuggers. This includes MPLAB ICD 3 in-circuit debuggers and PICkit™ 3 debug express
- **MPLAB X IDE** – The latest information on Microchip MPLAB X IDE, the Windows® Integrated Development Environment for development systems tools
- **Programmers** – The latest information on Microchip programmers including the PICkit™ 3 development programmer

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- Technical Support

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Technical support is available through the web site at:
http://www.microchip.com/support.

DOCUMENT REVISION HISTORY

Revision A (April 2015)

This is the initial release of this document.

Chapter 1. Overview

1.1 INTRODUCTION

The RN2483 LoRa™ Technology PICtail™/PICtail Plus Daughter Board is a demonstration board that showcases the Microchip RN2483 Low-Power Long Range, LoRa™ Technology Transceiver Module.

The RN2483 LoRa Technology PICtail/PICtail Plus Daughter Board provides access to the RN2483 UART and General Purpose Input and Output (GPIO) ports.

This chapter discusses the following topics:

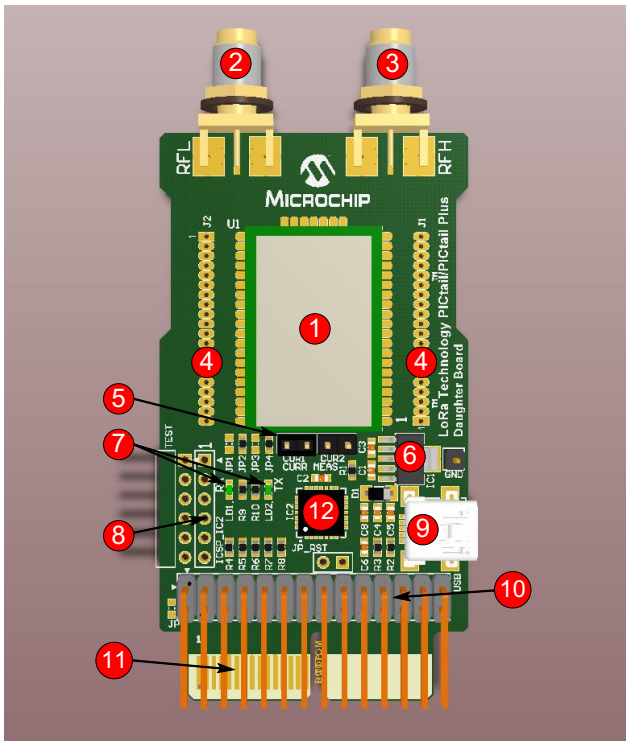
- Features
- Contents
- Board Configuration

1.2 FEATURES

The RN2483 LoRa Technology PICtail/PICtail Plus Daughter Board has the following features as represented in Figure 1-1:

1. Microchip RN2483 Low-Power Long Range, LoRa™ Technology Transceiver Module
2. SMA connector for 433 MHz band
3. SMA connector for 868 MHz band
4. Solder pads around the module for GPIOs, power pins and communication signals
5. Supply Current measurement points
6. On-board LDO
7. UART traffic LEDs
8. ICSP header to program the on-board PIC18 MCU
9. USB connector
10. PICtail connection interface
11. PICtail Plus connection interface
12. PIC18 MCU for custom functions

FIGURE 1-1: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD



The high-speed UART interface and the GPIO ports are available on the module to configure, control, and transfer data. The RN2483 LoRa Technology PICtail/PICtail Plus Daughter Board has PICtail and PICtail Plus connectors to interface with a PIC® microcontroller (MCU) on the development boards that support PICtail or PICtail Plus interface with the required pin mapping. The PICtail board also has an on-board PIC18 MCU available for custom user functions. It is preprogrammed to provide a simple USB-to-UART serial bridge enabling easy serial connection.

Demonstration of the RN2483 is performed by plugging the daughter board into a USB port of a PC. The USB port powers the daughter board and enables the user to communicate using the RN2483's ASCII commands.

Development of the RN2483 with Microchip's PIC MCU line is possible via the 28-pin PICtail connector to a PIC18 Explorer or 30-pin card edge PICtail Plus connector to an Explorer 16.

1.3 CONTENTS

The package kit contents contain the following tools as listed in Table 1-1.

TABLE 1-1: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD

Description	Part Number
RN2483 LoRa™ Technology PICtail™/PICtail Plus Daughter Board	RN-2483-PICtail
USB Cable	—
433 MHz antenna	—
868 MHz antenna	—

1.4 BOARD CONFIGURATION

Prior plugging the module into the motherboard's socket, ensure that one of the current measure jumpers, CUR1 or CUR2, are shunted.

PICtail Daughter Board can be powered from two sources, either from one of the PICtail headers or from USB. Both power sources can be active at the same time.

RF antennas must be connected to the SMA connectors prior attaching power to the board.

Ensure that the applied power supply voltage does *not* exceed the board limits. Figure 1-2, Figure 1-3, and Figure 1-4 show the connection to various development boards.

FIGURE 1-2: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD CONNECTED TO EXPLORER 16 DEVELOPMENT BOARD

FIGURE 1-3: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD CONNECTED TO PIC18 WIRELESS DEVELOPMENT BOARD

FIGURE 1-4: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD CONNECTED TO PICDEM™ PIC18 EXPLORER DEMONSTRATION BOARD

Chapter 2. Getting Started

2.1 INTRODUCTION

This chapter describes the hardware requirements for RN2483 LoRa Technology PICtail/PICtail Plus Daughter Board and also provides the different types of communication modes.

The module accepts commands via UART interface. Basically, two communication modes are supported by the daughter board, USB mode and PICtail mode.

PICtail mode gives more computing power to the user program, since motherboards contain additional MCUs.

This chapter discusses the following topics:

- Communication Modes
- Communication to the Module
- Hardware Description

2.2 COMMUNICATION MODES

2.2.1 USB mode

USB mode is initiated if the daughter board is connected to a USB port via a mini-USB cable. In this mode, the on-board PIC18 MCU provides a USB-to-UART bridge.

Supply voltage is provided via USB and the on-board LDO (IC1) which regulates 5V to the nominal 3.3V.

2.2.2 PICtail mode

PICtail mode is initiated if no USB cable is attached to the board and the board is plugged into the appropriate motherboard.

Note:	User must ensure that PICtail/PICtail Plus port pins are fully compatible to the pinout of the daughter board.

When USB power is *not* attached, the on-board PIC18 MCU does *not* influence UART communication.

Note:	Some motherboards may adjust the supply voltage to the attached MCU Plug-in Module. Do *not* exceed the supply voltage limits of the module.

2.2.3 PICtail mode with USB connected

The daughter board can be used in a third mode when it is connected to a PICtail motherboard while the USB is also connected. It is useful when the user wants to set the supply voltage from the PICtail connector while the communication must be continuously active via the USB interface. The on-board PIC18 MCU takes over the control of the UART interface. In this case, the motherboard is unable to send UART messages to the module, however, the messages sent by the module appear on the PICtail UART.

Another case is that the motherboard does *not* have power supply. In this case, the motherboard can be powered from the USB together with the daughter board. User must take care of the maximum output current of the on-board LDO, which is 500 mA.

A short on the jumper JP_RST on the daughter board forces the board to operate in PICtail mode, although USB remains connected. The jumper JP_RST keeps the on-board PIC18 MCU in reset state to ensure that USB-to-UART protocol translation is *not* performed in this mode. If jumper JP_RST is *not* shorted, on-board PIC18 MCU has the priority over the UART communication.

Note: Only 30-pin PICtail Plus connection is detected. If the daughter board is attached to a 28-pin PICtail connector, the jumper JP_RST has no affect.

2.3 COMMUNICATION TO THE MODULE

In PICtail mode, the Microchip 8/16/32-bit PIC MCUs on the motherboards can run custom functions and connect to the module using the UART interface, which accepts ASCII commands from the host.

In USB mode, when the daughter board is connected to the host via USB, the on-board PIC18 MCU uses the CDC class to create a USB-to-UART bridge device. The host can run a simple terminal emulator application to issue commands.

2.4 HARDWARE DESCRIPTION

The RF signal path is connected to the SMA edge connectors. The 433 MHz band RF signal is transmitted through RFL SMA edge connector, whereas RFH SMA connector is used for the 868 MHz band.

The current consumption measurement of the module is supported by the on-board current measure jumpers. If jumper CUR1 is shunted, the supply current flows directly to the module.

There are two ways to measure current consumption:

- A current meter can be connected to CUR1 jumper pins to measure the actual current consumption of the module. CUR2 must be left open.
- The current consumption graph can be recorded in the time domain by removing the shunt from CUR1 jumper and shunting CUR2 at the same time. Use a two channel oscilloscope, which supports subtracting mathematical function. Connect oscilloscope probes to CUR1 jumper pins while CUR2 jumper is shunted. Set the oscilloscope to display the difference between the two channels.

All pins of the module can be accessed via through hole pads which is located on both sides of the module. User can mount two 1.27 mm pitched socket headers if required. Sockets can connect the module pins to a custom board, whereas the daughter board provides the power. The through hole pads are classified into two groups which are located on both sides of the module. Each pad group, J1 and J2, has a dedicated pad on which power is delivered to the custom board. The supply current is measured together with the module's supply current. To do this, JP2 must be shorted for J2 and JP3 for J1.

If the supply current is separated from the module, the other two jumpers must be shorted. To power the custom board separately, shunt JP1 or JP4.

The on-board PIC18 MCU is programmable via programming port ICSP_IC2. In USB mode, LD1 and LD2 LEDs indicate communication on the UART.

Table 2-1 shows the PICtail/PICtail Plus connections to various boards.

TABLE 2-1: PICtail™ AND PICtail PLUS CONNECTIONS

Signal Name	Description	Pin number on PICtail connector	Pin number on PICtail Plus connector
+3V3	Positive Supply Rail	26	21, 22
GND	Ground Supply Rail	28	9, 10, 16
Module_TX	UART transmit output of the module	21	2
Module_RX	UART receive input of the module	17	4
Module_RTS	UART Hardware handshake output of the module(1)	4	19
Module_CTS	UART Hardware handshake input of the module(1)	3	20
PT_Module_RESET	Master Clear input of the module	1	6
PT+_SENSE	Sensing signal for PICtail Plus connector (the platform connects this line to GND when plugged)	—	15

Note 1: Optional handshake lines are supported in future firmware releases.

NOTES:

Appendix A. Board Schematic and PCB Details

A.1 INTRODUCTION

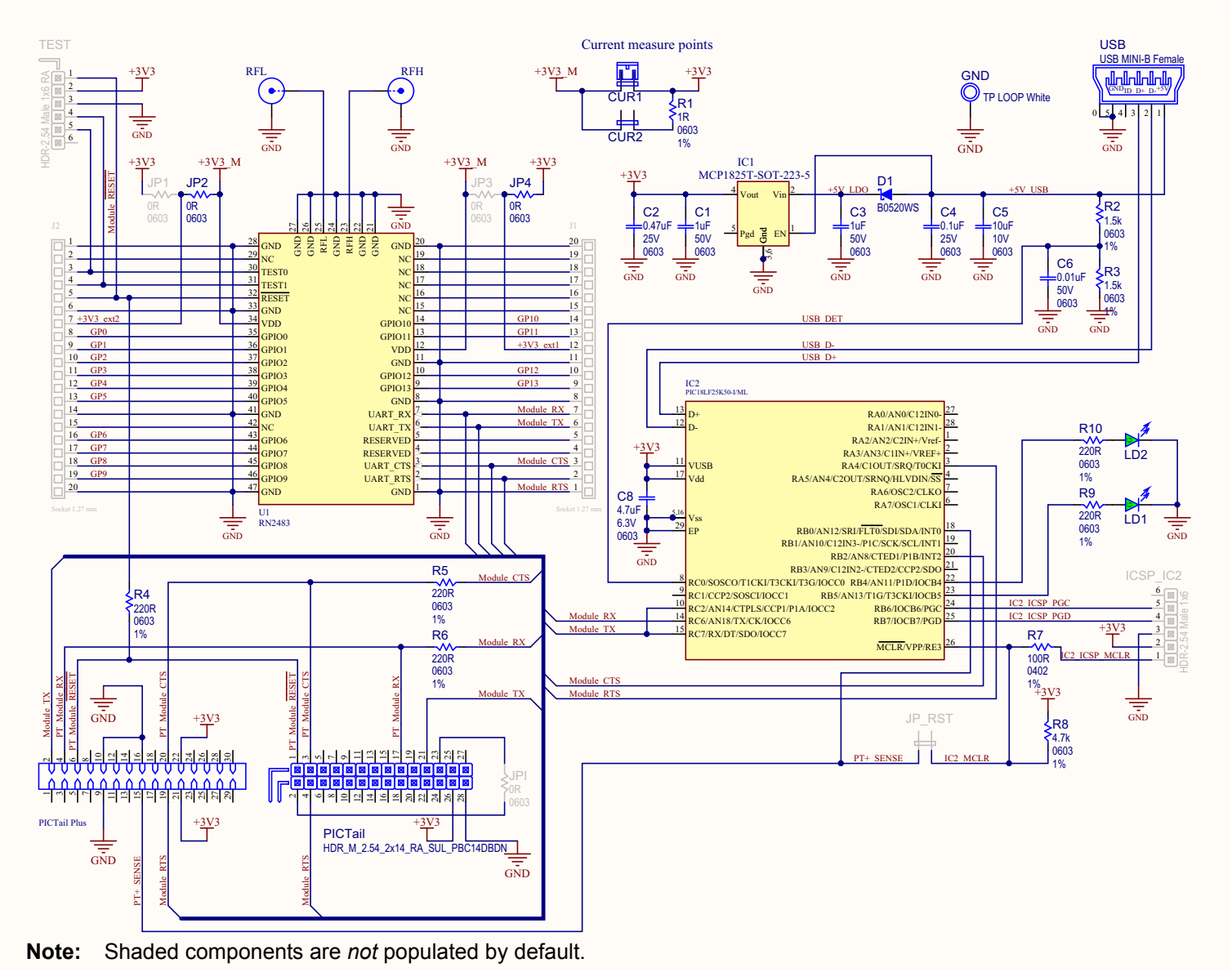
This appendix provides the RN2483 LoRa Technology PICtail/PICtail Plus Daughter Board schematic, PCB layout and Bill of Materials (BOM).

- Board Schematic
- PCB Layout
- Bill of Materials

A.2 BOARD SCHEMATIC

Figure A-1 shows the board schematic.

FIGURE A-1: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD SCHEMATIC



Note: Shaded components are *not* populated by default.

A.3 PCB LAYOUT

LoRa Technology PICtail/PICtail Plus Daughter Board is a 2-layer, FR4, 1.55 mm, plated through hole PCB construction.

Figure A-2 through Figure A-4 illustrate the PCB layers, Figure A-5 shows the assembly drawing of LoRa Technology PICtail/PICtail Plus Daughter Board.

FIGURE A-2: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD TOP SILKSCREEN

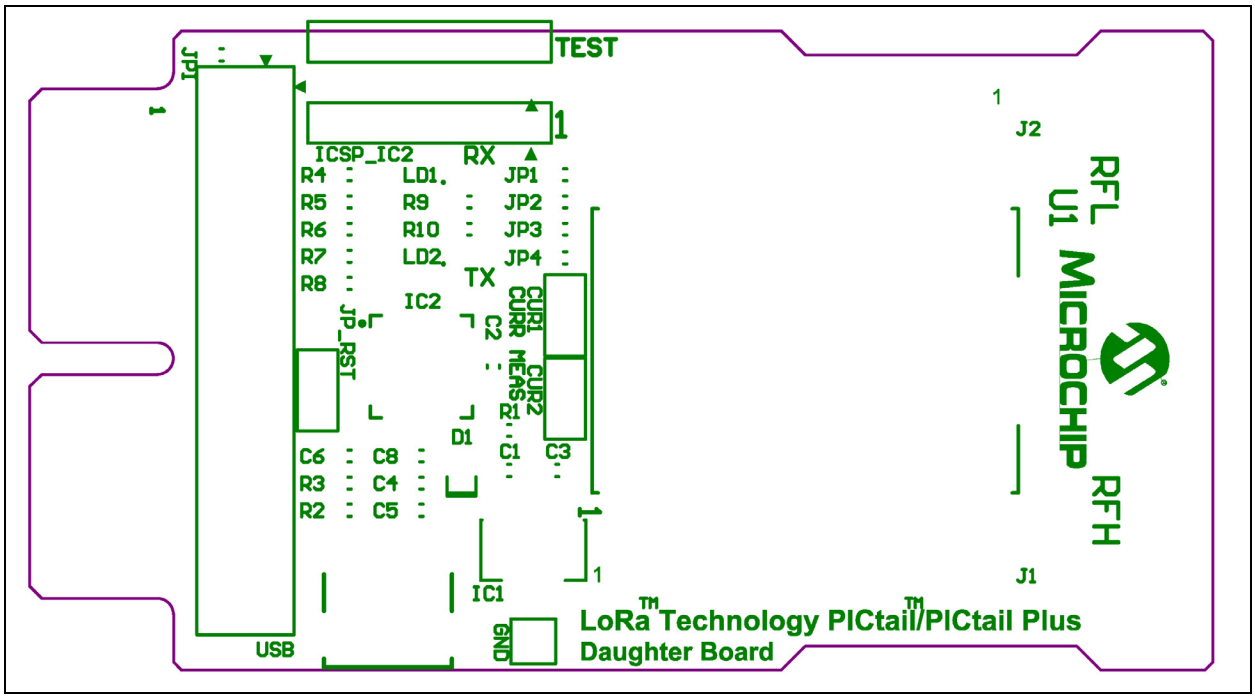


FIGURE A-3: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD TOP COPPER

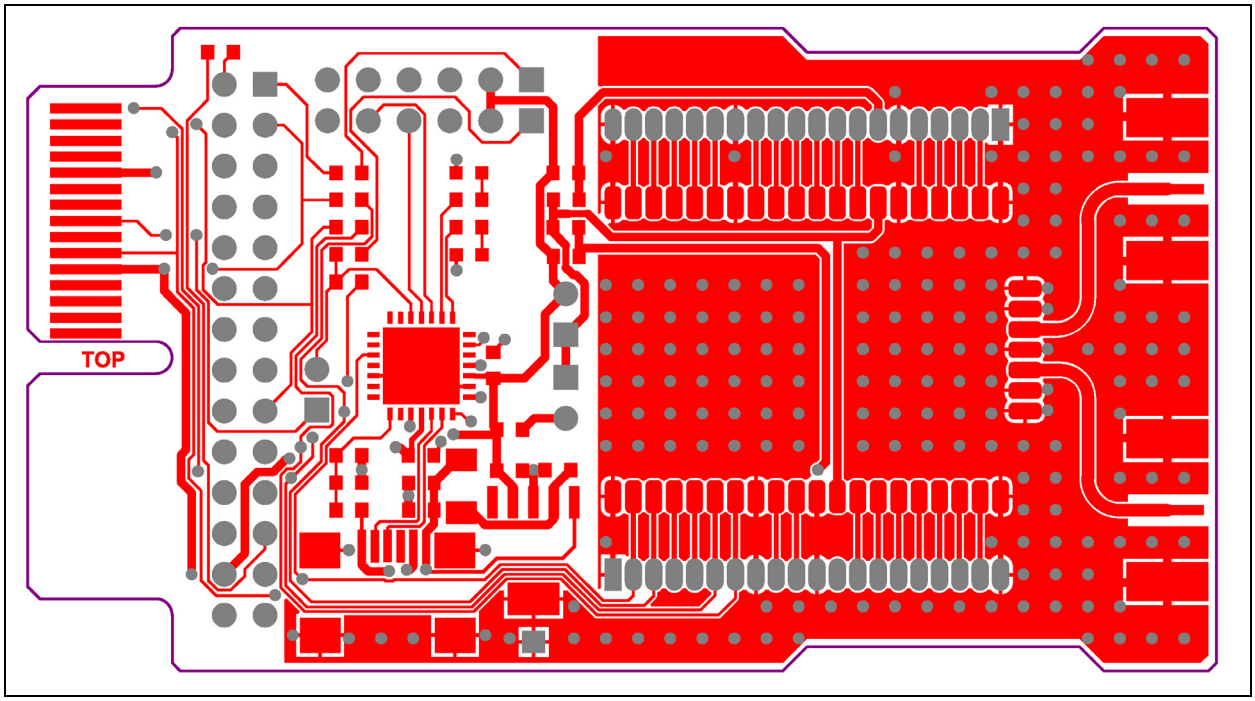


FIGURE A-4: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD BOTTOM COPPER (BOTTOM VIEW)

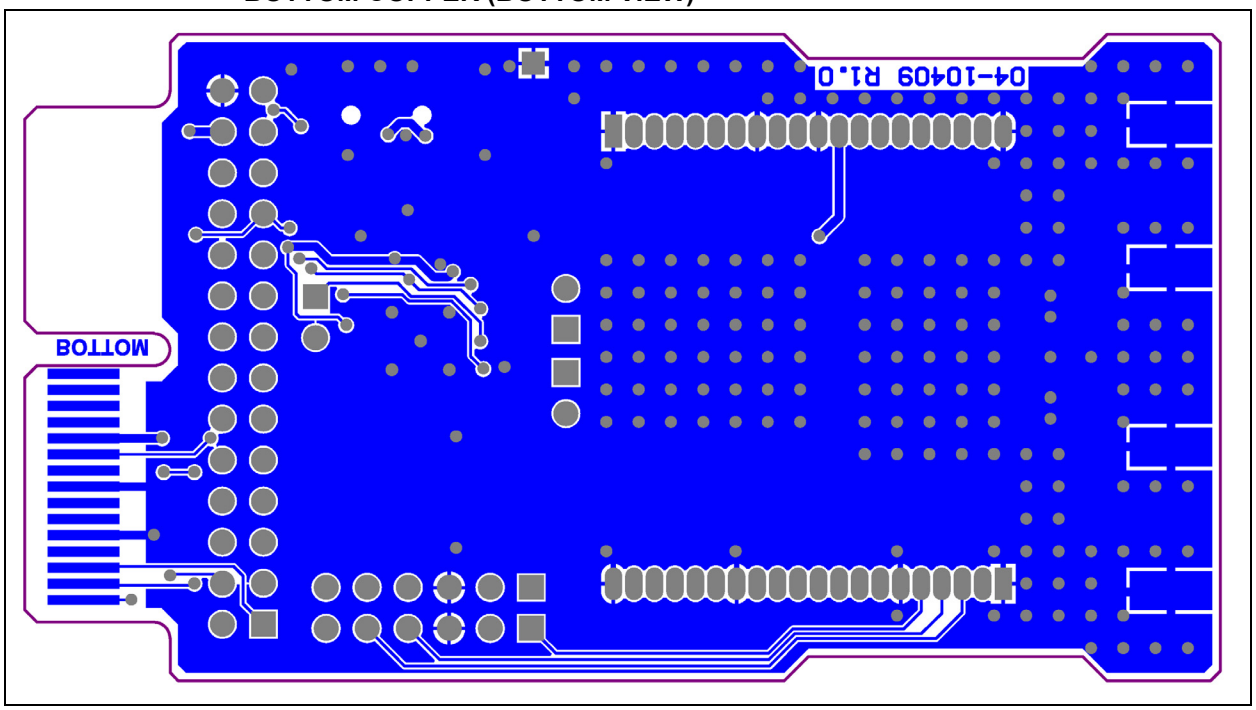
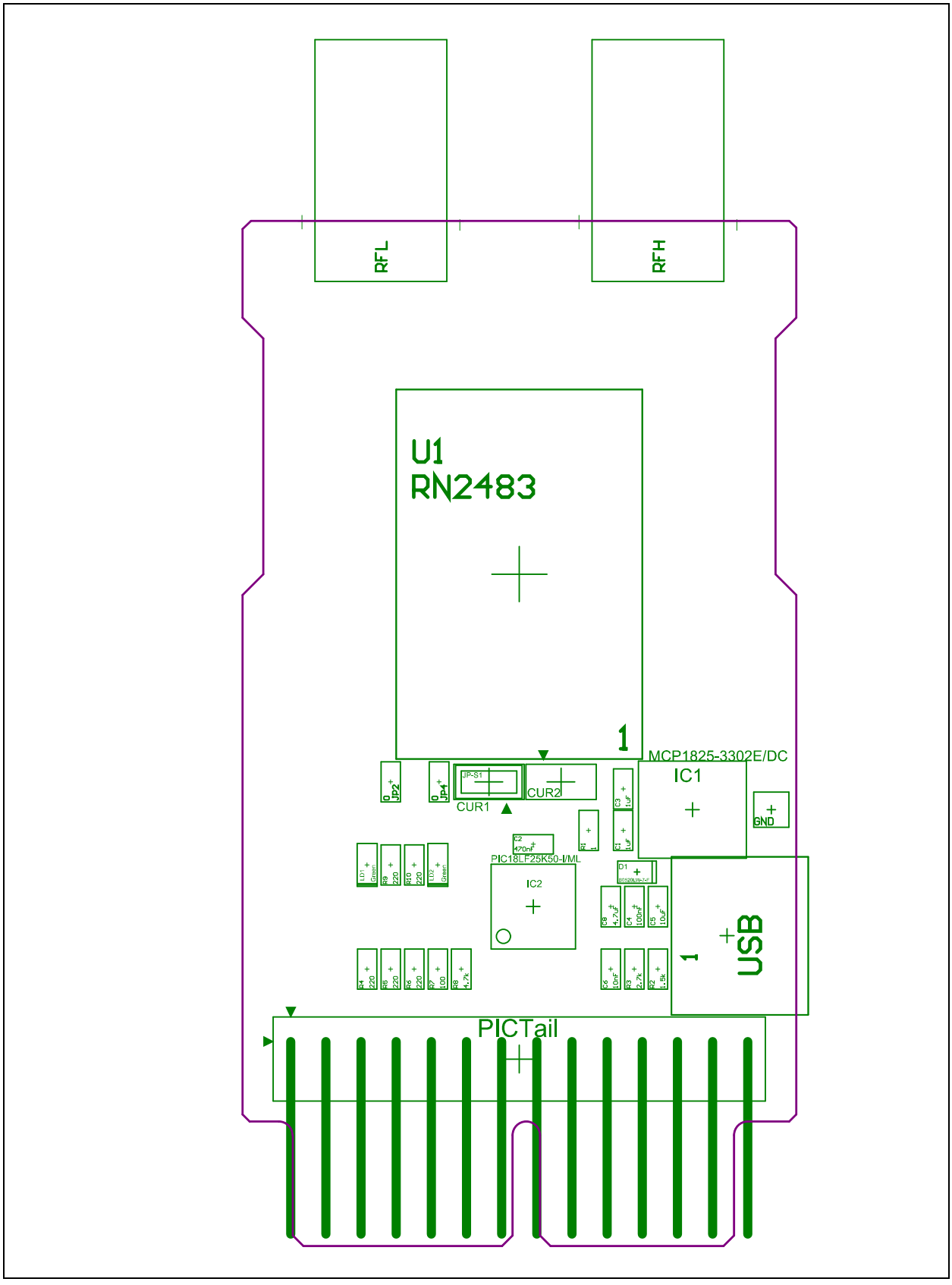


FIGURE A-5: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD TOP ASSEMBLY



A.4 BILL OF MATERIALS

TABLE A-1: RN2483 LORA™ TECHNOLOGY PICtail™/PICtail PLUS DAUGHTER BOARD BILL OF MATERIALS (BOM)

Reference	Value	Description	Vendor	Vendor P/N
C1, C3	1uF	CAP, 0603, 25V, 10%, X7R	Murata Electronics North America	GRM188R71E105KA12D
C2	470nF	CAP, 0603, 25V, 10%, X7R	Murata Electronics North America	GRM188R71E474KA12D
C4	100nF	CAP, 0603, 25V, Y5V	Yageo	CC0603ZRY5V8BB104
C5	10uF	CAP, 0603, 6.3V, 20%, X5R	Murata Electronics North America	GRM188R60J106ME47D
C6	10nF	CAP, 0603, 50V, 10%, X7R	Murata Electronics North America	GRM188R71H103KA01D
C8	4.7uF	CAP, 0603, 6.3V, 10%, X5R	Murata Electronics North America	GRM188R60J475KE19D
CUR1, CUR2	—	CONN Pin2 2.54 mm_jumper	Harwin Inc	M20-9990245
D1	—	DIODE SCHOTTKY 20V 0.5A SOD123	Diodes Inc	B0520LW-7-F
GND	—	CONN Pin1	Keystone	5012
IC1	—	IC MCP1825-3302E/DC	Microchip	MCP1825-3302E/DC
IC2	—	IC PIC18LF25K50-I/ML	Microchip	PIC18LF25K50-I/ML
JP2, JP4	—	RES 0 OHM 0603 JUMPER 2P	Vishay Dale	CRCW06030000Z0EA
JP-S1	—	JUMPER SHUNT 2POS 2.54 mm LOPRO GOLD	TE Connectivity	382811-8
LD1, LD2	—	LED 565NM GRN DIFF 0603	Lumex Opto/Components Inc	SML-LX0603GW-TR
PICTail	—	CONN Pin14x2 2.54 mm right angle (PBC14DBDN)	Sullins Connector Solutions	PBC14DBDN
R1	1.00 Ohm	RES 0603 1/10W 1%	Yageo	RC0603FR-071RL
R2	1.50 kOhm	RES 0603 1/10W 1%	Vishay Dale	CRCW06031K50FKEA
R3	2.70 kOhm	RES 0603 1/10W 1%	Vishay Dale	CRCW06032K70FKEA
R4, R5, R6, R9, R10	220 Ohm	RES 0603 1/10W 1%	Vishay Dale	CRCW0603220RFKEA
R7	100 Ohm	RES 0603 1/10W 1%	Vishay Dale	CRCW0603100RFKEA
R8	4.70 kOhm	RES 0603 1/10W 1%	Vishay Dale	CRCW06034K70FKEA
RFH, RFL	—	CONN JACK SMA 50 OHM EDGE MOUNT	Cinch Connectivity Solutions Johnson	142-0711-821
U1	—	RF module RN2483 LoRa EU 433/868MHz	Microchip	RN2483
USB	—	CONN MINI B USB R/A SMD	Hirose	UX60-MB-5ST

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