

High Voltage Ceramic Capacitor - JYC

1.STANDARD RATINGS

CAD	пν		tolerance	D	Т	Р	L	d	С	
CAP	κ.ν	Material	±	Max	Max	±1	Min	±0.05	Max	
(pF)	(KV)		(%)	(mm)						
1000	15	Y5T	10	13.5	7.5	10	16	0.65	3.0	

2.Dimension:(mm)



3. Electrical characteristics

NO	Item	Specification	Testing Method			
1	Temperature range		-25℃~85℃			
2	Appearance	No marked defect on appearance form	Visually inspected			
3	Marking	To be easily legible	Visually inspected			
4	Capacitance	Within specified tolerance				
5	(D.F.) Dissipation Factor	Y5T:D.F.≤2.5%	dissipation factor should be measured at 25°C with 1±0.1KHz and AC1.0V(r.m.s.)			
6	(I.R.) Insulation Resistance	Y5T: CR≤25nF,≥4000MΩ CR>25nF, Rj·CR≥100s Note: "s" for the time constant, that is, insulation resistance times the capacitance, in units of seconds, also known as megohm micro method.	The insulation resistance should be measured with DC500V within 60±5 sec of charging. Charge/Discharge current ≤50mA			
7	(T.V.) Voltage proof	Requirements: during the trials capacitor should be no breakdown or fly arc.	Test conditions: The spec provides the following capacitor voltage is applied between the terminations for the identification of the approval and periodic tests applied voltage time 1min, quality and consistency of the batch test for the applied voltage time 2s.Following table provides an applied voltage; charge current should not exceed 0.05A. Set 0.50mA leakage current.Rated voltage (UR)1000V≤UR<15KV 1.5URNote: if the customer has special requirements or the size of special specifications, then according to customer special requirements or special requirements or testing			

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NO	Ite	em	Specification	Testing Method						
	Temperature Characteristics			The capacitance each st	The capacitance measurement should be made at each step specified in Table 3.					
8			Char.Capacitance ChangeY5T $+22/-33\%$ Temp. range: $-25 \sim +85^{\circ}C$	Step	1	2	3	4	5	
				Temperature°C	20 ±2	-25 ±2	20 ±2	85 ±2	20 ±2	
9	Solderabilit	ty of Leads	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of a capacitor should be dipped into molten solder for 2±0.5 sec. The depth of immersion is up to about 1.5 to 2.0mm from the root of lead Temp. of solder: Lead Free Solder (Sn-2Ag-0.5Cu) 260±5°C.						
		Appearance	No marked defect As in figure, the lead wires should						rsed in	
10	Soldering Effect	Capacitance Change	Y5T:±15%	solder of 260±5°C up to 1.5 to 2.0mm from root of terminal for 10.0±0.5sec.					om the	
11	Robustness of Terminations	Pull	Lead wire should not be cut off. Capacitor should not be broken.	As shown in the f capacitor and ap each lead wire capacitor up to as Diameter(n $0.35 < d \le 0$ $0.5 < d \le 0$ $0.8 < d \le 1$	igure a ply a t e in th s follov	at rightensile e radi vs and	nit, fix t e weigh al direc keep i keep i keep i nimum 5± 10= 20=	tensior 10% 10% 10%	v of the ally to the ±1sec.	
		Bending		Each lead wire should be subjected to 5N weight and then a 90° bend, at the point of egress, in one direction, return to original position, and then apply a 90° bend in the opposite direction at the rate of one bend in 2 to 3 sec. For a cycle, a total of 22 cyclesDiameter(mm)Minimum tension (N) $0.35 < d \le 0.5$ 0.35 < d \le 0.5 $2.5 \pm 10\%$ $0.8 < d \le 1.25$						



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NO		Item	Specification	Testing Method					
		Appearance	No marked defect.	Capacitors should be tested in the order shown in					
	Rapid changes in	Capacitance Change	Y5T:C/C≤15%	the following table (for one cycle) for five consecutive cycles.					
		D.F.	Y5T: tanδ≤7%	Step	Temperature (℃)	Time (min)			
				1	-25 +25	30			
12			Y5T: Ri≥1000MΩor Ri∙CR≥25s, Whichever is smaller.	3	+85	30			
	temperature			4	+25	3			
		IR		[Pre-treatment]: Capacitor should be stored at 85±2°C for 1 hr., then placed at room condition for 24±2 hrs. [Post-treatment]: Capacitor should be stored for 24±2 hrs. at room condition.					
	Steady	Appearance	No marked defect.	Test conditions:					
10		Capacitance Change	Y5T:C/C≤15%	1) half of the sample to impose UR, the other half is not the applied voltage, charge current					
13		D.F.	Y5T: tanδ≤7%	2) Temperature	e: 40 ± 2 °C; rel	ative humidity:			
		IR	Y5T:Ri≥1000MΩ or Ri∙CR≥25s,Whichever is smaller.	95± 2%. 3)continued 500h +24 /-0h.					
		Appearance	No marked defect.		Test conditions:				
		Capacitance Change	Y5T: C/C≤20%	1)temper	1)temperature: the upper category				
	Durability	D.F.	Y5T:tanδ≤7%	2)Voltage: 2 type ceramic 1 5UR					
14				Charge current should not exceed 50mA.					
		IR		3)Duration: 1000h +48 /-24h.					
			Y5T:Ri≥2000MΩ or	[Post-processing test]:					
			Ri·CR≥50s,Whichever is smaller.	n the standard atmospheric conditions at least					
				24 hours after recovery, measuring electrical					
				properties.					

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