Product Overview

MC74AC14: Hex Inverter with Schmitt Trigger Input

For complete documentation, see the data sheet.

The MC74AC14/74ACT14 contains six logic inverters which accept standard CMOS Input signals (TTL levels for MC74ACT14) and provide standard CMOS output levels. They are capable of transforming slowly changing input signals into sharply defined, jitter-free output signals. In addition, they have a greater noise margin then conventional inverters.

The MC74AC14/74ACT14 has hysteresis between the positive-going and negative-going input thresholds (typically 1.0 V) which is determined internally by transistor ratios and is essentially insensitive to temperature and supply voltage variations.

Features

- Schmitt Trigger Inputs
- Outputs Source/Sink 24 mA
- ACT14 Has TTL Compatible Inputs
- Pb-Free Packages are Available

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
MC74AC14DG	Pb-free Halide free	Active	Inverter	6	2	6	7	24	SOIC-14
MC74AC14DR2G	Pb-free Halide free	Active	Inverter	6	2	6	7	24	SOIC-14
MC74AC14DTR2G	Pb-free Halide free	Active	Inverter	6	2	6	7	24	TSSOP-14
NLV74AC14DR2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	Inverter	6	2	6	7	24	SOIC-14
NLV74AC14DTR2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	Inverter	6	2	6	7	24	TSSOP-14
NLVACT14DR2G	AEC Qualified Pb-free Halide free	Active	Inverter	6	4.5	5.5	7	24	SOIC-14

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