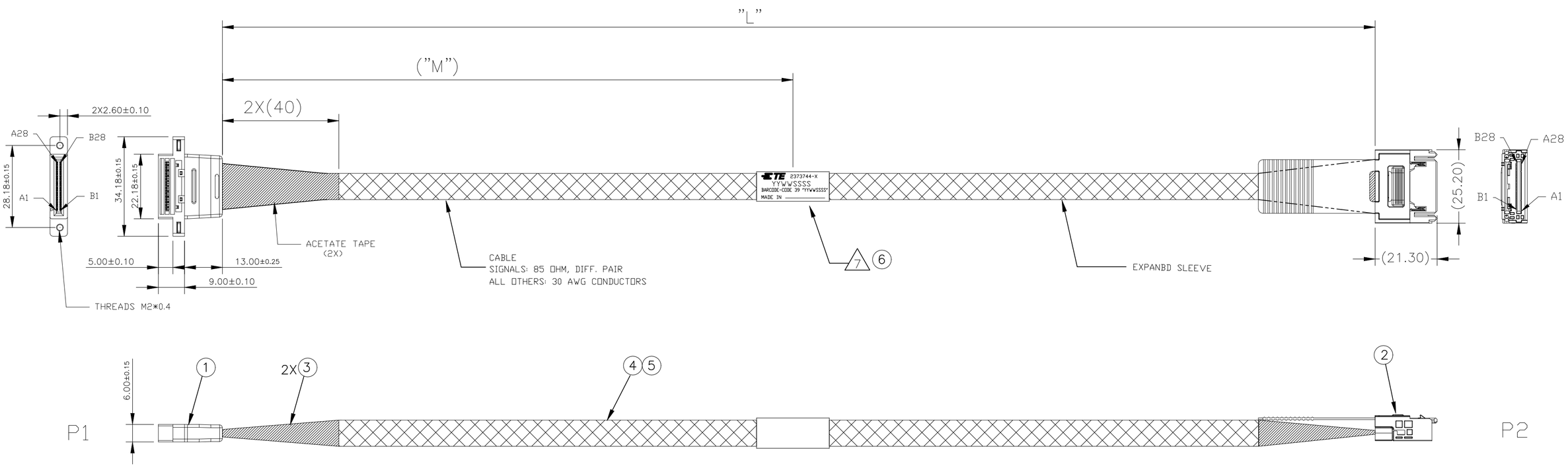
REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	A	RELEASED PER ECN-22-152543	06MAY2022	JG	DZ



1. ALL MATERIALS, COMPONENTS AND PROCESS SHALL COMPLY WITH TEC-138-702. (CONTAINS NO BANNED OR RESTICTED SUBSTANCES).
2. NO REACH SvHC SHALL BE CONTAINED ABOVE THE THRESHOLD AS DEFINED IN REACH SvHC COMPLIANCE DEFINITION IN ANNEX A OF TEC-138-702.
3. ASSEMBLY TO BE TESTED FOR CONTINUITY, OPENS, SHORTS, AND SIGNAL INTEGRITY.
4. CABLE BEND RADIUS 7X BUNDLED CABLE OD.
5. SEE SHEET 2 FOR WIRING SCHEMATIC.
6. CONNECTORS ARE GEN-Z COMPLIANT.
7. LABEL INFORMATION SHOWN BELOW:

8. SLIVER CABLE PLUG MATES WITH TE SLIVER RECEPTACLE P/N 2332141-X, OR 2332208-X.
9. KEY CHARACTERISTIC PER STANDARD OPERATING PROCEDUCE TEC-407-55. APPLIES TO BOTH DIMENSION AND FEATURE CONTROL TOLERANCE.

500	1000+/−15	2373744-2
250	500+/−10	2373744-1
"M"	"L"	TE PN

ITEM	QTY	QTY	QTY	DESCRIPTION	PART NAME	U/M
6	1	1	1	LABEL, SYNTHETIC PAPER	LABEL	PC
5	AR	AR	AR	EXPAND SLEEVE,BLACK, HF	SLEEVE	MR
4	AR	-	MR	CABLE, 29AWG, 85OHM,DIFF PAIRS, VW-1	CABLE WIRE	MR
	-	AR		CABLE, 30AWG, 85OHM,DIFF PAIRS, VW-1		
3	AR	AR	MR	ACETATE TAPE, BLACK	ACETATE TAPE	MR
2	1	1	PC	SLIVER 2.0 1C STR,PULL TAB	CABLE PLUG	PC
1	1	1	PC	SLIVER 2.0 1C CABLED RECEPTACLE	CABLED RECEPTACLE	PC
ITEM	-2	-1	U/M	DESCRIPTION	PART NAME	U/M

THIS DRAWING IS A CONTROLLED DOCUMENT.

DIMENSIONS: mm

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ± –
1 PLC ± –
2 PLC ± –
3 PLC ± –
4 PLC ± –
ANGLES ± –

MATERIAL –
FINISH –

DWN GARNETT WU 25MAY2020
CHK – –
APVD DAVID ZHANG 25MAY2020
PRODUCT SPEC –
APPLICATION SPEC –
WEIGHT –

TE Connectivity Ltd.

NAME
S;LIVER 2.0 1C LEAD FRAME TO SLIVER 2.0 1C 85OHM
–

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A2	00779	C-2373744	–

CUSTOMER DRAWING | SCALE 1:1 | SHEET 1 OF 2 | REV A

2373744

REVISIONS

P	LTR	DESCRIPTION	DATE	DWN	APVD
	–	SEE SHEET 1	–	—	—

P1			P2	
PIN NUMBER	PIN DEFINE	WIRE TYPE	PIN DEFINE	PIN NUMBER
A1	GND		GND	A1
A2	GND		GND	A2
A3	GND		GND	A3
A4	GND		GND	A4
A5	GND		GND	A5
A6	GND		GND	A6
A7	SMB_BMC_OCP_V3_1_A_SCL		SMB_BMC_OCP_V3_1_A_SCL	A7
A8	SMB_BMC_OCP_V3_1_A_SDA		SMB_BMC_OCP_V3_1_A_SDA	A8
A9	SMB_BMC_OCP_V3_1_A_RST_N		SMB_BMC_OCP_V3_1_A_RST_N	A9
A10	PRSNTA#		PRSNTA#	A10
A11	RST_OCP_V3_1_A_1_N		RST_OCP_V3_1_A_1_N	A11
A12	OCP_V3_1_PRSNTB2_N		OCP_V3_1_PRSNTB2_N	A12
A13	GND		GND	A13
A14	CLK_100M_DB2001_OCP_V3_1_D_DN		CLK_100M_DB2001_OCP_V3_1_D_DN	A14
A15	CLK_100M_DB2001_OCP_V3_1_D_DP		CLK_100M_DB2001_OCP_V3_1_D_DP	A15
A16	GND		GND	A16
A17	P3E_CPU0_PCIE3_RX_DP<0>		P3E_CPU0_PCIE3_RX_DP<0>	A17
A18	P3E_CPU0_PCIE3_RX_DN<0>		P3E_CPU0_PCIE3_RX_DN<0>	A18
A19	GND		GND	A19
A20	P3E_CPU0_PCIE3_RX_DP<1>		P3E_CPU0_PCIE3_RX_DP<1>	A20
A21	P3E_CPU0_PCIE3_RX_DN<1>		P3E_CPU0_PCIE3_RX_DN<1>	A21
A22	GND		GND	A22
A23	P3E_CPU0_PCIE3_RX_DP<2>		P3E_CPU0_PCIE3_RX_DP<2>	A23
A24	P3E_CPU0_PCIE3_RX_DN<2>		P3E_CPU0_PCIE3_RX_DN<2>	A24
A25	GND		GND	A25
A26	P3E_CPU0_PCIE3_RX_DP<3>		P3E_CPU0_PCIE3_RX_DP<3>	A26
A27	P3E_CPU0_PCIE3_RX_DN<3>		P3E_CPU0_PCIE3_RX_DN<3>	A27
A28	GND		GND	A28

P1			P2	
PIN NUMBER	PIN DEFINE	WIRE TYPE	PIN DEFINE	PIN NUMBER
B1	P12V_OCP_V3_1		P12V_OCP_V3_1	B1
B2	P12V_OCP_V3_1		P12V_OCP_V3_1	B2
B3	P12V_OCP_V3_1		P12V_OCP_V3_1	B3
B4	P12V_OCP_V3_1		P12V_OCP_V3_1	B4
B5	P12V_OCP_V3_1		P12V_OCP_V3_1	B5
B6	P12V_OCP_V3_1		P12V_OCP_V3_1	B6
B7	OCP_V3_1_ISO_BIF0_N		OCP_V3_1_ISO_BIF0_N	B7
B8	OCP_V3_1_ISO_BIF1_N		OCP_V3_1_ISO_BIF1_N	B8
B9	OCP_V3_1_ISO_BIF2_N		OCP_V3_1_ISO_BIF2_N	B9
B10	RST_OCP_V3_1_A_0_N		RST_OCP_V3_1_A_0_N	B10
B11	P3V3_OCP_V3_1		P3V3_OCP_V3_1	B11
B12	OCP_V3_1_A_AUX_PWR_EN		OCP_V3_1_A_AUX_PWR_EN	B12
B13	GND		GND	B13
B14	CLK_100M_DB2001_OCP_V3_1_C_DN		CLK_100M_DB2001_OCP_V3_1_C_DN	B14
B15	CLK_100M_DB2001_OCP_V3_1_C_DP		CLK_100M_DB2001_OCP_V3_1_C_DP	B15
B16	GND		GND	B16
B17	P3E_CPU0_PCIE3_TX_C_DP<0>		P3E_CPU0_PCIE3_TX_C_DP<0>	B17
B18	P3E_CPU0_PCIE3_TX_C_DN<0>		P3E_CPU0_PCIE3_TX_C_DN<0>	B18
B19	GND		GND	B19
B20	P3E_CPU0_PCIE3_TX_C_DN<1>		P3E_CPU0_PCIE3_TX_C_DN<1>	B20
B21	P3E_CPU0_PCIE3_TX_C_DP<1>		P3E_CPU0_PCIE3_TX_C_DP<1>	B21
B22	GND		GND	B22
B23	P3E_CPU0_PCIE3_TX_C_DN<2>		P3E_CPU0_PCIE3_TX_C_DN<2>	B23
B24	P3E_CPU0_PCIE3_TX_C_DP<2>		P3E_CPU0_PCIE3_TX_C_DP<2>	B24
B25	GND		GND	B25
B26	P3E_CPU0_PCIE3_TX_C_DP<3>		P3E_CPU0_PCIE3_TX_C_DP<3>	B26
B27	P3E_CPU0_PCIE3_TX_C_DN<3>		P3E_CPU0_PCIE3_TX_C_DN<3>	B27
B28	GND		GND	B28

WIRE DIAGRAM

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DIMENSIONS: mm

TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ± –
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2 PLC ± –
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DWN GARNETT WU 25MAY2020
CHK – –
APVD DAVID ZHANG 25MAY2020
PRODUCT SPEC —
APPLICATION SPEC —
WEIGHT —

TE Connectivity Ltd.

NAME S;LIVER 2.0 1C LEAD FRAME TO SLIVER 2.0 1C 85OHM –

SIZE A2 | CAGE CODE 00779 | DRAWING NO C-2373744 | RESTRICTED TO —

CUSTOMER DRAWING | SCALE 1:1 | SHEET 2 OF 2 | REV A

2373744

1471-9 (1/15)

Mouser Electronics

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