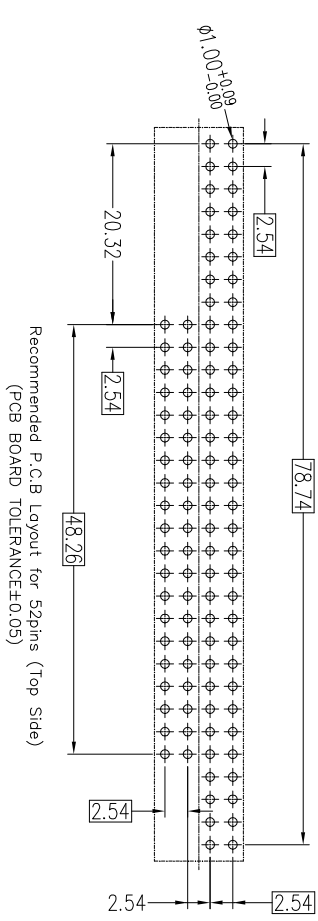
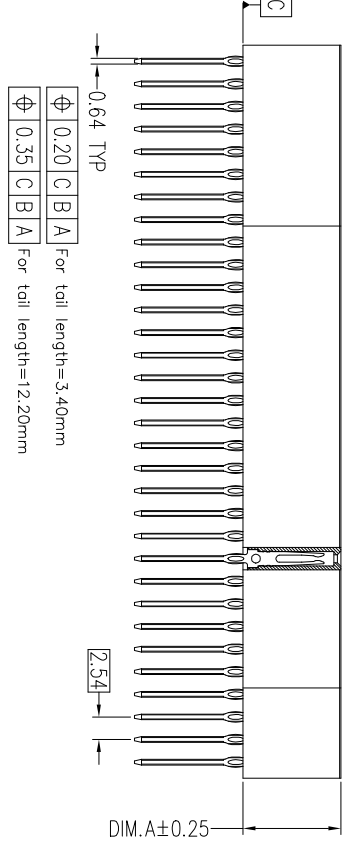
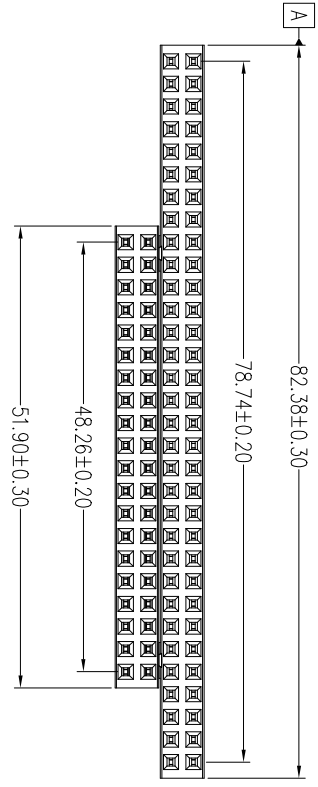
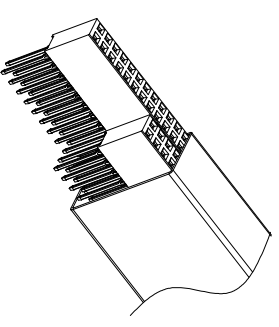
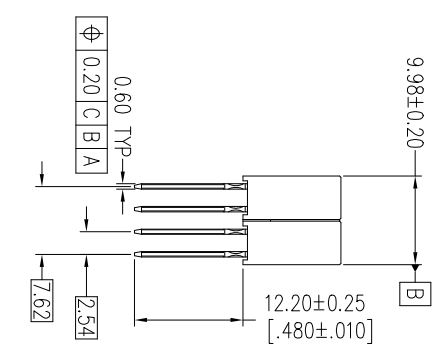


PRODUCT NUMBER
SEE PART NO. TABLE



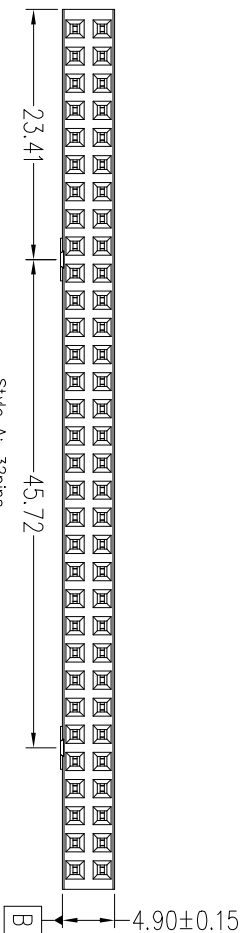
NOTES:

1. MATERIAL:
INSULATION MATERIAL: PPS+30%GF UL94V-0
CONTACT MATERIAL: PHOSPHER BRONZE
CONTACT PLATING: NICKEL UNDERPLATE,COLD REFER TO ORDERING CODE
 2. OPERATION TEMPERATURE: -55°C TO +105°C
 3. RECOMMEND PROCESS: PRESS FIT
 4. INSULATION RESISTANCE: 1000MΩ AT 500V/MIN
 5. WITHSTAND VOLTAGE: 1000V AC/DC
 6. RATED VOLTAGE: 12V DC, RATED CURRENT: 2AMP
 7. CONTACT RESISTANCE: 30mΩ MAX
 8. PRODUCT SPEC: GS-12-1542, PACKING SPEC: GS-14-2678
 9. ORDERING CODE: 10153302-XX X X X LF
- NO. OF PINS
20 = 20 pins
32 = 32 pins
52 = 32+20 pins
- LEAD FREE
PACKAGE
1: TUBE
- CONTACT FINISH
1: GOLD FLASH OVER NICKEL
2: 10U GOLD AT MATING AREA+3u" Gold AT TAIL
- LENGTH OF TAIL
1: 12.20mm
2: 3.40mm

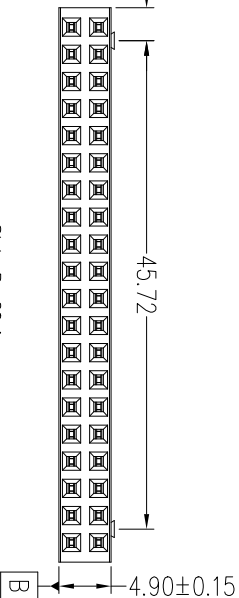


3D View for 52pins
After packing

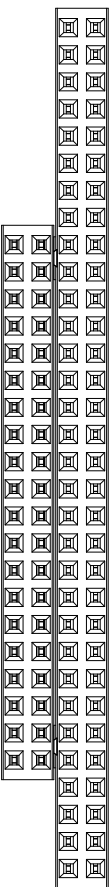
mat'l. code	ecn no	dr	date	surface	tolerance	projection	title
A	New Release	JASON	2019-07-15	ISO 1302	ISO 406 ISO	1109	2.54pitch PC104 2x20P+2x32P Vertical PF
				tolerances unless otherwise specified	.XX±.25	mm	
				angles	.XXX±.15	mm	
				3±x°			
				dr	Jason Wang	2019-06-14	
				engr	Jason Wang	2019-07-15	
				chr	-	-	
				qppd	TIM YAO	2019-07-15	
sheet index	revision	A					
	sheet	1					



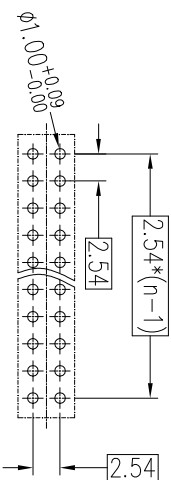
Style A: 32pins



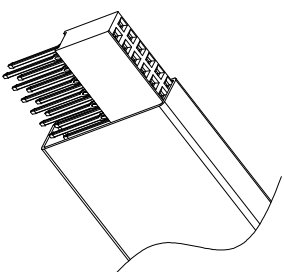
Style B: 20pins



Style C: 52pins
Assembled by 32pins and 20pins



Recommended PCB Layout for 32pins & 20pins (Top Side)
(PCB BOARD TOLERANCE±0.05)



3D View for 32pins & 20pins
After packing

mat'l. code	surface	tolerance	projection	product family
tr ecn no	ISO 1302	ISO 406 ISO	110°	PC104
dr	tolerances unless otherwise specified			
date	angles	.XX±.40	mm	title
2019-07-15	linear	.XXX±.25	Scale 1:1	2.54pitch PC104 2x20P+2x32P Vertical PF
A New Release	3±x'	.XXX±.15		dwg no
	dr			10153302
	engr			sheet 2 of 2 size
	chr			A3
	qppd			type Product Customer Drawing
sheet	revision			
index	sheet			