ΡΛΝ	ĴΪΤ
	SEMI CONDUCTOR

30V P-Channel Enhancement Mode MOSFET

Current

Voltage

-35 A

Features

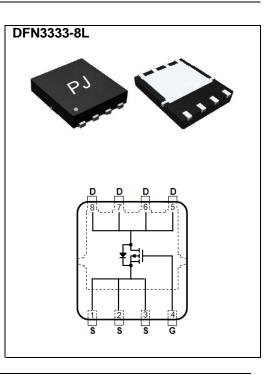
• R_{DS(ON)}, V_{GS}@-10V,I_D@-10A<15.5mΩ

-30 V

- R_{DS(ON)}, V_{GS}@-4.5V,I_D@-6A<23mΩ
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: DFN3333-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.001 ounces, 0.03 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMET	ER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _C =25°C		-35	А	
	T _C =100°C	Ι _D	-22		
Pulsed Drain Current (Note 1)	T _C =25°C	I _{DM}	-140		
Power Dissipation	T _C =25°C	D-	30	W	
	T _c =100°C	PD	11		
Continuous Drain Current	T _A =25°C		-9.8	A	
	T _A =70°C	ID	-7.8		
Power Dissipation	T _A =25°C	5	2.0	14/	
Power Dissipation	T _A =70°C	PD	1.3	W	
Operating Junction and Storage	e Temperature Range	T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{ extsf{ heta}JC}$	4.2	°C/W	
	Junction to Ambient	R _{θJA}	62.5		

Limited only By Maximum Junction Temperature

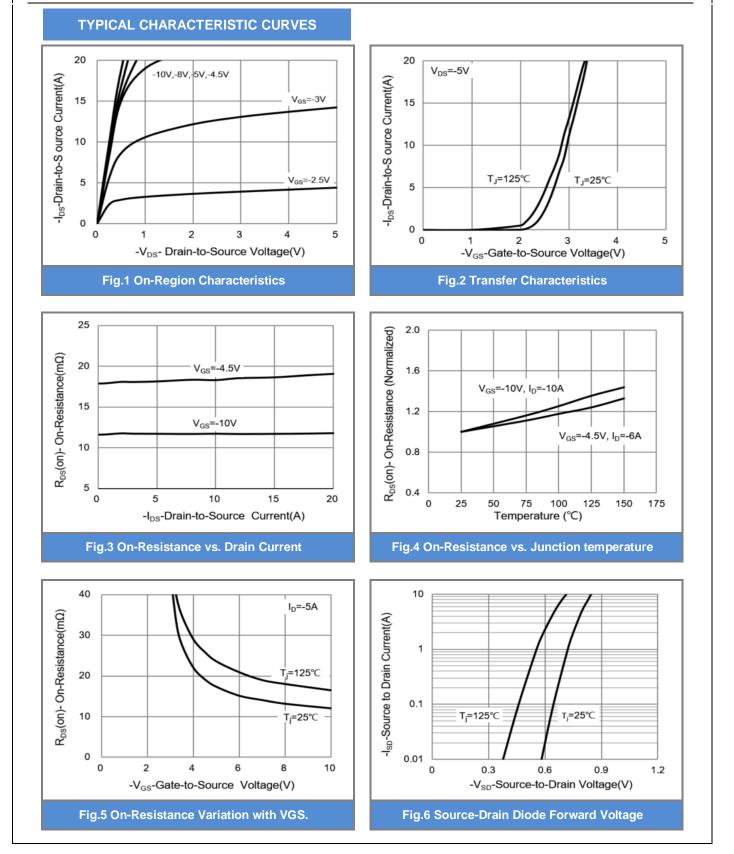


Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

DADAMETED		TEAT CONDITION		TVD		
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		I	1	1		1
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V,I _D =-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-1.0	-1.6	-2.5	v
Drain-Source On-State Resistance	5	V _{GS} =-10V,I _D =-10A	-	12	15.5	mΩ
	$R_{DS(on)}$	V _{GS} =-4.5V,I _D =-6A	-	18	23	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V,V _{GS} =0V	-	-	-1.0	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Qg	V_{DS} =-15V, I _D =-8A, V_{GS} =-4.5V ^(Note 1,2)	-	15	-	nC
Gate-Source Charge	Q_{gs}		-	4	-	
Gate-Drain Charge	Q_gd		-	6	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	-	1730	-	pF
Output Capacitance	Coss		-	180	-	
Reverse Transfer Capacitance	Crss		-	125	-	
Turn-On Delay Time	td _(on)	V_{DD} =-15V, I _D =-1A, V _{GS} =-10V, R _G =6Ω (Note 1,2)	-	9	-	
Turn-On Rise Time	tr		-	22	-	ns
Turn-Off Delay Time	td _(off)		-	60	-	
Turn-Off Fall Time	t _f		-	14	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	-35	A
Diode Forward Current	I _S					
Diode Forward Voltage	V _{SD}	I _S =-1A,V _{GS} =0V	-	-0.7	-1	V

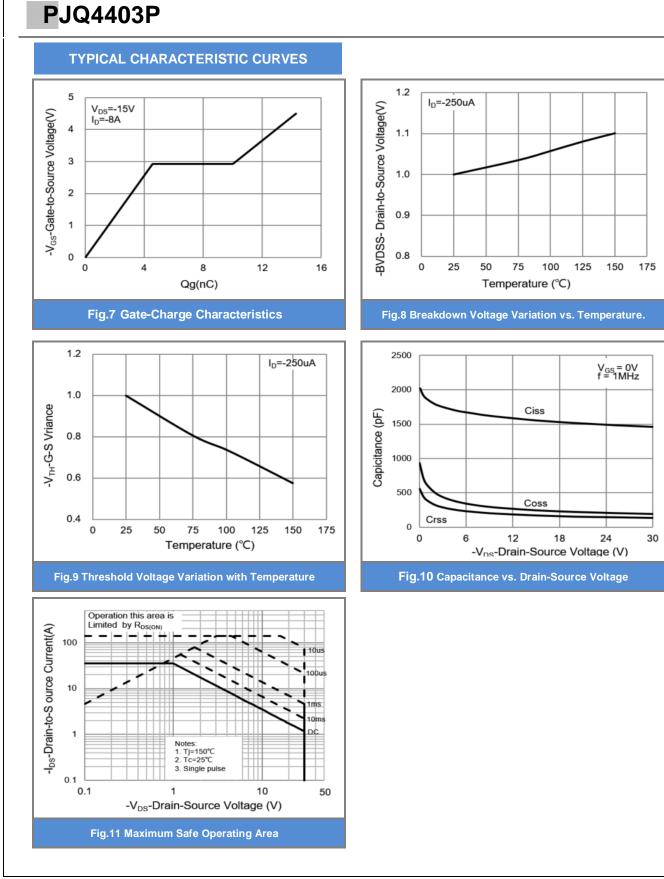
NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics
- Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited
- 5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper
- 6. Guaranteed by design, not subject to production testing.



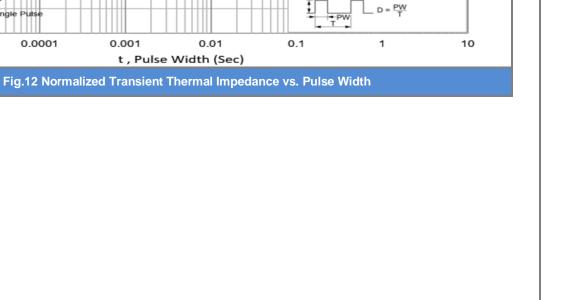












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 $\begin{array}{l} \mathsf{T}_{\mathsf{J},\mathsf{PK}} = \mathsf{Tc} + \mathsf{P}_{\mathsf{DM}} ^* \mathsf{Z}_{\mathsf{TH} \cdot \mathsf{JC}} ^* \mathsf{R}_{\mathsf{TH} \cdot \mathsf{JC}} \\ \mathsf{R}_{\mathsf{TH} \cdot \mathsf{JC}} = 4.2^\circ \mathsf{C} / \mathsf{W} \\ \mathsf{Tc} = 25^\circ \mathsf{C} \end{array}$

PJQ4403P

1

0.1

0.01 0.00001

D=0.5

0.2 0.1

0.05 0.0

single

0.0001

TYPICAL CHARACTERISTIC CURVES

PANJIT SEMI CONDUCTOR

 $Z_{TH \rightarrow JC}$ Normalized Transient Thermal Impedance



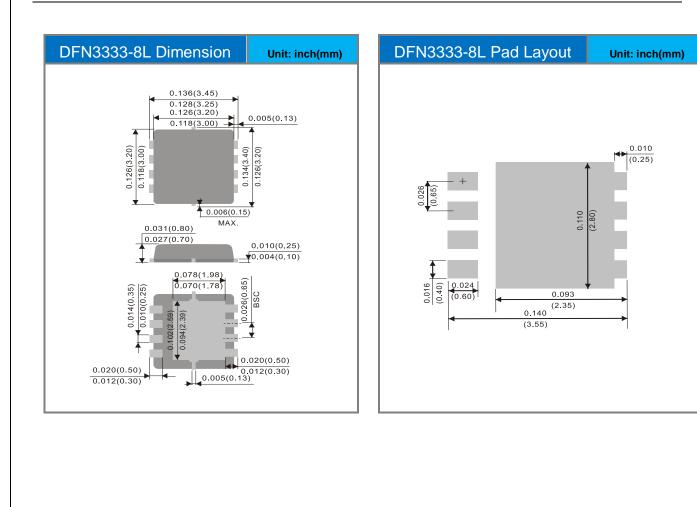




Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJQ4403P_R2_00001	DFN3333-8L	5K pcs / 13" reel	4403	Halogen free

Packaging Information & Mounting Pad Layout





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