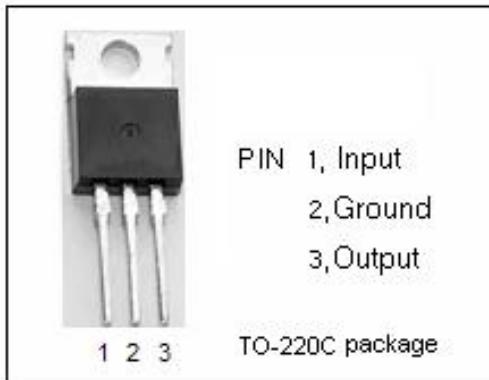


isc Three Terminal Positive Voltage Regulator

LM7824

FEATURES

- Output current in excess of 1.5A
- Output voltage of 24V
- Internal thermal overload protection
- Short circuit protection
- Output transition Safe-Area compensation
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

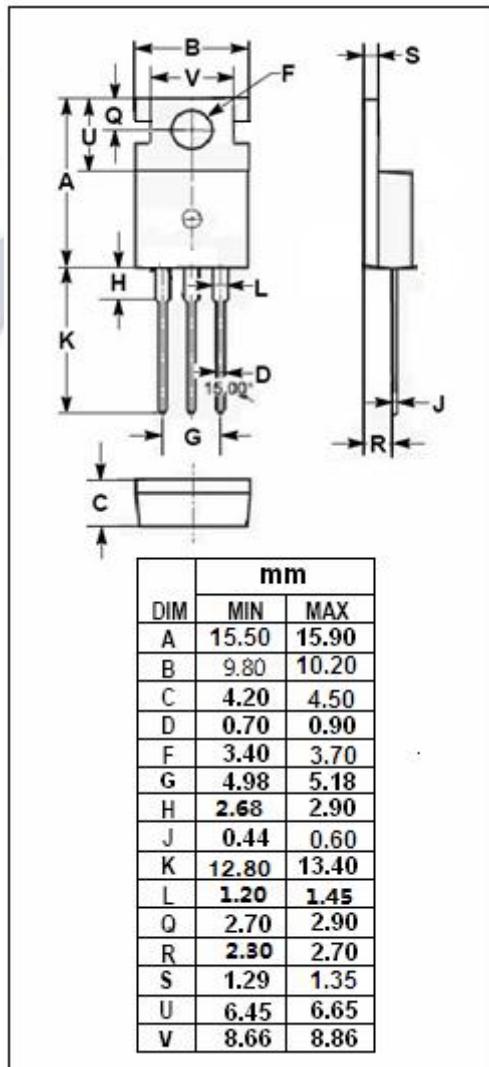


ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	RATING	UNIT
V_i	DC input voltage	40	V
I_o	Output current	internally limited	
P_{tot}	Power dissipation	internally limited	
T_{OP}	Operating junction temperature	0~150	°C
T_{stg}	Storage temperature	-55~150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th j-c}$	Thermal Resistance, Junction to Case	3	°C/W
$R_{th j-a}$	Thermal Resistance, Junction to Ambient	50	°C/W



isc Three Terminal Positive Voltage Regulator**LM7824****• ELECTRICAL CHARACTERISTICS** $T_j=25^\circ\text{C}$ ($V_i=33\text{V}$, $I_o=0.5\text{A}$, $C_i=0.33\text{\mu F}$, $C_o=0.1\text{\mu F}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_o	Output Voltage	$V_{in}=33\text{V}$; $I_o=500\text{mA}$	23	25	V
ΔV_v	Line Regulation	$27\text{V} \leq V_{in} \leq 38\text{V}$; $I_o=500\text{mA}$		240	mV
ΔV_i	Load Regulation	$5.0\text{mA} \leq I_o \leq 1.5\text{A}$; $V_{in}=33\text{V}$		240	mV
I_b	Quiescent Current	$V_{in}=33\text{V}$; $I_o=1.0\text{A}$		8.0	mA
Δb_1	Quiescent Current Change	$5.0\text{mA} \leq I_o \leq 1.0\text{A}$; $V_{in}=33\text{V}$		0.5	mA
Δb_2	Quiescent Current Change	$28\text{V} \leq V_{in} \leq 38\text{V}$; $I_o=500\text{mA}$		1.0	mA