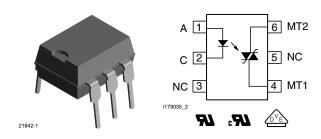


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Optocoupler, Phototriac Output, High dV/dt, Low Input Current



DESCRIPTION

The VO4257 and VO4258 phototriac consists of a GaAs IRLED optically coupled to a photosensitive non-zero crossing TRIAC packaged in a DIP-6 package.

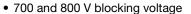
High input sensitivity is achieved by using an emitter follower phototransistor and a cascaded SCR predriver resulting in an LED trigger current of 1.6 mA for bin D, 2 mA for bin H, and 3 mA for bin M.

The new non zero phototriac family use a proprietary dV/dt clamp resulting in a static dV/dt of greater than 5 kV/µs.

The VO4257, VO4258 phototriac isolates low-voltage logic from 120 VAC, 240 VAC, and 380 VAC lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

FEATURES

- High static dV/dt 5 kV/µs
- High input sensitivity I_{FT} = 1.6 mA, 2 mA, and 3 mA



- 300 mA on-state current
- Isolation rated voltage 4420 V_{RMS}
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

Pb-free



RoHS COMPLIANT

APPLICATIONS

- · Solid-state relays
- · Industrial controls
- Office equipment
- Consumer appliances

AGENCY APPROVALS

- UL1577, file no. E52744, double protection
- cUL file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1

ORDERING INFORMATION							
V O 4 2 5 # X - X O O # T TAPE Option 7						Option 6	
PAR	PART NUMBER PACKAGE OPTION AND REEL						
AGENCY			V _{DRM} 800				
CERTIFIED/PACKAGE	TRIGGER CURRENT, I _{FT} (mA)						
UL, cUL, BSI, FIMKO	1.6	2	3	1.6	2	3	
DIP-6	VO4257D	VO4257H	VO4257M	VO4258D	VO4258H	VO4258M	
DIP-6, 400 mil, option 6	VO4257D-X006	VO4257H-X006	VO4257M-X006	VO4258D-X006	VO4258H-X006	VO4258M-X006	
SMD-6, option 7	VO4257D-X007T	VO4257H-X007T	VO4257M-X007T	VO4258D-X007T	VO4258H-X007T	VO4258M-X007T	
VDE, UL, cUL, BSI, FIMKO	1.6	2	3	1.6	2	3	
SMD-6, option 7	-	-	-	-	VO4258H-X017T	-	



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT			
INPUT								
Reverse voltage			V_{R}	6	V			
Forward current			I _F	60	mA			
Derate from 25 °C				1.33	mW/°C			
OUTPUT								
Peak off-state voltage		VO4257D/H/M	V_{DRM}	700	V			
reak on-state voltage		VO4258D/H/M	V_{DRM}	800	V			
RMS on-state current			I _{TM}	300	mA			
Derate from 25 °C				6.6	mW/°C			
COUPLER								
Storage temperature range			T _{stg}	-55 to +150	°C			
Ambient temperature range			T _{amb}	-55 to +100	°C			
Soldering temperature	Max. ≤ 10 s dip soldering ≥ 0.5 mm from case bottom		T _{sld}	260	°C			

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

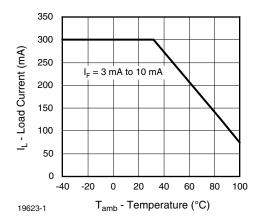


Fig. 1 - Recommended Operating Condition



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THERMAL CHARACTERISTICS			
PARAMETER	SYMBOL	VALUE	UNIT
LED power dissipation	P _{diss}	100	mW
Output power dissipation	P _{diss}	500	mW
Total power dissipation	P _{tot}	600	mW
Maximum LED junction temperature	T _{jmax.}	125	°C
Maximum output die junction temperature	T _{jmax.}	125	°C
Thermal resistance, junction emitter to board	θ_{JEB}	150	°C/W
Thermal resistance, junction emitter to case	θ_{JEC}	139	°C/W
Thermal resistance, junction detector to board	θ_{JDB}	78	°C/W
Thermal resistance, junction detector to case	θ_{JDC}	103	°C/W
Thermal resistance, junction emitter to junction detector	θ_{JED}	496	°C/W
Thermal resistance, case to ambient	θ_{CA}	3563	°C/W

Note

• The thermal characteristics table above were measured at 25 °C and the thermal model is represented in the thermal network below. Each resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	I _F = 10 mA		V_{F}		1.2	1.4	V		
Reverse current	V _R = 6 V		I _R		0.1	10	μA		
Input capacitance	$V_F = 0 V$, $f = 1 MHz$		C _I		40		pF		
OUTPUT	OUTPUT								
Repetitive peak off-state voltage	I – 100 uA	VO4257D/H/M V _{DRM} 700				V			
	$I_{DRM} = 100 \mu A$	VO4258D/H/M	V_{DRM}	800			V		
Off-state current	$V_D = V_{DRM}$		I _{DRM}			100	μΑ		
On-state voltage	$I_T = 300 \text{ mA}$		V_{TM}			3	V		
On-current	$PF = 1, V_{T(RMS)} = 1.7 V$		I _{TM}			300	mA		
Critical state of rise of off-state voltage	V _D = 0.67 V _{DRM} , T _J = 25 °C		dV/dt _{cr}	5000			V/µs		
COUPLER									
LED trigger current, current required to latch output		VO4257D	I _{FT}			1.6	mA		
		VO4257H	I _{FT}	MIN. TYP. 1.2 0.1 40 700 800		2	mA		
	V - 2 V	VO4257M	I _{FT}			3	mA		
	$V_D = 3 V$	VO4258D	I _{FT}			1.6	mA		
		VO4258H	I _{FT}			2	mA		
		VO4258M	I _{FT}			3	mA		
Capacitance (input to output)	f = 1 MHz, V _{IO} = 0 V		C _{IO}		0.8		pF		

Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements

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SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION SYMBO		VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 100 / 21				
Comparative tracking index		CTI	175				
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	4420	V _{RMS}			
Maximum transient isolation voltage		V _{IOTM}	8000	V _{peak}			
Maximum repetitive peak isolation voltage		V _{IORM}	890	V _{peak}			
Leafalte a metala con	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω			
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω			
Output safety power		P _{SO}	500	mW			
Input safety current		I _{SI}	250	mA			
Safety temperature		T _S	175	°C			
Creepage distance			≥ 7	mm			
Clearance distance			≥ 7	mm			
Insulation thickness		DTI	≥ 0.4	mm			
Pollution degree (DIN VDE 0109)			2				

Note

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

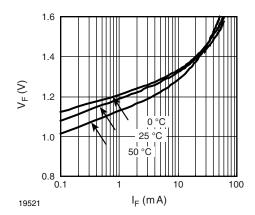


Fig. 2 - Diode Forward Voltage vs. Forward Current

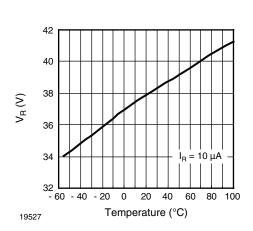


Fig. 3 - Diode Reverse Voltage vs. Temperature

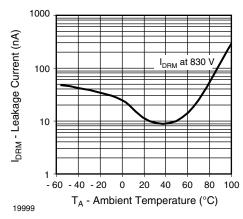


Fig. 4 - Leakage Current vs. Ambient Temperature

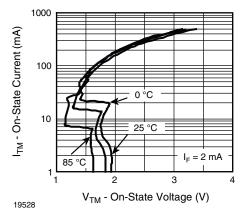


Fig. 5 - Output On Current (I_{TM}) vs. Voltage

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits

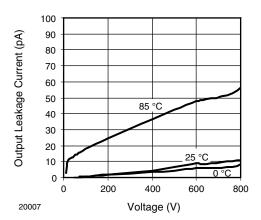


Fig. 6 - Output Off Current (Leakage) vs. Voltage

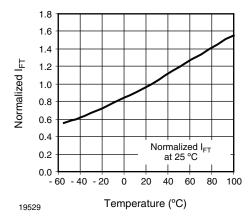


Fig. 7 - Normalized Trigger Input Current vs. Temperature

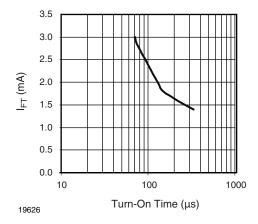


Fig. 8 - Trigger Current vs. Turn-On Time

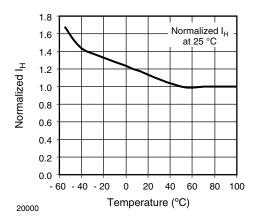


Fig. 9 - Normalized Holding Current vs. Temperature

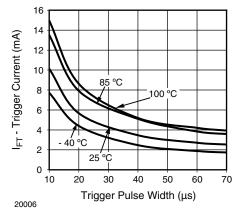
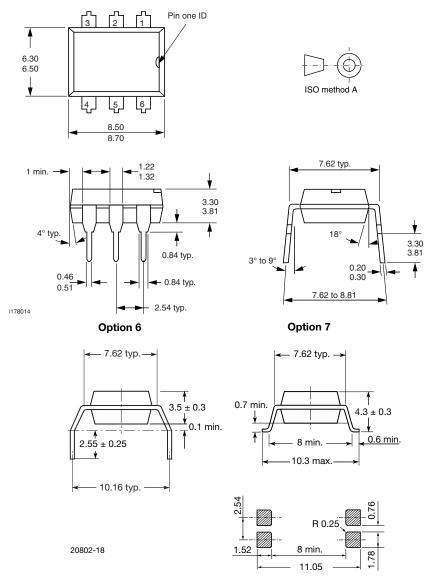


Fig. 10 - I_{FT} vs. LED Pulse Width



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PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING (example)



Note

• VDE logo is only marked on option 1 parts. Tape and reel suffix (T) is not part of the package marking



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