

Nch 600V 7A Power MOSFET

V _{DSS}	600V
R _{DS(on)} (Max.)	0.62Ω
I _D	±7A
P _D	46W

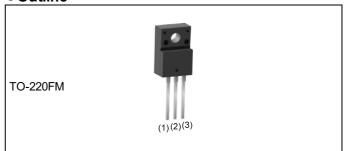
● Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V_{GSS}) guaranteed to be ±20V.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating; RoHS compliant

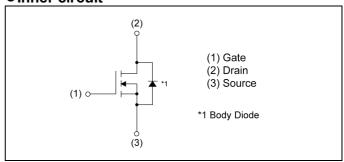
Application

Switching

Outline



•Inner circuit



Packaging specifications

	Packing	Bulk
	Reel size (mm)	1
T	Tape width (mm)	-
Туре	Quantity (pcs)	500
	Taping code	-
	Marking	R6007ENX

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	600	V
Continuous dusin suument	T _C = 25°C	I _D *1	±7	Α
Continuous drain current	T _C = 100°C	I _D *1	±3.8	Α
Pulsed drain current	•	I _{DP} *2	±14	Α
Cata Source voltage	static	V	±20	V
Gate - Source voltage	AC(f>1Hz)	V_{GSS}	±30	V
Avalanche current, repetitive		I _{AR}	1.3	Α
Avalanche energy, single pulse		E _{AS} *3	133	mJ
Avalanche energy, repetitive		E _{AR} *3	0.2	mJ
Power dissipation (T _C = 25°C)		P _D *4	46	W
Junction temperature	T _j	150	°C	
Operating junction and storage temp	perature range	T _{stg}	-55 ~ +150	°C

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Conditions	Values	Unit
Reverse diode dv/dt	dv/dt	-	15	V/ns
Drain - Source voltage slope	dv/dt	$V_{DS} = 480V, T_j = 25^{\circ}C$	50	V/ns

●Thermal resistance

Doromotor	Cumb of	Values			Lleit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	2.7	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	70	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	1	-	265	°C

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai netei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	-	V
		V _{DS} = 600V, V _{GS} = 0V				
Zero gate voltage drain current	I _{DSS}	T _j = 25°C	-	0.1	100	μA
didiri odriorit		T _j = 125°C	ı	1	1000	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA	2	-	4	V
		V _{GS} = 10V, I _D = 2.4A				
Static drain - source on - state resistance	R _{DS(on)} *5	T _j = 25°C	-	0.57	0.62	Ω
		T _j = 125°C	-	1.20	-	
Gate resistance	R _G	f =1MHz, open drain	-	10.6	-	Ω

● Electrical characteristics (T_a = 25°C)

Daramatar	Cymahal	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Transfer Admittance	Y _{fs} *5	V _{DS} = 10V, I _D = 3.5A	1.9	3.8	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	390	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	390	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	50	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V	-	21	-	
Effective output capacitance, time related	C _{o(tr)}	V _{DS} = 0V to 480V	-	88	-	pF
Turn - on delay time	t _{d(on)} *5	V _{DD} ≃ 300V,V _{GS} = 10V	-	25	-	
Rise time	t _r *5	I _D = 3.5A	-	25	-	
Turn - off delay time	t _{d(off)} *5	R _L ≃ 86.6Ω		70		ns
Fall time	t _f *5	$R_G = 10\Omega$	-	35	-	

● Gate charge characteristics (T_a = 25°C)

Darameter	Symbol	Conditions	Values			l limit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*5}	V _{DD} ≈ 300V,	-	20	-	
Gate - Source charge	Q _{gs} *5	I _D = 7.0A,	-	3	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 10V	-	11	-	
Gate plateau voltage	V _(plateau)	V _{DD} = 300V, I _D = 7A	-	6.2	-	V

^{*1} Limited only by maximum channel temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \doteqdot 100mH, V_{DD}=50V, R_G=25 Ω , STARTING T_j=25 $^{\circ}$ C

^{*4} T_C=25°C

^{*5} Pulsed

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Urill	
Continuous forward current	I _S *1	T _c = 25°C	1	ı	7	А	
Pulse forward current	l _{SP} *2	1 _c - 25 C	-	-	14	Α	
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 7.0A	-	-	1.5	V	
Reverse recovery time	t _{rr} *5	74.1/	1	340	1	ns	
Reverse recovery charge	Q_{rr}^{*5}	I _S = 7A, V _{GS} =0V di/dt = 100A/µs	-	2.8	-	μC	
Peak reverse recovery current	I _{mm} *5	απατ 100/ υμο	-	17	-	Α	

Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	0.385	_	C _{th1}	0.00128	
R _{th2}	1.24	K/W	C _{th2}	0.013	Ws/K
R _{th3}	2.2		C _{th3}	0.448	

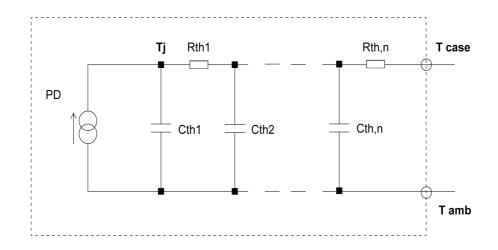


Fig.1 Power Dissipation Derating Curve

120

100

80

40

20

0

50

100

150

200

Junction Temperature : T_i [°C]

Fig.2 Normalized Transient Thermal Resistance vs. Pulse Width

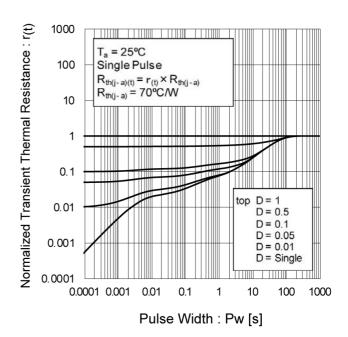
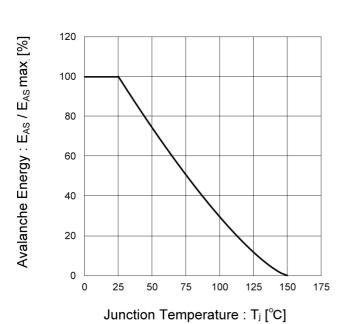


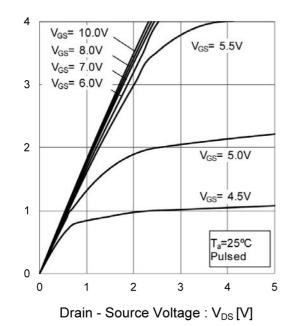
Fig.3 Avalanche Energy Derating
Curve vs. Junction Temperature



Drain Current : I_D [A]

• Electrical characteristic curves

Fig.4 Typical Output Characteristics(I)



Drain Current : I_D [A]

Fig.5 Typical Output Characteristics(II)

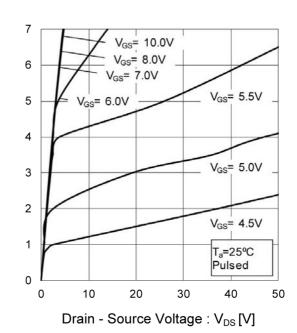


Fig.6 Tj = 150°C Typical Output Characteristics (I)

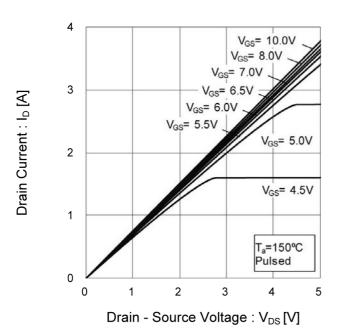
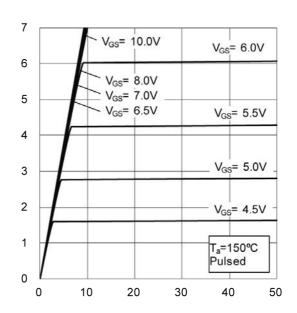


Fig.7 Tj = 150°C Typical Output Characteristics (II)



Drain Current : I_D [A]

Fig.8 Breakdown Voltage vs. Junction Temperature

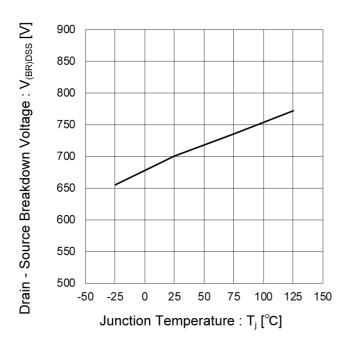


Fig.9 Typical Transfer Characteristics

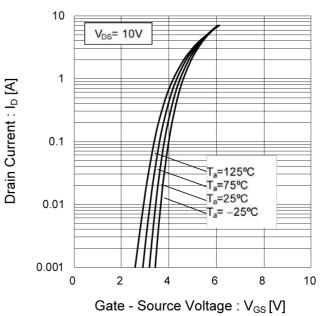


Fig.10 Gate Threshold Voltage vs.
Junction Temperature

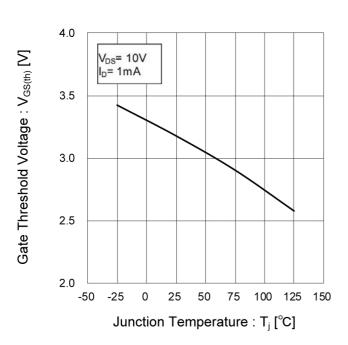


Fig.11 Forward Transfer Admittance vs.
Drain Current

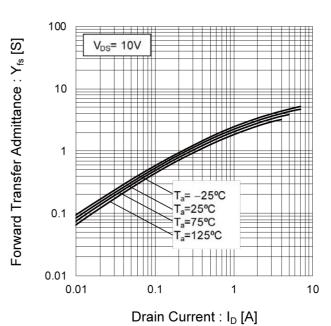


Fig.12 Static Drain - Source On - State
Resistance vs. Gate Source Voltage

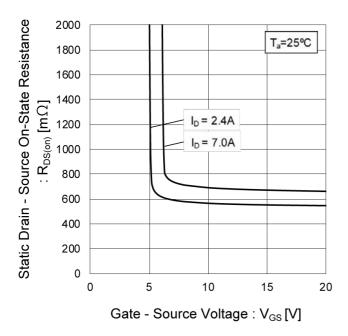


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

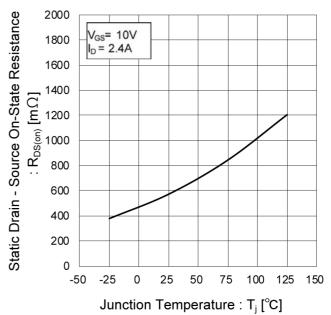


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

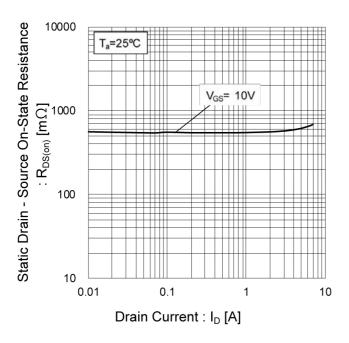


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

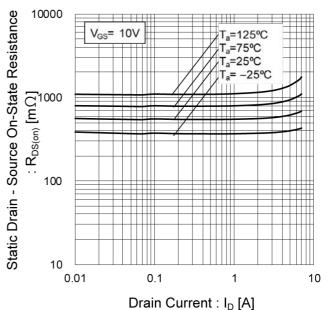


Fig.16 Typical Capacitance vs.
Drain - Source Voltage

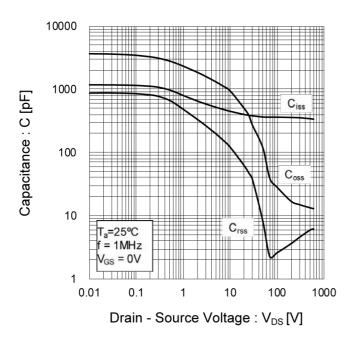


Fig.17 Coss Stored Energy

Coss Stored Energy : E_{oss} [uJ]

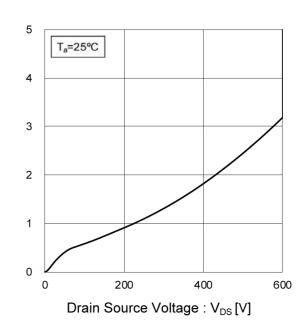


Fig.18 Switching Characteristics

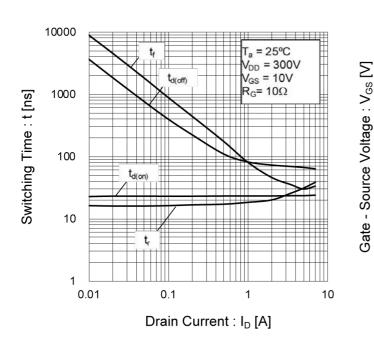


Fig.19 Dynamic Input Characteristics

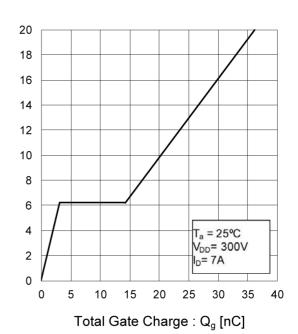


Fig.20 Inverse Diode Forward Current vs. Source - Drain Voltage

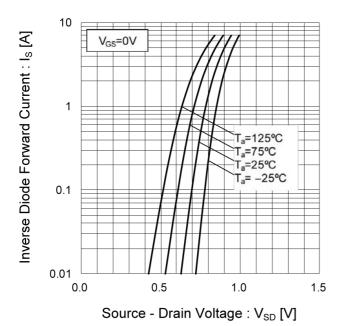
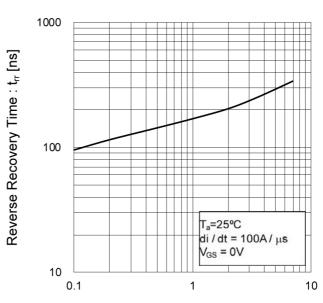


Fig.21 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

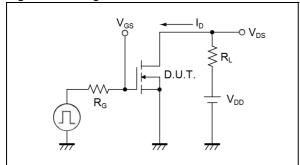


Fig.2-1 Gate Charge Measurement Circuit

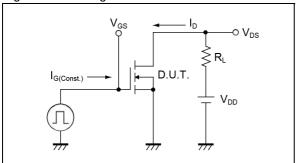


Fig.3-1 Avalanche Measurement Circuit

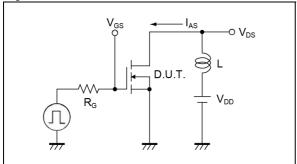


Fig.4-1 dv/dt Measurement Circuit

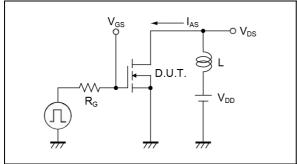


Fig.5-1 dv/dt Measurement Circuit

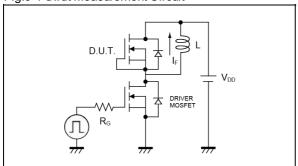


Fig.1-2 Switching Waveforms

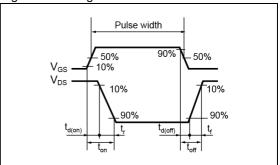


Fig.2-2 Gate Charge Waveform

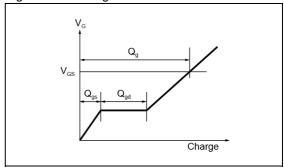


Fig.3-2 Avalanche Waveform

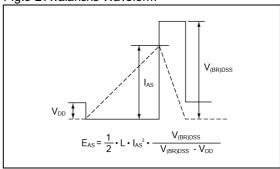


Fig.4-2 dv/dt Waveform

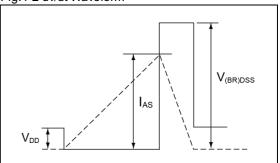
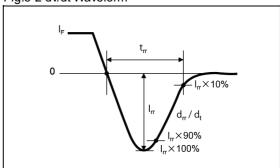
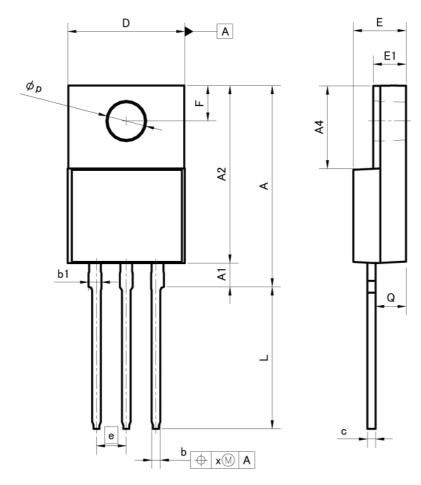


Fig.5-2 dv/dt Waveform



Dimensions

TO-220FM



DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	16.60	17.60	0.654	0.693
A1	1.80	2.20	0.071	0.087
A2	14.80	15.40	0.583	0.606
A4	6.80	7.20	0.268	0.283
b	0.70	0.90	0.028	0.035
b1	1.10	1.50	0.043	0.059
С	0.70	0.85	0.028	0.033
D	9.90	10.30	0.390	0.406
E	4.40	4.80	0.173	0.189
е	2.	2.54		00
E1	2.70	3.00	0.106	0.118
F	2.80	3.20	0.110	0.126
L	11.50	12.50	0.453	0.492
р	3.00	3.40	0.118	0.134
Q	2.10	3.10	0.083	0.122
×	9 _	0.38	<u>=</u>	0.015

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
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CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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