THYRISTORS 5P4M,5P6M

5 A (8 Ar.m.s.) THYRISTOR

The 5P4M and 5P6M are a P gate all diffused mold type Thyristor <R> granted 5 A On-state Average Current (Tc = 103°C).

FEATURES

- · Easy installation by TO-220AB package.
- 80 A surge current.

NEC

High Voltage.

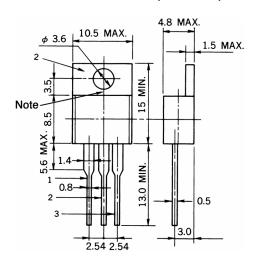
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- : VDRM, VRRM = 400 V (5P4M)
- : VDRM, VRRM = 600 V (5P6M)

APPLICATIONS

- · Motor speed control for household appliance.
- Temperature control for heater and constant temperature box.
- Constant voltage power source and battery charger.
- Automotive application such as regulator.
- · Various solid state relay etc.

PACKAGE DRAWING (Unit: mm)



Pin Connection

- 1. Cathode
- 2. Anode
- 3. Gate

Standard weight: 2 g

Note Tc test point

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The mark <R> shows major revised points.

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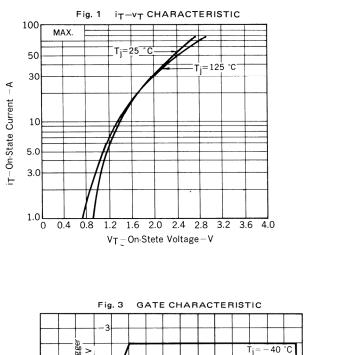
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

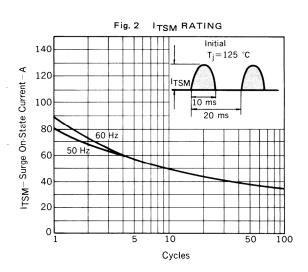
<R> MAXIMUM RATINGS

CHARACTERISTICS	SYMBOL	5P4M	5P6M		REMARK
Non-repetitive Peak Reverse Voltage	VRSM	500	500 700		-
Non-repetitive Peak Off-state Voltage	VDSM	500	700		-
Repetitive Peak Reverse Voltage	VRRM	400	600	V	-
Repetitive Peak Off-state Voltage	VDRM	400	600	V	-
Average On-state Current	IT(AV)	5 (Tc = 103°C, θ= 180°,	А	See Fig. 5	
Effective On-state Current	IT(RMS)	ξ	А		
Surge On-state Current	Ітѕм	80 (f = 50 Hz, sine	А	See Fig. 2	
		88 (f = 60 Hz, sine			
Fusing Current	∫i⊤²dt	28 (1 ms ≤	A ² s	-	
Critical Rate Rise of On-state Current	dl⊤/dt	5	A/µs	-	
Peak Gate Power Dissipation	Рсм	5 (f \ge 50 Hz,	W	See Fig. 3	
Average Gate Power Dissipation	P _{G(AV)}	0	W		
Peak Gate Forward Current	IFGM	2 (f \ge 50 Hz,	А	-	
Peak Gate Reverse Voltage	Vrgm	1	V	_	
Junction Temperature	Tj	–40 to	°C	_	
Storage Temperature	Tstg	–55 to	°C	_	

<R> ELECTRICAL CHARACTERISTICS (Tj = 25°C)

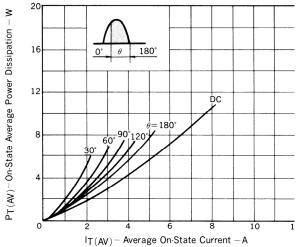
CHARACTERISTICS	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	REMARK
Repetitive Peak Reverse Current	IRRM	V _{RM} = V _{RRM}	Tj = 25°C	_	_	100	μA	-
			Tj = 125°C	_	_	2	mA	-
Repetitive Peak Off-state Current	Idrm	V _{DM} = V _{DRM}	Tj = 25°C	_	_	100	μA	_
			Tj = 125°C	_	_	2	mA	_
Critical Rate Rise of Off-state Voltage	dV⊳/dt	V _{DM} = 2/3 V _{DRM} , T _j = 125°C		-	40	-	V/µs	-
On-state Voltage	Vtm	I _{TM} = 10 A		-	-	1.4	V	See Fig. 1
Gate-trigger Current	Іст	V _{DM} = 6 V, R _L = 100 Ω		-	-	10	mA	See Fig. 3
Gate-trigger Voltage	Vgt	V _{DM} = 6 V, R _L = 100 Ω		-	-	1.5	V	
Gate Non-trigger Voltage	Vgd	V _{DM} = 1/2 V _{DRM} , T _j = 125°C		0.2	-	_	V	
Holding Current	Ін	V _{DM} = 24 V, I _{TM} = 10 A		-	6	-	mA	-
Circuit Commuted Turn-off Time	tq	$I_{TM} = 5 \text{ A}, V_{\text{R}} \geq 25 \text{ V}$		-	50	-	μS	-
V _{DM} = 2/3 V _{DRM} , diR/dt = 15 A/µ		t = 15 A/ <i>μ</i> s						
		dV _D /dt = 10 V/µs, T _j = 125°C						
Thermal Resistance	Rth(j-c)	Junction to case DC		-	-	3	°C/W	See Fig. 7
	Rth(j-a)	Junction to ambient DC		-	-	65	°C/W	

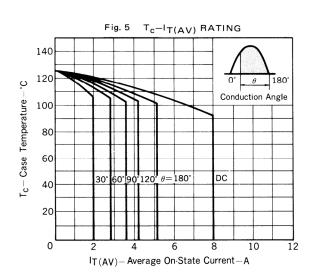


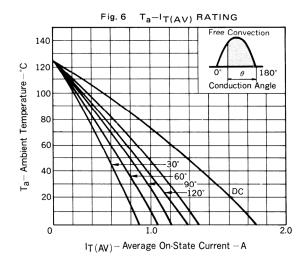


Gate Trigge Ťj= 0°C Voltage VFG – Gate Forward Voltage – V 25 °C 10 125 °C 5 8 20 25 10 15 IGT - Gate Trigger 6 PGM=5 W Current - mA f=50⁻Hz⁻ 4 Duty≦10–% -PG(AV) = 0.5 W2 Tji 40 to +125 °C 0 1.0 2.0 IFG-Gate Forward Current - A

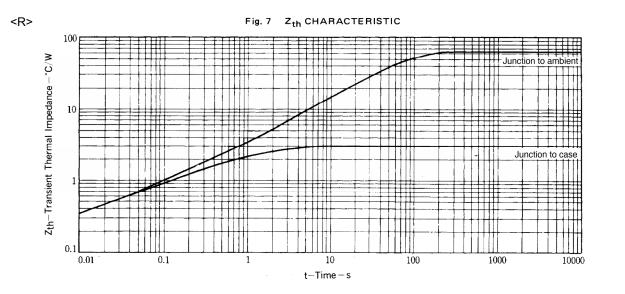








TYPICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)



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