BCM857BV; BCM857BS; BCM857DS

PNP/PNP matched double transistors

Rev. 06 — 28 August 2009

Product data sheet

1. Product profile

1.1 General description

PNP/PNP matched double transistors in small Surface-Mounted Device (SMD) plastic packages. The transistors are fully isolated internally.

Table 1.	Product	overview

Type number	Package	Package		Matched version of
	Nexperia	JEITA	complement	
BCM857BV	SOT666	-	BCM847BV	BC857BV
BCM857BS	SOT363	SC-88	BCM847BS	BC857BS
BCM857DS	SOT457	SC-74	BCM847DS	-

1.2 Features

- Current gain matching
- Base-emitter voltage matching
- Drop-in replacement for standard double transistors

1.3 Applications

- Current mirror
- Differential amplifier

1.4 Quick reference data

Table 2.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per trans	istor					
V _{CEO}	collector-emitter voltage	open base	-	-	-45	V
I _C	collector current		-	-	-100	mA
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -2 mA	200	290	450	



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Table 2.	Quick reference data	conunuea				
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per device						
h _{FE1} /h _{FE2}	h _{FE} matching	$V_{CE} = -5 V;$ $I_C = -2 mA$	<u>[1]</u> 0.9	1	-	
$V_{BE1} - V_{BE2}$	V _{BE} matching	$V_{CE} = -5 V;$ $I_C = -2 mA$	[2] _	-	2	mV

 Table 2.
 Quick reference data ...continued

 $\begin{tabular}{ll} [1] & The smaller of the two values is taken as the numerator. \end{tabular}$

[2] The smaller of the two values is subtracted from the larger value.

2. Pinning information

Table 3.	Pinning	
Pin	Description	Simplified outline Symbol
1	emitter TR1	
2	base TR1	
3	collector TR2	
4	emitter TR2	
5	base TR2	
6	collector TR1	001aab555
		sym018

3. Ordering information

BCM857BV-plastic surface-mounted package; 6 leadsSOTBCM857BSSC-88plastic surface-mounted package; 6 leadsSOT	Table 4. Ordering information						
BCM857BV-plastic surface-mounted package; 6 leadsSOTBCM857BSSC-88plastic surface-mounted package; 6 leadsSOT	Type number	Package	ackage				
BCM857BS SC-88 plastic surface-mounted package; 6 leads SOT		Name	Description	Version			
	BCM857BV	-	plastic surface-mounted package; 6 leads	SOT666			
	BCM857BS	SC-88	plastic surface-mounted package; 6 leads	SOT363			
BCM857DS SC-74 plastic surface-mounted package (TSOP6); 6 leads SO	BCM857DS	SC-74	plastic surface-mounted package (TSOP6); 6 leads	SOT457			

4. Marking

Table 5. Marking codes	
Type number	Marking code ^[1]
BCM857BV	3B
BCM857BS	A9*
BCM857DS	R8

[1] * = -: made in Hong Kong

- * = p: made in Hong Kong
- * = t: made in Malaysia
- * = W: made in China

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5. Limiting values

Symbol	Parameter	Conditions	Min	Max	Unit
Per transis	stor				
V _{CBO}	collector-base voltage	open emitter	-	-50	V
V _{CEO}	collector-emitter voltage	open base	-	-45	V
V _{EBO}	emitter-base voltage	open collector	-	-5	V
I _C	collector current		-	-100	mA
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-200	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
	SOT666		<u>[1][2]</u> _	200	mW
	SOT363		<u>[1]</u> -	200	mW
	SOT457		<u>[1]</u> _	250	mW
Per device)				
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
	SOT666		<u>[1][2]</u> _	300	mW
	SOT363		<u>[1]</u> _	300	mW
	SOT457		<u>[1]</u> -	380	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C
T _{stg}	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

6. Thermal characteristics

Table 7.	Thermal characteristics							
Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
Per transistor								
R _{th(j-a)}	thermal resistance from junction to ambient	in free air						
	SOT666		<u>[1][2]</u>	-	625	K/W		
	SOT363		<u>[1]</u> _	-	625	K/W		
	SOT457		<u>[1]</u> _	-	500	K/W		

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Table 7.	Thermal characteristics continued							
Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
Per device								
R _{th(j-a)}	thermal resistance from junction to ambient	in free air						
	SOT666		[1][2] _	-	416	K/W		
	SOT363		<u>[1]</u> -	-	416	K/W		
	SOT457		<u>[1]</u> -	-	328	K/W		

. .

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

7. **Characteristics**

Table 8. **Characteristics**

T_{amb} = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transis	stor					
I _{CBO}	collector-base cut-off current	V _{CB} = -30 V; I _E = 0 A	-	-	-15	nA
		V _{CB} = -30 V; I _E = 0 A; T _j = 150 °C	-	-	-5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 V;$ $I_C = 0 A$	-	-	-100	nA
h _{FE}	DC current gain	$V_{CE} = -5 V;$ $I_{C} = -10 \ \mu A$	-	250	-	
		$V_{CE} = -5 V;$ $I_{C} = -2 mA$	200	290	450	
V _{CEsat}	collector-emitter saturation voltage	$I_{C} = -10 \text{ mA};$ $I_{B} = -0.5 \text{ mA}$	-	-50	-200	mV
		l _C = -100 mA; l _B = -5 mA	-	-200	-400	mV
V _{BEsat}	base-emitter saturation voltage	$I_{C} = -10 \text{ mA};$ $I_{B} = -0.5 \text{ mA}$	<u>[1]</u> -	-760	-	mV
		I _C = -100 mA; I _B = -5 mA	<u>[1]</u> -	-920	-	mV
V _{BE}	base-emitter voltage	$V_{CE} = -5 V;$ $I_{C} = -2 mA$	<u>[2]</u> –600	-650	-700	mV
		$V_{CE} = -5 \text{ V};$ $I_C = -10 \text{ mA}$	[2] _	-	-760	mV
C _c	collector capacitance	$V_{CB} = -10 \text{ V};$ $I_E = i_e = 0 \text{ A};$ f = 1 MHz	-	-	2.2	pF
C _e	emitter capacitance	$V_{EB} = -0.5 \text{ V};$ $I_C = i_c = 0 \text{ A};$ f = 1 MHz	-	10	-	pF

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	Parameter	Conditions	Min	Тур	Max	Unit
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz	100	175	-	MHz
NF	noise figure	$V_{CE} = -5 V;$ $I_{C} = -0.2 mA;$ $R_{S} = 2 k\Omega;$ f = 10 Hz to 15.7 kHz	-	1.6	-	dB
	$V_{CE} = -5 V; \\ I_{C} = -0.2 \text{ mA}; \\ R_{S} = 2 \text{ k}\Omega; \\ f = 1 \text{ kHz}; \\ B = 200 \text{ Hz}$	-	3.1	-	dB	
Per device						
h _{FE1} /h _{FE2}	h _{FE} matching	$V_{CE} = -5 V;$ $I_{C} = -2 mA$	<u>[3]</u> 0.9	1	-	
$V_{BE1} - V_{BE2}$	V _{BE} matching	$V_{CE} = -5 V;$ $I_{C} = -2 mA$	<u>[4]</u> _	-	2	mV

Table 8.Characteristics ... continued $T_{amb} = 25 \,^{\circ}C$ unless otherwise specified

[1] V_{BEsat} decreases by about 1.7 mV/K with increasing temperature.

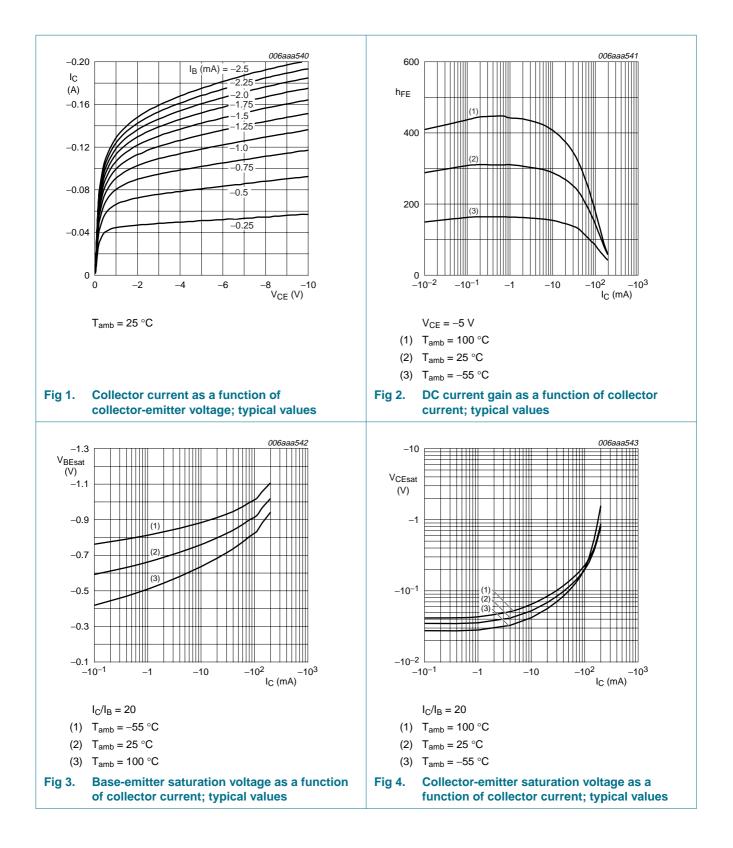
[2] V_{BE} decreases by about 2 mV/K with increasing temperature.

[3] The smaller of the two values is taken as the numerator.

[4] The smaller of the two values is subtracted from the larger value.

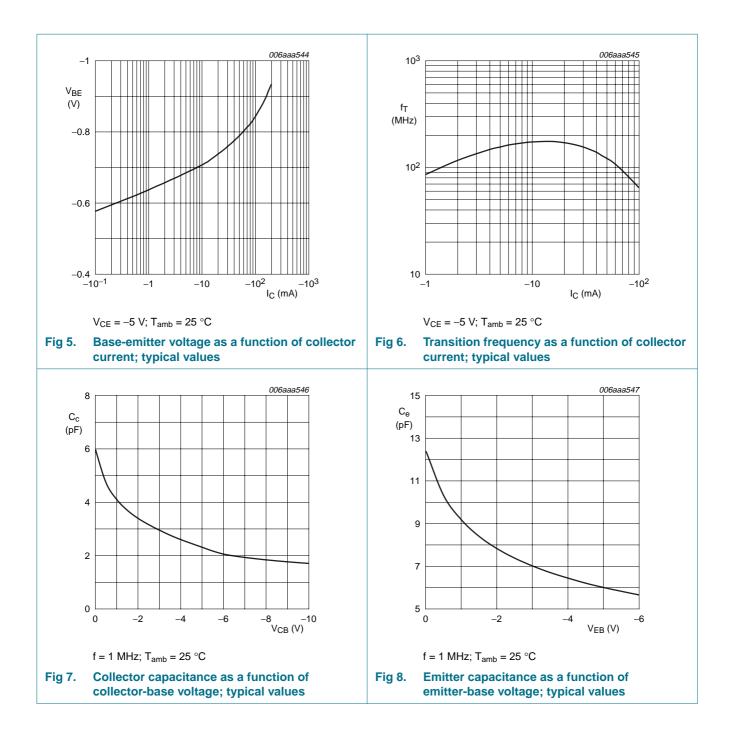
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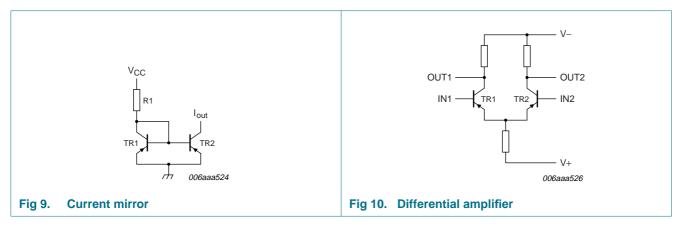
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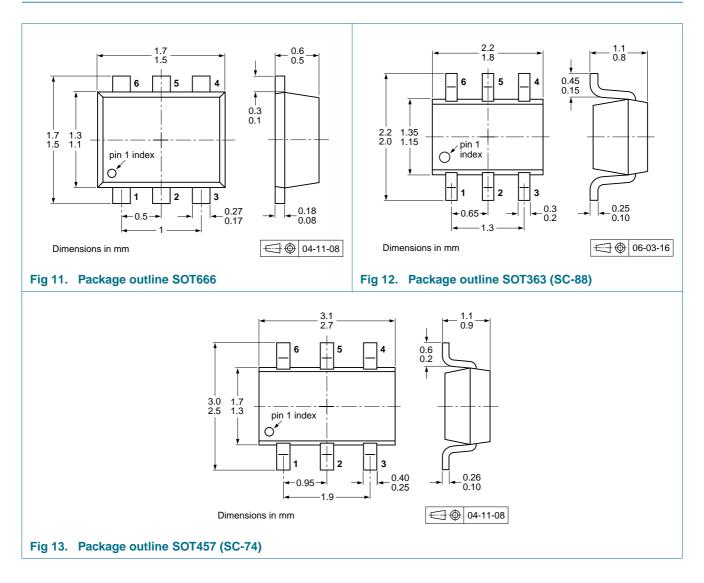


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8. Application information



9. Package outline



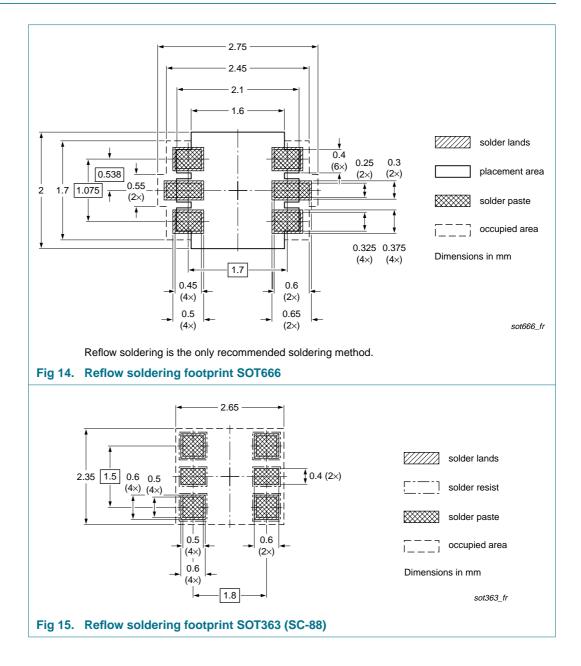
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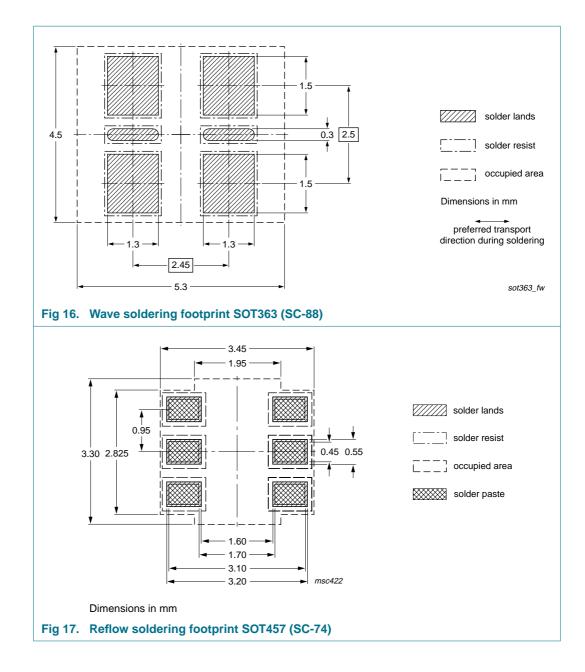
10. Packing information

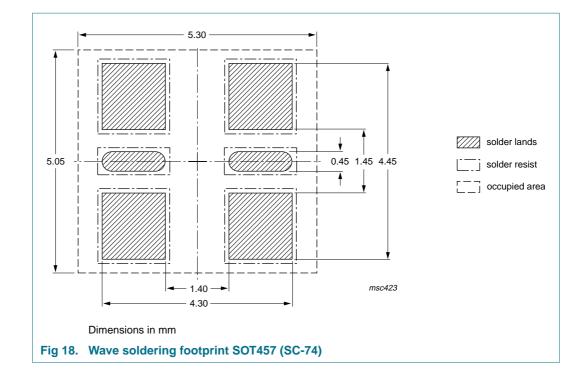
Please refer to packing information on <u>www.nexperia.com</u>.

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11. Soldering







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12. Revision history

Release date	Data sheet status	Change notice	Supersedes	
20090828	Product data sheet	-	BCM857BV_BS_DS_5	
 This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content. 				
 Figure 12 "Package outline SOT363 (SC-88)": updated 				
 Figure 14 "Reflow soldering footprint SOT666": updated 				
 Figure 15 "Reflow soldering footprint SOT363 (SC-88)": updated 				
 Figure 16 "Wave soldering footprint SOT363 (SC-88)": updated 				
Figure 18 "V	Vave soldering footprint SC	<u> </u>	d	
20060627	Product data sheet	-	BCM857BS_DS_4	
20060216	Product data sheet	-	BCM857BS_DS_3	
20060130	Product data sheet	-	BCM857BS_2	
20050411	Product data sheet	-	BCM857BS_1	
20040914	Product data sheet	-	-	
	 This data shi including ne content. Figure 12 "F Figure 14 "F Figure 15 "F Figure 16 "V Figure 18 "V 20060627 20060216 20050411 	 This data sheet was changed to reflect including new legal definitions and discontent. Figure 12 "Package outline SOT363 (Secondary Secondary Secondar	20090828 Product data sheet - • This data sheet was changed to reflect the new company namincluding new legal definitions and disclaimers. No changes we content. • Figure 12 "Package outline SOT363 (SC-88)": updated • Figure 14 "Reflow soldering footprint SOT666": updated • Figure 15 "Reflow soldering footprint SOT363 (SC-88)": updated • Figure 16 "Wave soldering footprint SOT363 (SC-88)": updated • Figure 18 "Wave soldering footprint SOT363 (SC-74)": updated • 2006027 Product data sheet • 20060130 Product data sheet • 20050411 Product data sheet	

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13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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Date of release: 28 August 2009 Document identifier: BCM857BV_BS_DS

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