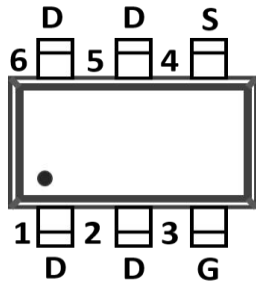
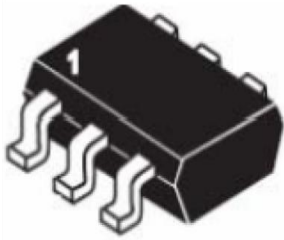
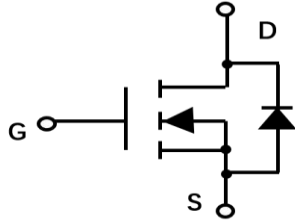


N-Channel Enhancement Mode Field Effect Transistor



SOT-23-6L



Product Summary

- V_{DS} 100V
- I_D 3A
- $R_{DS(ON)}$ (at $V_{GS}=10V$) < 120 mohm
- $R_{DS(ON)}$ (at $V_{GS}=4.5V$) < 140 mohm
- 100% UIS Tested
- 100% ∇V_{DS} Tested

General Description

- Trench Power HV MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$

Applications

- DC-DC Converters
- Power management functions

■ Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-source Voltage	V_{DS}	100	V
Gate-source Voltage	V_{GS}	± 20	V
Drain Current	I_D	$T_A=25^\circ\text{C}$	3
		$T_A=70^\circ\text{C}$	2.4
Pulsed Drain Current ^A	I_{DM}	15	A
Single Pulse Avalanche Energy	E_{AS}	8	mJ
Total Power Dissipation @ $T_A=25^\circ\text{C}$	P_D	1.5	W
Thermal Resistance Junction-to-Ambient ^B	$R_{\theta JA}$	83	$^\circ\text{C}/\text{W}$
Thermal Resistance From Junction To Lead	R_{thJL}	36	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	$^\circ\text{C}$

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJS03N10A	F2	1003	3000	/	180000	7" reel



YJS03N10A

■ Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250μA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} =0V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	1	1.8	2.5	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D =3A		95	120	mΩ
		V _{GS} = 4.5V, I _D =2.4A		100	140	
Diode Forward Voltage	V _{SD}	I _S =3A, V _{GS} =0V		0.8	1.2	V
Maximum Body-Diode Continuous Current	I _S				3	A
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, f=1MHZ		810		pF
Output Capacitance	C _{oss}			40		
Reverse Transfer Capacitance	C _{rss}			32		
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =10V, V _{DS} =80V, I _D =2.5A		19.2		nC
Gate-Source Charge	Q _{gs}			3.4		
Gate-Drain Charge	Q _{gd}			6.1		
Turn-on Delay Time	t _{D(on)}	V _{GS} =10V, V _{DD} =50V, R _L =6.4Ω R _{GEN} =3Ω		15		ns
Turn-on Rise Time	t _r			5		
Turn-off Delay Time	t _{D(off)}			30		
Turn-off fall Time	t _f			5		

A. Pulse Test: Pulse Width ≤ 300us, Duty cycle ≤ 2%.

B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.



■ Typical Performance Characteristics

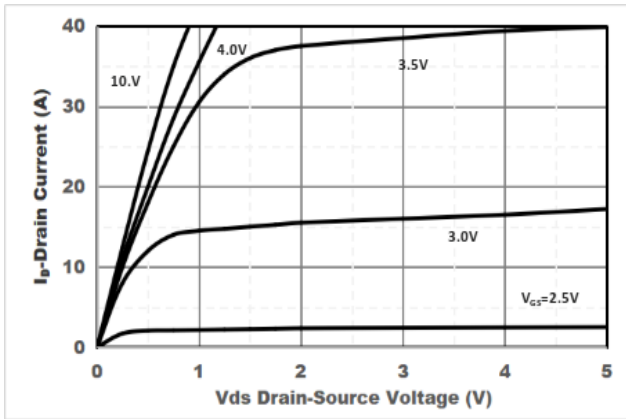


Figure1. Output Characteristics

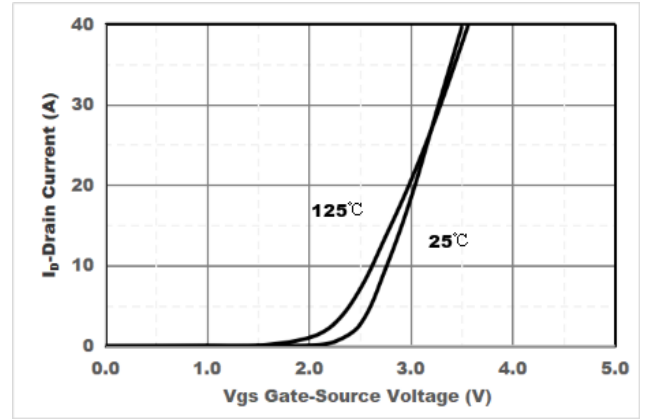


Figure2. Transfer Characteristics

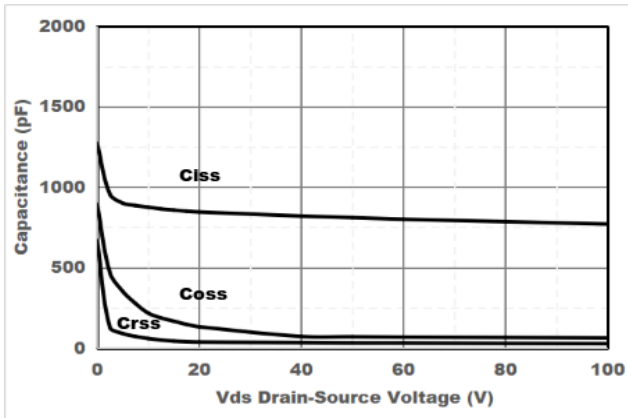


Figure3. Capacitance Characteristics

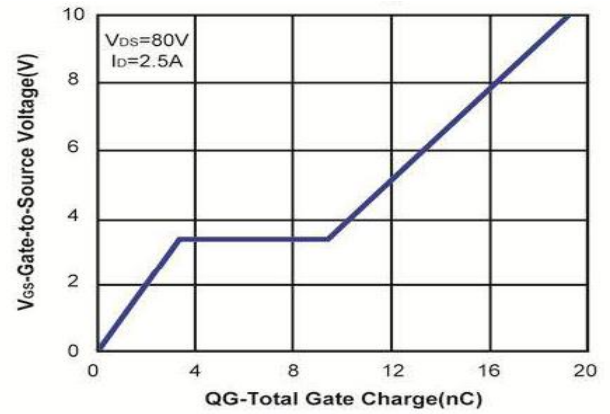


Figure4. Gate Charge

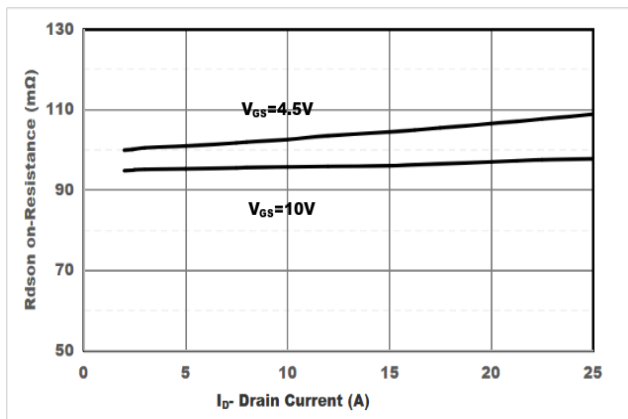


Figure5. Drain-Source on Resistance

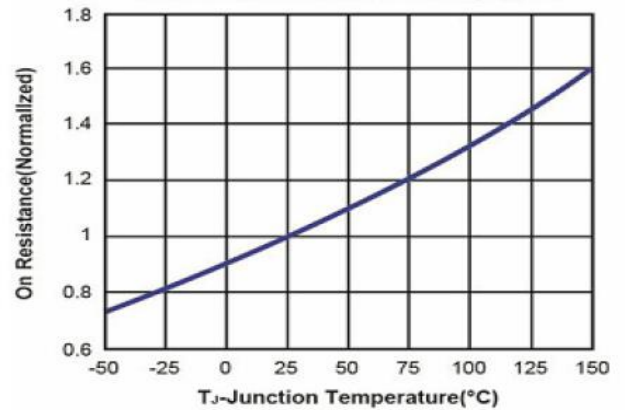


Figure6. Drain-Source on Resistance



YJS03N10A

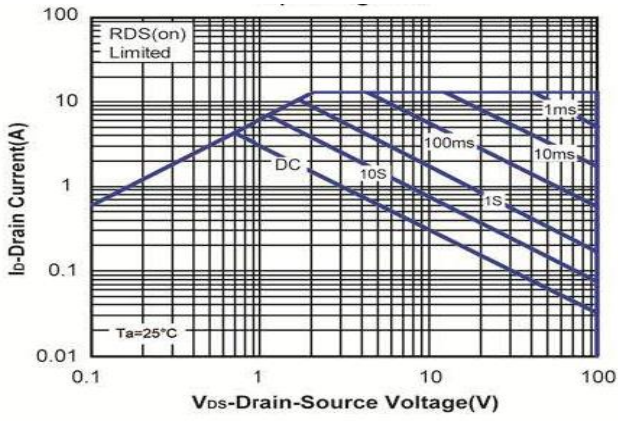


Figure7. Safe Operation Area

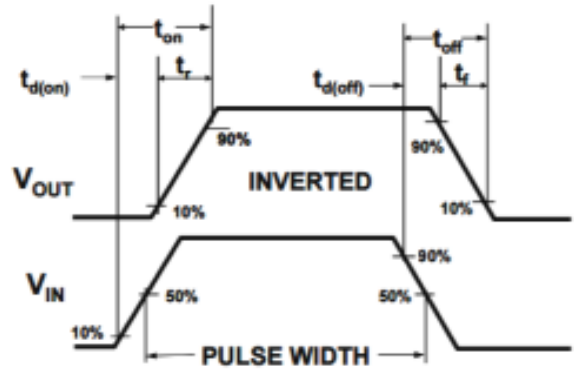
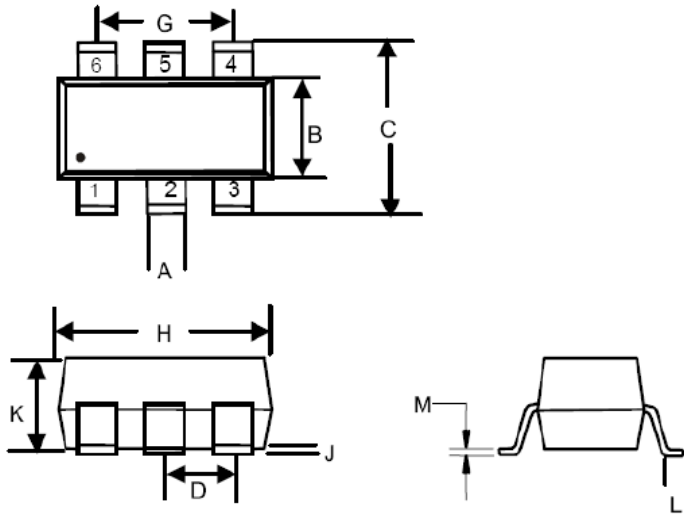


Figure8. Switching wave

■ SOT-23-6L Package information

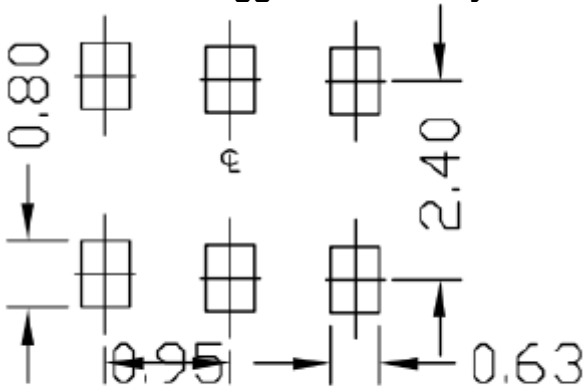


YJS03N10A



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.012	.020	0.30	0.50	
B	.051	.070	1.30	1.80	
C	.087	.126	2.20	3.20	
D	.037		0.95BSC		
G	.074		1.90BSC		
H	.106	.122	2.70	3.10	
J	.002	.006	0.05	0.15	
K	.035	.051	0.90	1.30	
L	.012	.024	0.30	0.60	
M	.003	.008	0.08	0.22	

■ SOT-23-6L Suggested Pad Layout



UNIT: mm



YJS03N10A

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