DISCONTINUED PLEASE USE ZVN4424A

N-CHANNEL ENHANCEMENT MODE VERTICAL DMOS FET

ZVN4424A/C

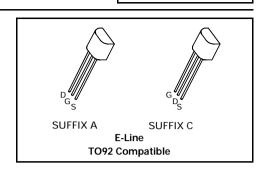
ISSUE 3 - August 1994

FEATURES

- Compact E-LINE (TO92 style) package
- * 240 Volt BV_{DS}
- * $R_{DS(on)}$ =4.3 Ω Typical at V_{GS} =2.5V
- Low threshold
- Fast switching

APPLICATIONS

- * Earth recall and dialling switches
- * Electronic hook switches
- * Battery powered equipment
- * Telecoms and high voltage dc-dc converters



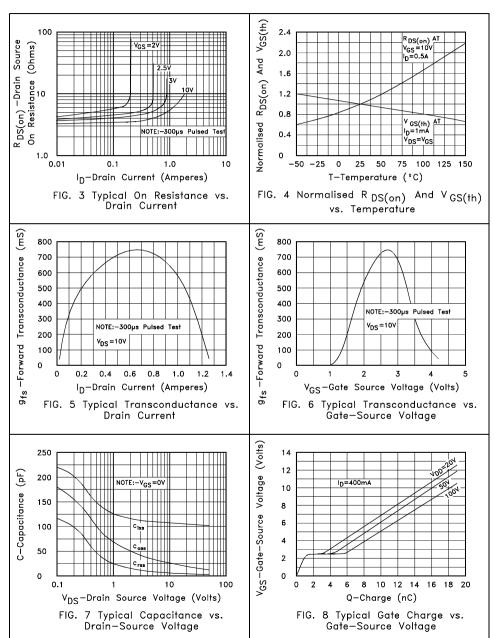
ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	VALUE	UNIT
Drain-Source Voltage	V _{DS}	240	V
Continuous Drain Current at T _{amb} =25°C	I _D	260	mA
Pulsed Drain Current	I _{DM}	1.5	А
Gate Source Voltage	V _{GS}	± 40	V
Power Dissipation at T _{amb} =25°C	P _{tot}	750	mW
Operating and Storage Temperature Range	T _j :T _{stg}	-55 to +150	°C



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TYPICAL CHARACTERISTICS



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ELECTRICAL CHARACTERISTICS (at T_{amb} = 25°C unless otherwise stated).

PARAMETER	SYMBOL	MIN.	TYP	MAX.	UNIT	CONDITIONS.	
Drain-Source Breakdown Voltage	BV _{DSS}	240			V	I _D =1mA, V _{GS} =0V	
Gate-Source Threshold Voltage	$V_{GS(th)}$	8.0	1.3	1.8	V	$I_D=1mA$, $V_{DS}=V_{GS}$	
Gate-Body Leakage	I _{GSS}			100	nA	V _{GS} =± 40V, V _{DS} =0V	
Zero Gate Voltage Drain Current	I _{DSS}			10 100	μ Α μ Α	V _{DS} =240 V, V _{GS} =0 V _{DS} =190 V, V _{GS} =0V, T=125°C	
On-State Drain Current	I _{D(on)}	0.8	1.4		Α	V _{DS} =10 V, V _{GS} =10V	
Static Drain-Source On-State Resistance	R _{DS(on)}		4 4.3	5.5 6	Ω Ω	V _{GS} =10V,I _D =500mA V _{GS} =2.5V,I _D =100mA	
Forward Transconductance (1) (2)	g _{fs}	0.4	0.75		S	V _{DS} =10V,I _D =0.5A	
Input Capacitance (2)	C _{iss}		110	200	pF		
Common Source Output Capacitance (2)	C _{oss}		15	25	pF	V _{DS} =25V, V _{GS} =0V, f=1MHz	
Reverse Transfer Capacitance (2)	C _{rss}		3.5	15	pF		
Turn-On Delay Time (2)(3)	t _{d(on)}		2.5	5	ns		
Rise Time (2)(3)	t _r		5	8	ns	V _{DD} ≈50V, I _D =0.25A, V _{GEN} =10V	
Turn-Off Delay Time (2)(3)	t _{d(off)}		40	60	ns		
Fall Time (2)(3)	t _f		16	25	ns		

- (1)*Measured under pulsed conditions. Pulse width=300 μ s. Duty cycle \leq 2% (2)Sample Test
- (3) Switching times measured with 50Ω source impedance and >5ns rise time on pulse generator

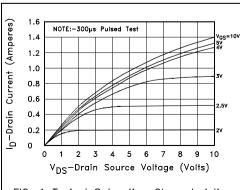


FIG. 1 Typical Saturation Characteristics

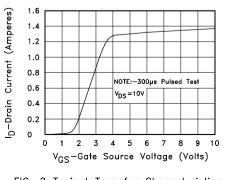
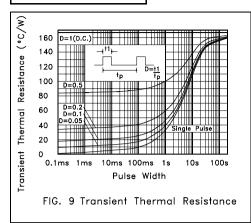
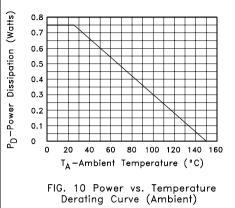


FIG. 2 Typical Transfer Characteristics

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SPICE PARAMETERS

*ZVN4424 MODEL LAST REVISION 1/94

*

.SUBCKT ZVN4424 30 40 50

* NODES: DRAIN GATE SOURCE

M1 30 20 50 50 MOD1 L=1 W=1

RG 40 20 200

RL 30 50 240E6

D1 50 30 DIODE1

.MODEL MOD1 NMOS VT0=1.25 RS=2.34 RD=1.634 IS=1E-15 KP=5.319

+CGS0=101P CGD0=4P CBD=66.2P PB=1

.MODEL DIODE1 D IS=5.516E-13 RS=0.2084 N=1.0078

.ENDS ZVN4424

For clarification of the above or for technical enquires generally please contact the Applications Dept. at Zetex plc.

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